

METHOD FOR REFRESHING ROW HAMMER, CIRCUIT FOR REFRESHING ROW HAMMER AND SEMICONDUCTOR MEMORY

ABSTRACT

Embodiments of the present disclosure provide a method for refreshing row hammer, a circuit for refreshing row hammer and a semiconductor memory. The method includes the following operations. A row hammer refresh instruction for a target word line is determined. According to the row hammer refresh instruction, a preset row hammer refresh signal is set to a valid state. The valid state of the preset row hammer refresh signal indicates that the row hammer refresh instruction is performed in a first refresh period. In response to detecting that the row hammer refresh instruction is not completed within the first refresh period, the valid state of the preset row hammer refresh signal will be continued to a next refresh period of the first refresh period.

[Fig. 4]