ABSTRACT

APPARATUS AND METHOD FOR PREFETCHING DATA TO LOAD BUFFERS IN A BRIDGE BETWEEN TWO BUSES IN A COMPUTER

A bridge circuit adapted to be associated with first and second bus circuits which bridge circuit includes a first path including a plurality of buffers for storing data being read from the first bus to the second bus, a circuit arrangement for reading an amount of data sufficient to fill the storage space in a first line buffer beginning at an address being read, and apparatus for completely filling a next line buffer with data at sequential addresses following the addresses of data placed in the first line buffer if read operations from the first line buffer occur in sequence.

(Figure 4)