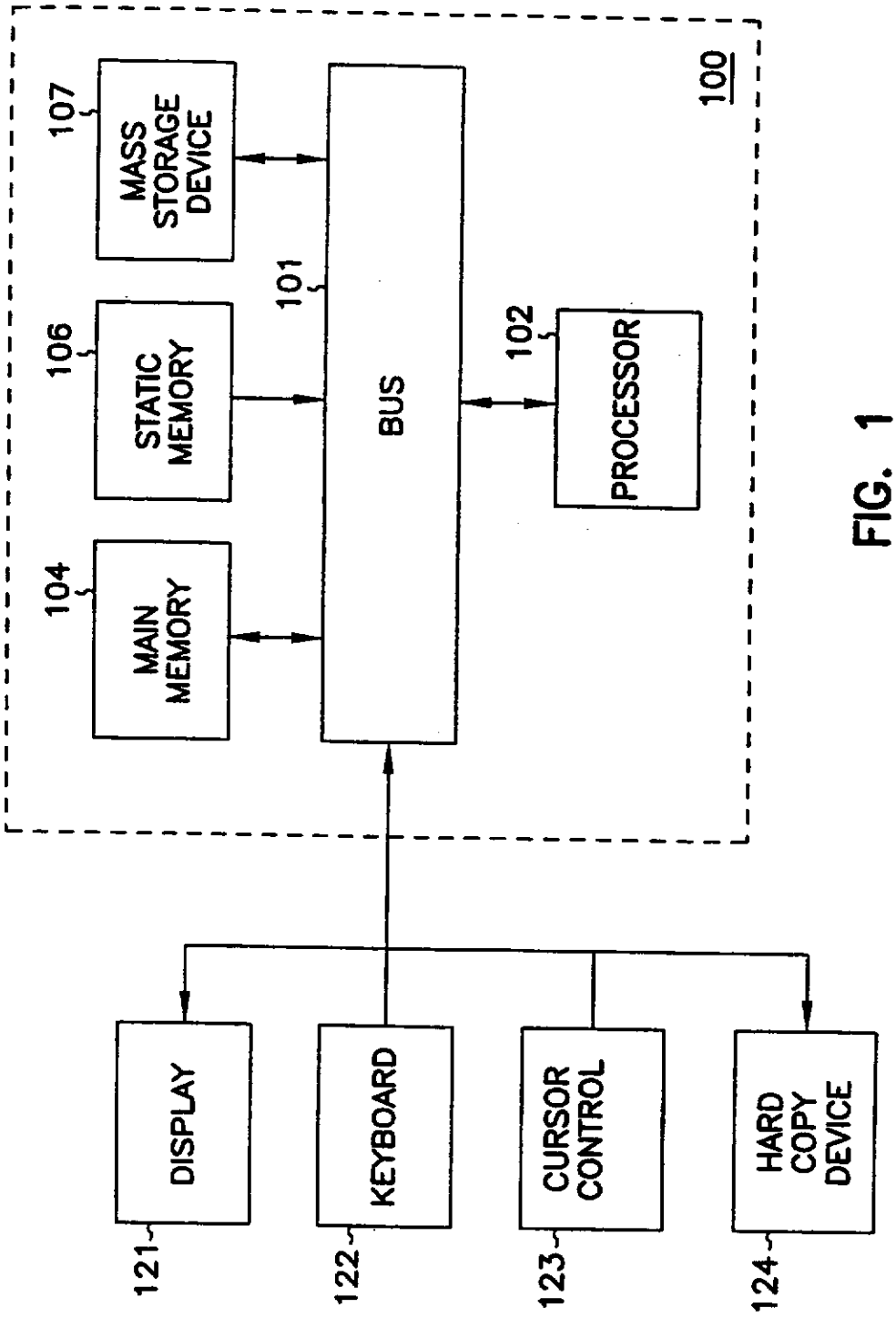

100
107
MASS STORAGE DEVICE
106
STATIC MEMORY
104
MAIN MEMORY
101
BUS
102
PROCESSOR
121
DISPLAY
122
KEYBOARD
123
CURSOR CONTROL
124
HARD COPY DEVICE

FIG. 1

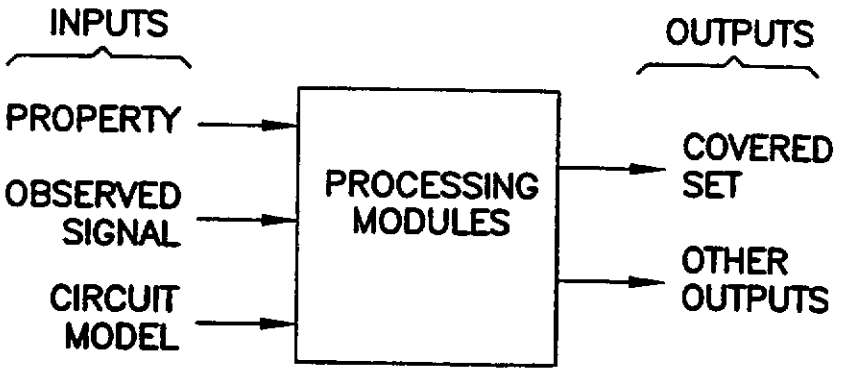

INPUTS
OUTPUTS
PROPERTY
OBSERVED SIGNAL
CIRCUIT MODEL
PROCESSING MODULES
COVERED SET
OTHER OUTPUTS

FIG. 2

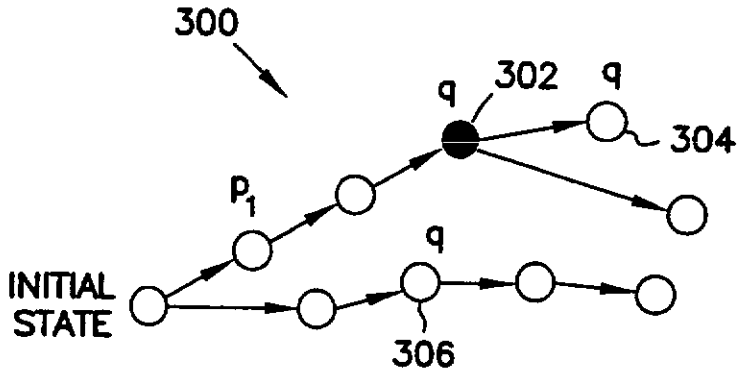


FIG. 3

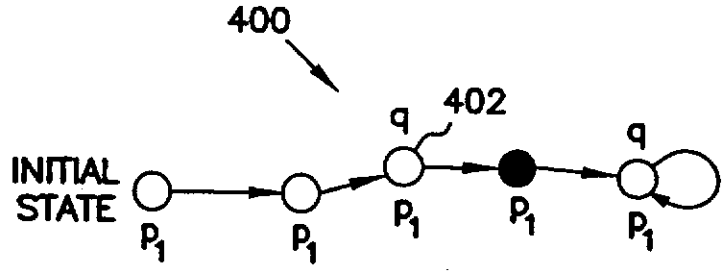


FIG. 4

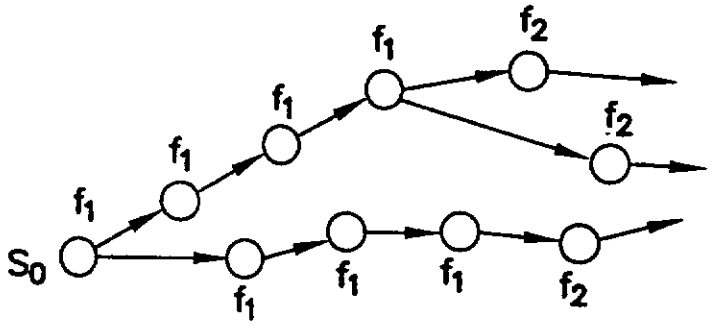


FIG. 5