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(71) Applicant (for all designated States except US):
RAYTHEON COMPANY [US/US]; 870 Winter Street,
Waltham, Massachusetts 02451 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **HESTON, David, D.** [US/US]; 9418 Hillview, Dallas, Texas 75231 (US).
HESTON, John, G. [US/US]; 226 Jason Cir., Murphy,
Texas 75094 (US). **MIDDLEBROOK, Thomas, L., III**
[US/US]; 3509 Bridgewater Drive, Rowlett, Texas 75088
(US).

(74) Agent: **WILLIAMS, Bradley**; BAKER BOTTS L.L.P.,
2001 Ross Avenue, Suite 600, Dallas, Texas 75201 (US).

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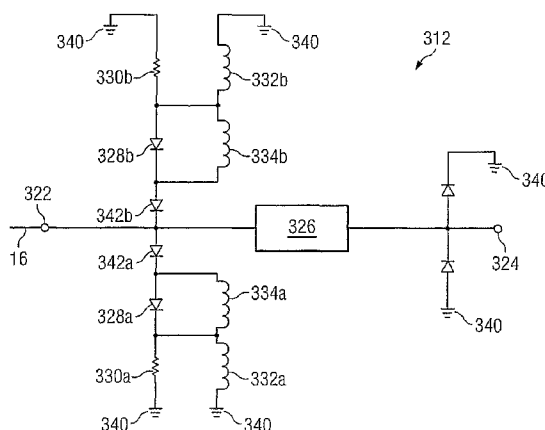
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Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))

[Continued on next page]

(54) Title: POWER ABSORBER SYSTEM AND METHOD



(57) Abstract: According to one embodiment of the invention, a power absorber for receiving a signal having a fundamental frequency includes an input node for receiving the signal and a first PIN diode circuit having a first end electrically connected at the input node and a second end. The first PIN diode circuit includes at least one PIN diode. The absorber also includes a load resistance having a first end electrically connected to the second end of the PIN diode circuit and a second end electrically connected to a reference voltage. The absorber also includes a quarter wave transmission line having a first end electrically connected to the input node at a second end. The quarter wave transmission line has an electrical link that is one quarter of the wavelength of the fundamental frequency. The power absorber also includes a second PIN diode circuit having an input electrically connected to the second end of the quarter wave transmission line and an output electrically connected to the reference voltage. The first choke is provided in parallel with the load resistor.

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