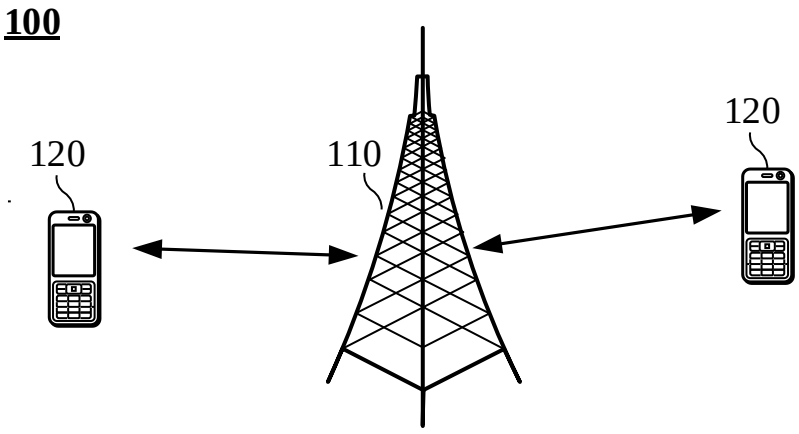

FIG. 1

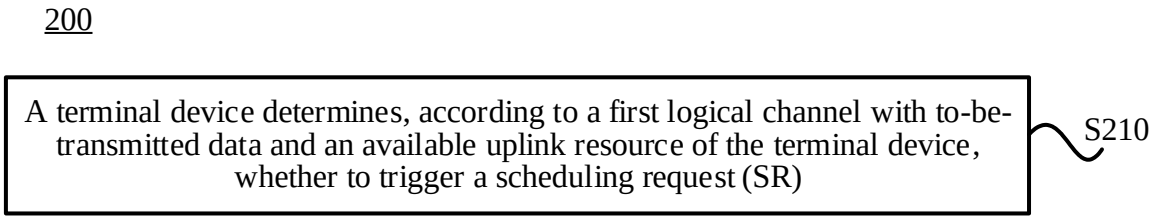

FIG. 2

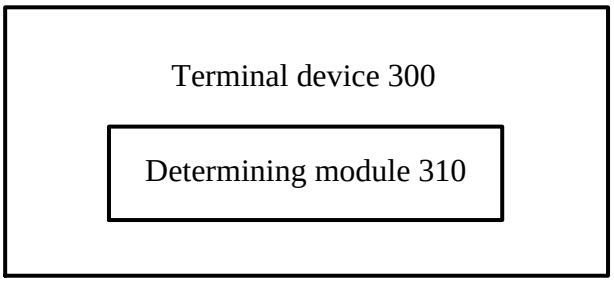

FIG. 3

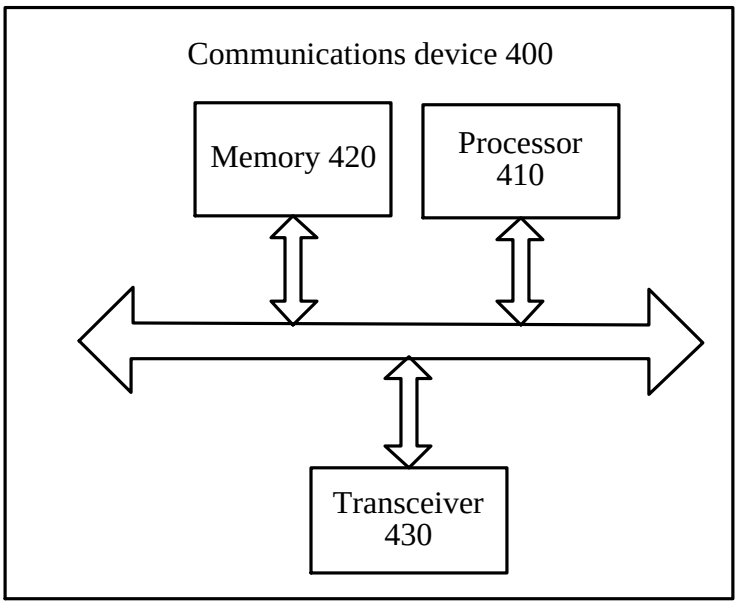
Communications device 400
Memory 420
Processor 410
Transceiver 430

FIG. 4

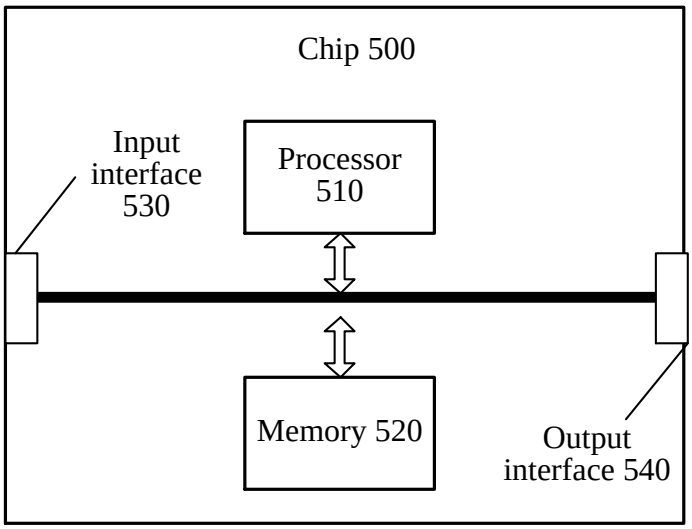
Chip 500
Input interface 530
Processor 510
Memory 520
Output interface 540

FIG. 5