

Abstract

Chip card with memory for programs which can be reloaded as a function of the application

The chip card (CK) according to the invention has a program execution unit (P) and a memory (S) for at least one program (P1..Pn) which can be reloaded as a function of the application, in every reloadable program (P1..Pn) first command instructions (OP11..OP1z) with absolute address parameters (adr11..adr1z) which refer to memory areas (S1) and which are not occupied by the reloadable program (P1..Pn) being separated from second command instructions (OP21..OP2x) with absolute address parameters (adr21..adr2x) which refer to the memory area (B1..Bn) which is occupied by the reloadable program (P1..Pn). It is advantageously possible for the reloadable program to be reloaded as a function of the application at any desired start address (SA1..SAn) in the memory (S) and to be adapted to said start address (SA1..SAn) by the program execution unit (P). In particular, the need to create different reloadable program versions for possible application-related start addresses is advantageously dispensed with.

FIG. 2