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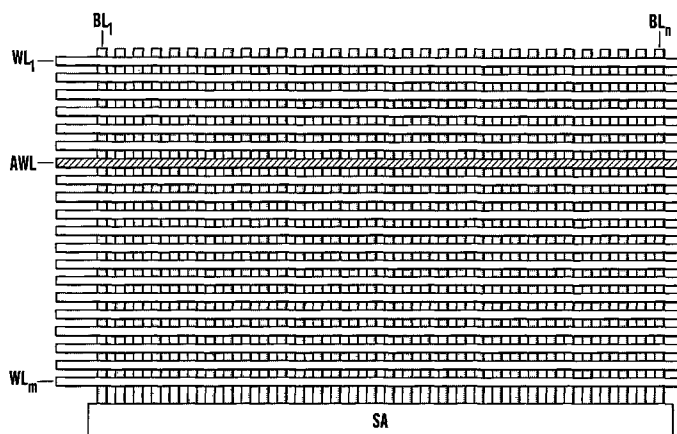
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(54) Title: A METHOD FOR READING A PASSIVE MATRIX-ADDRESSABLE DEVICE AND A DEVICE FOR PERFORMING THE METHOD



(57) Abstract: In a method for reading of a passive matrix-addressable device, particularly a memory device or a sensor device with individually addressable cells of a polarizable material, the cells store data in the form of one of two polarization states +P_i;-P_i in each cell, and the polarization states in the cells are written and read by addressing via electrodes which form word and bit lines (WL;BL) in an orthogonal electrode matrix, and wherein the cells are provided in or at the crossings between the word and bit lines (WL;BL) a voltage pulse protocol is used according to which electric potentials on all word and bit lines are controlled coordinated in time. During reading a word line (WL) is activated by applying voltage which relative to the potential on all crossing bit lines (BL) corresponds to the voltage V_S and data stored in the cells connected to this active word line (AWL) are determined by detecting the charge values of the cells in a detection means (SA). In a device for performing the method electric potentialson all word and bit lines (WL;BL) are controlled coordinated in time by therewith connected control means which implements the voltage pulse protocol. - Use in passive matrix-addressable memory and sensor devices.



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