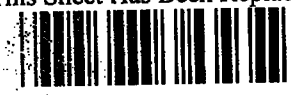
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ABSTRACT

EXPANDED MODE MICROCONTROLLER

In the expanded mode of operation, a microprocesssor (100) is coupled to external address (1) and data (4) buses and other lines, such as clock (3) and read/write (2), which are required to allow access to external memories, registeers and other peripherals. The microcontroller (100) includes a selector (8) to select whether an internal or an external resource is to be accessed, and a controller (5) for controlling the external lines or buses according to whether the resource to be accessed is internal or external by freezing them if they are not to be used, so as to save power.