ABSTRACT

VOLTAGE SUPPLY NOISE ANALYSIS

Systems and methods for implementing voltage supply noise analysis for electronic circuits (100) are disclosed. In an exemplary embodiment a computer program product executes a computer process. The computer process generates at least one spatial profile (300) for the electronic circuit, generates at least one temporal profile (400) for the electronic circuit, merges the at least one temporal profile and the at least one spatial profile, and determines if the electronic circuit is operating within acceptable voltage noise margins based on the merged temporal and spatial profiles.

(Fig. 2)