

ABSTRACT

According to one embodiment, a memory system comprises a plurality of memory packages, an on-die termination (ODT) circuit, and a controller. The plurality of memory packages
5 are coupled by a common bus and arranged in groups, and each memory package includes a plurality of memory chips. The ODT circuit is disposed in all the memory packages. The controller maintains an ODT start condition for turning on the ODT circuit set by a 2-bit information signal defined
10 by a periodic signal of at least two cycles, and turns on the ODT circuit when an asserted state of a chip enable signal CEn acquired using the periodic signal is included in a pattern of the ODT start condition.