

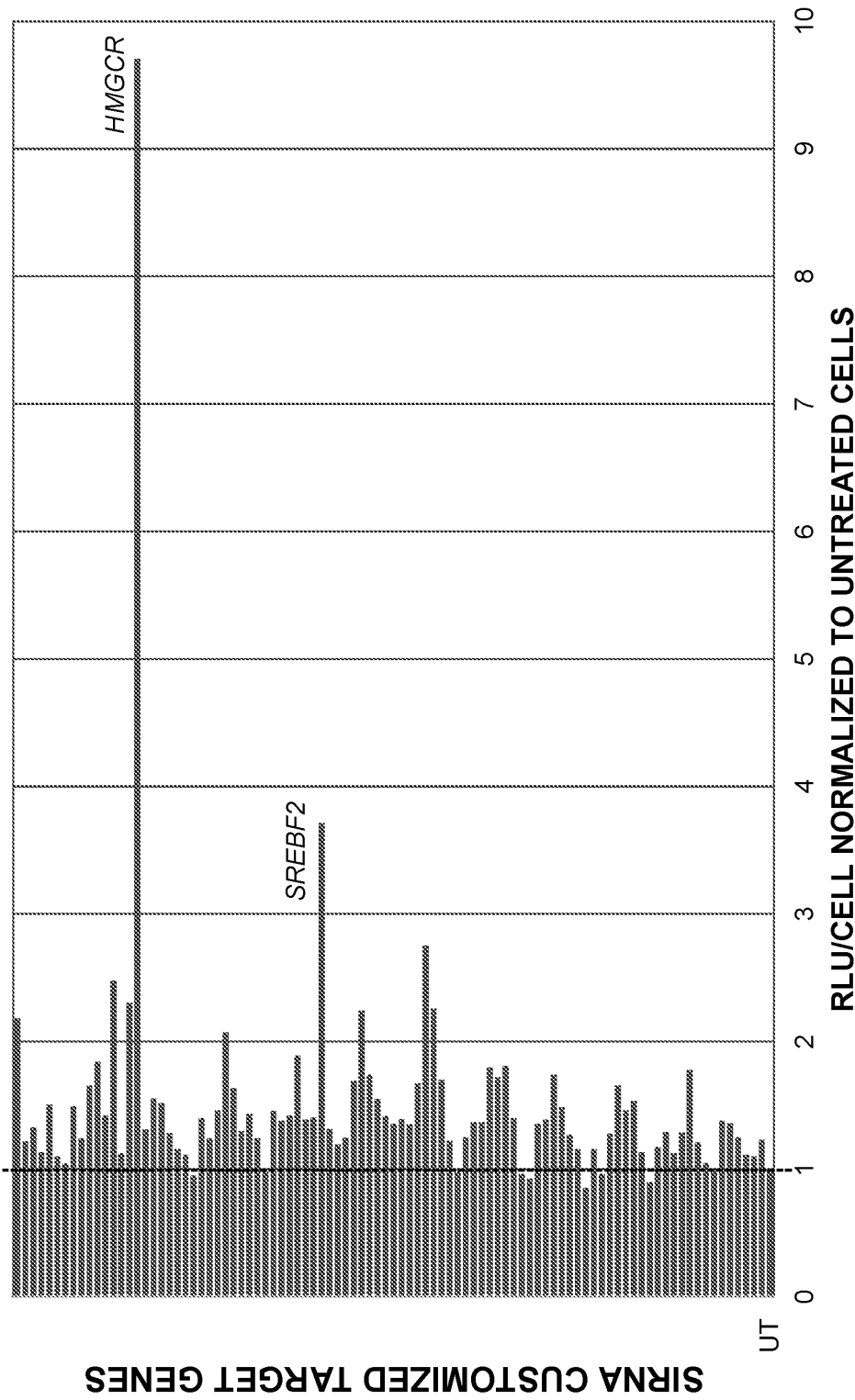


FIG. 1

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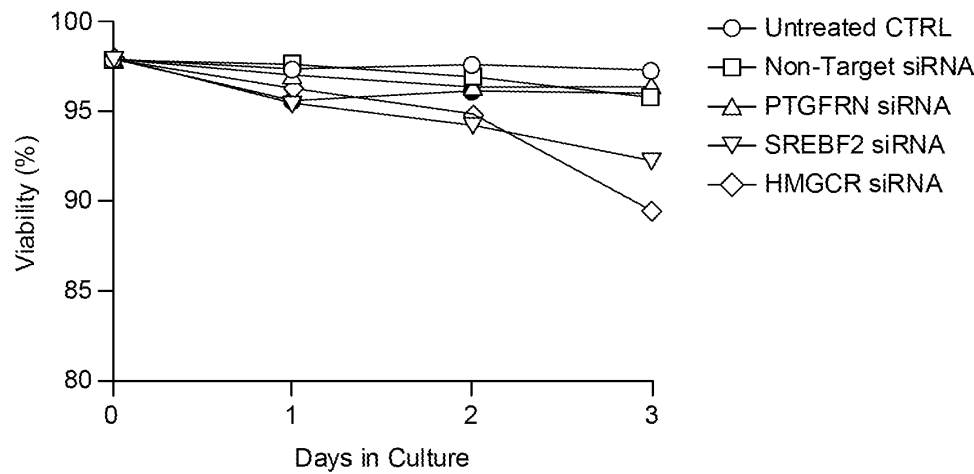


FIG. 2A

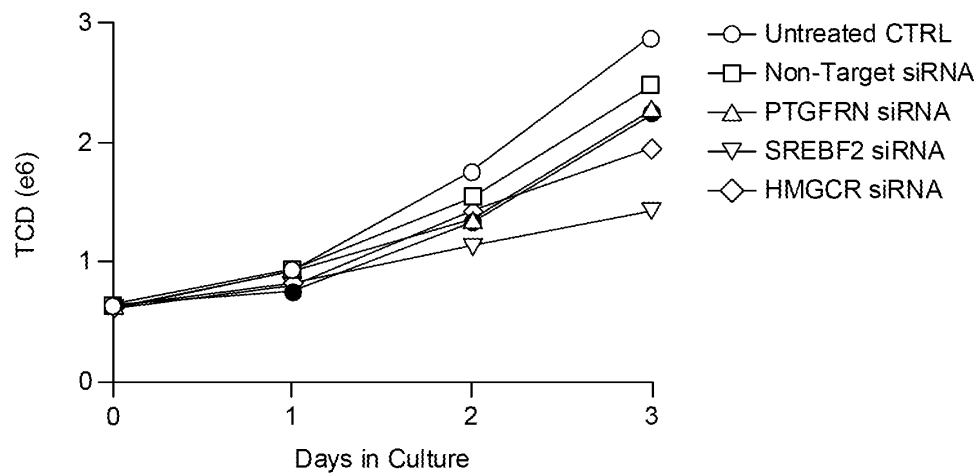


FIG. 2B



FIG. 2C

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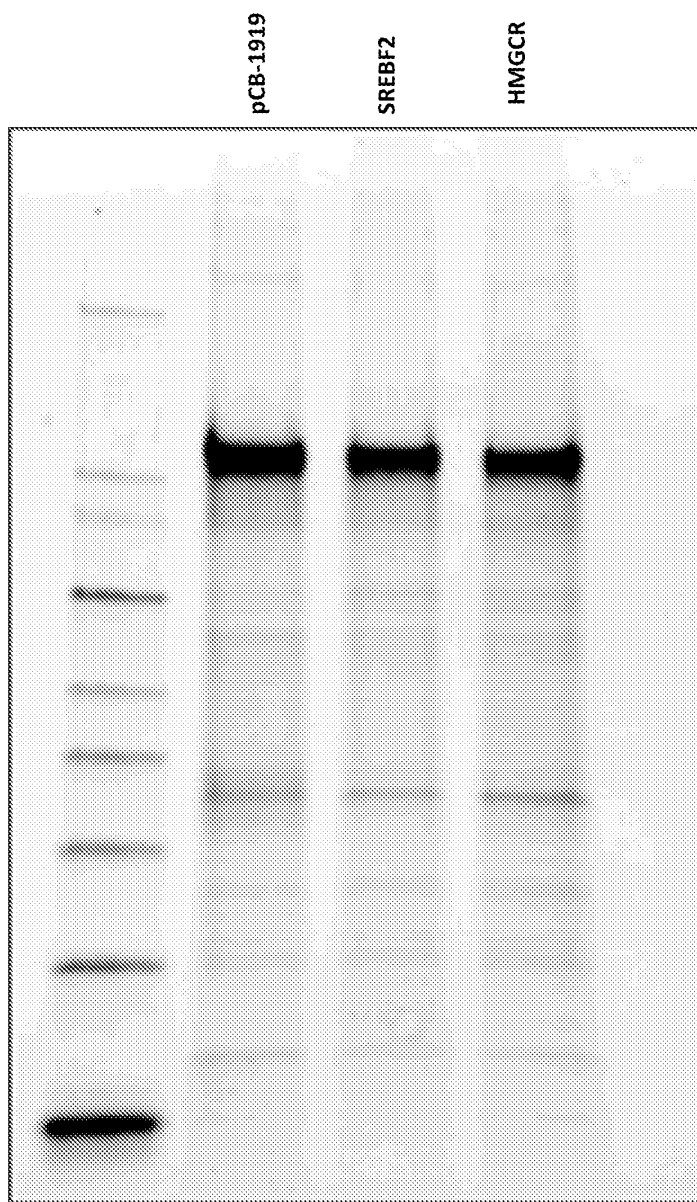


FIG. 2D

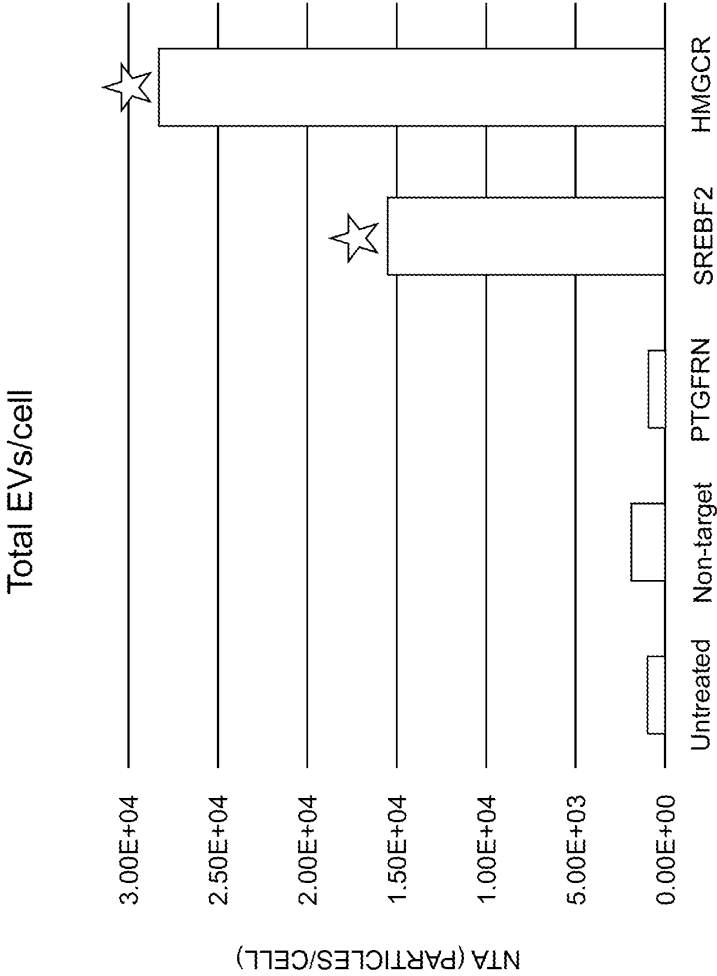


FIG. 2E

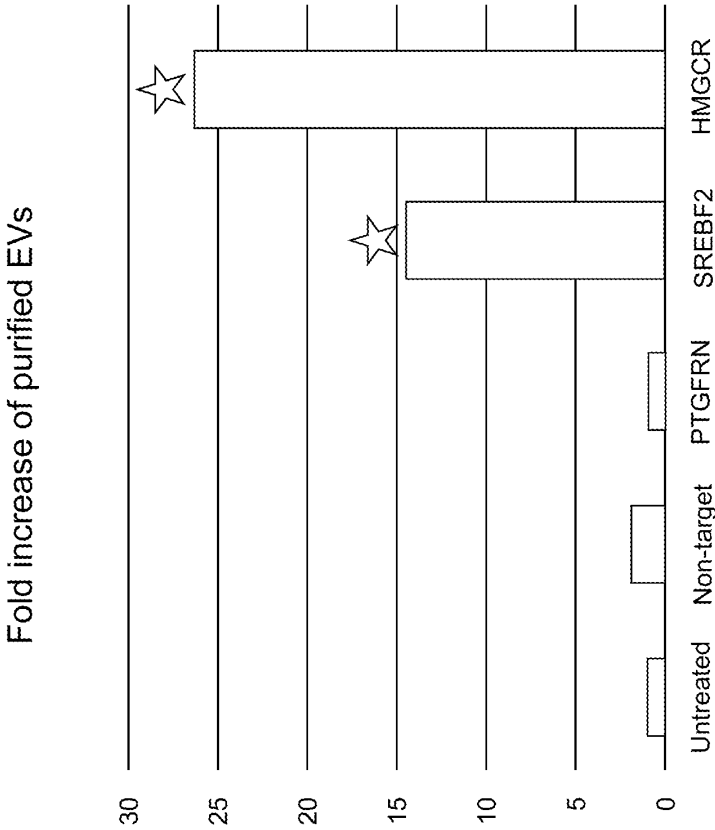


FIG. 2F

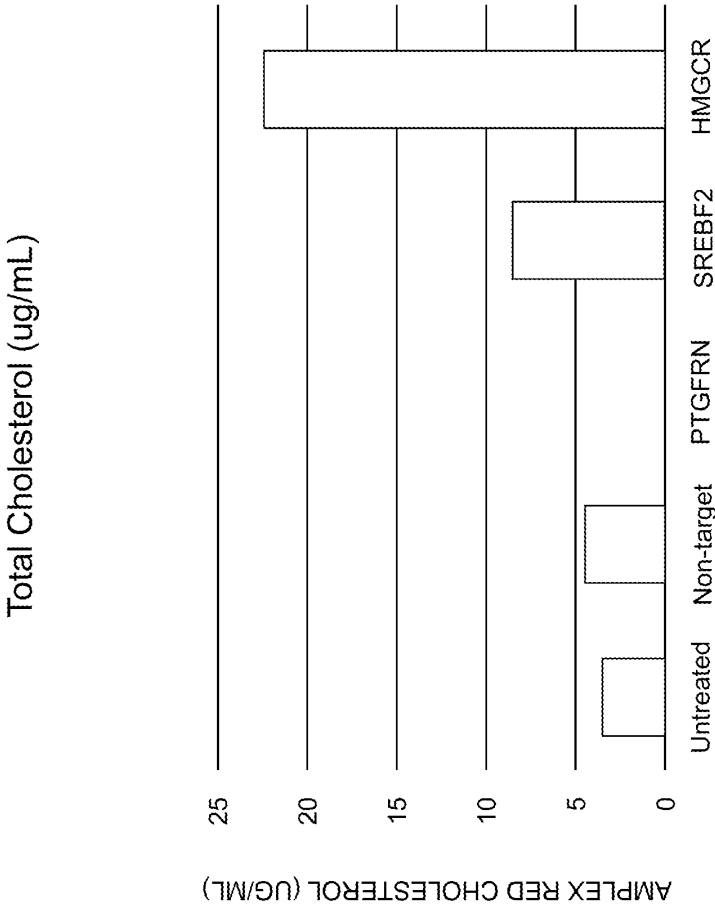


FIG. 2G

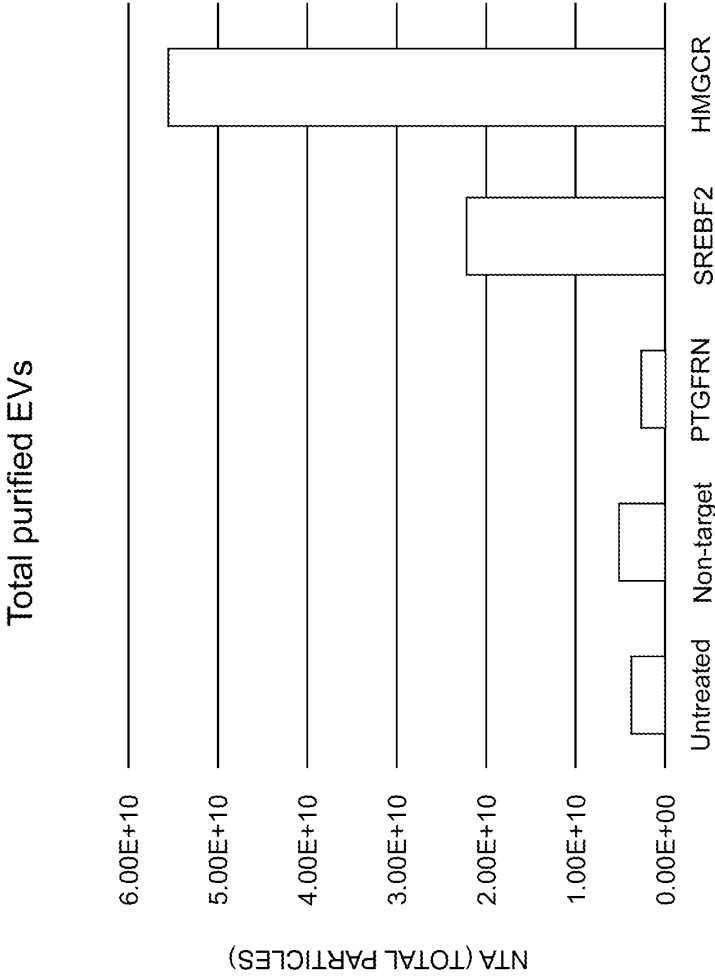


FIG. 2H

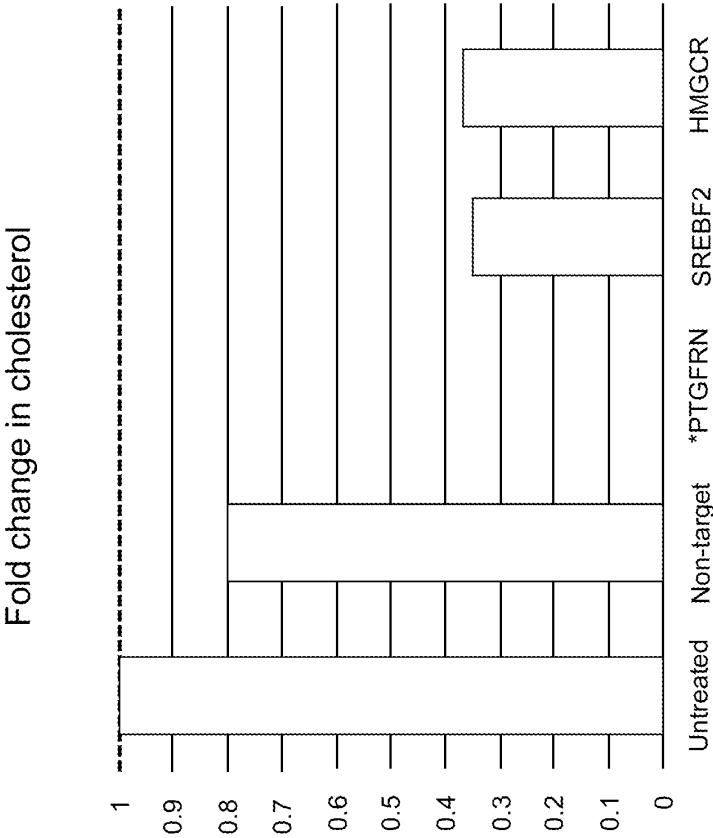


FIG. 2I

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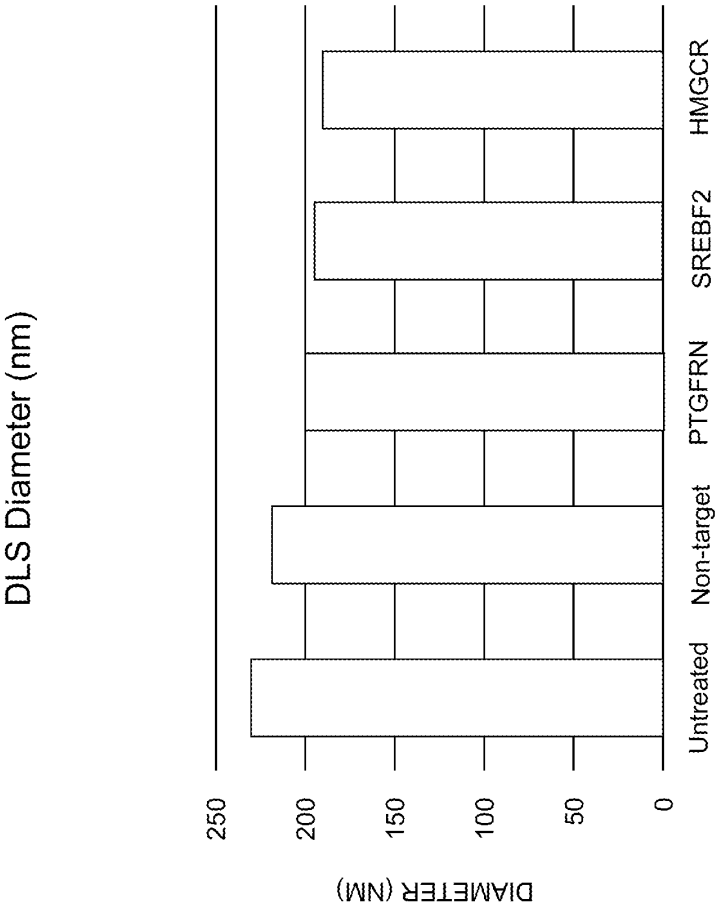


FIG. 2J

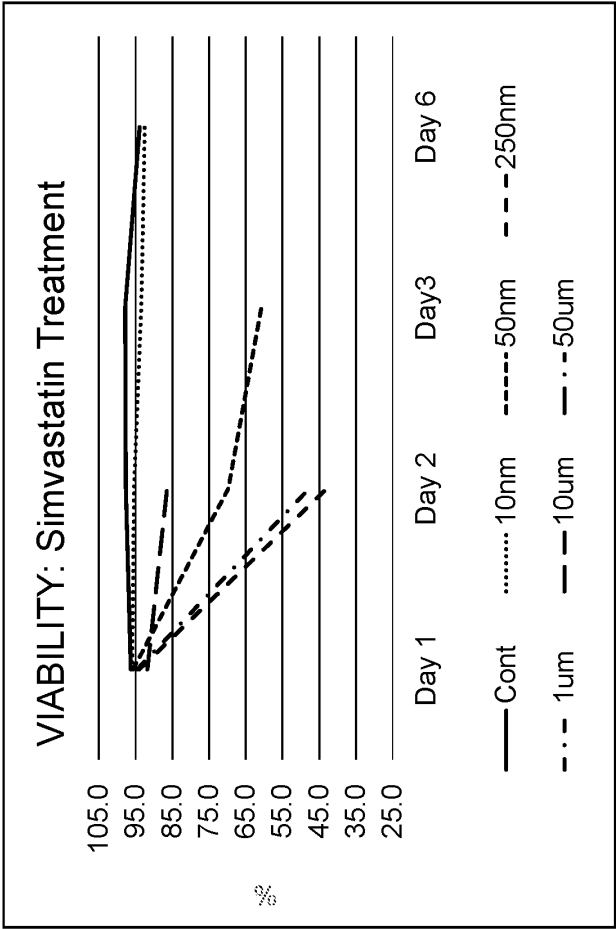


FIG. 3A

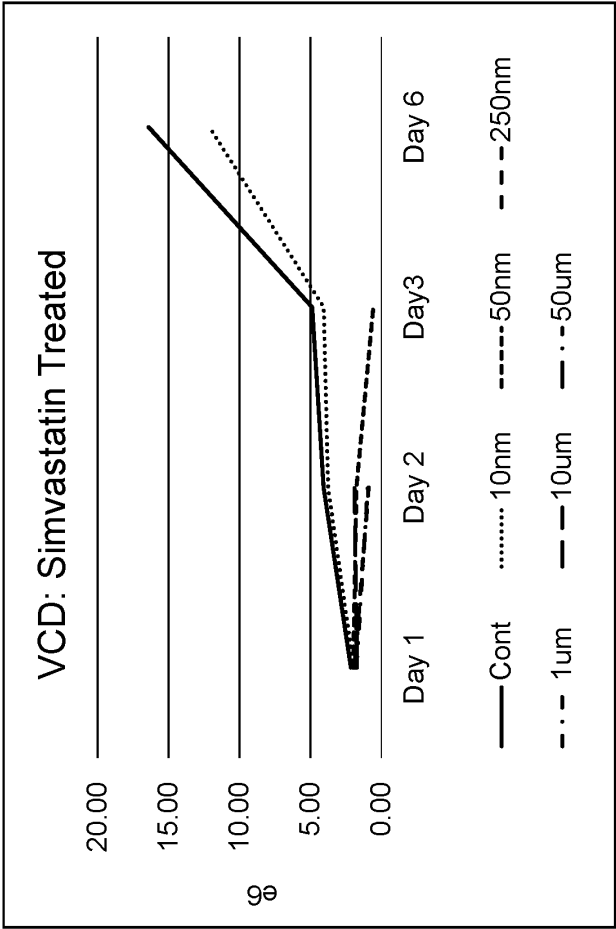


FIG. 3B

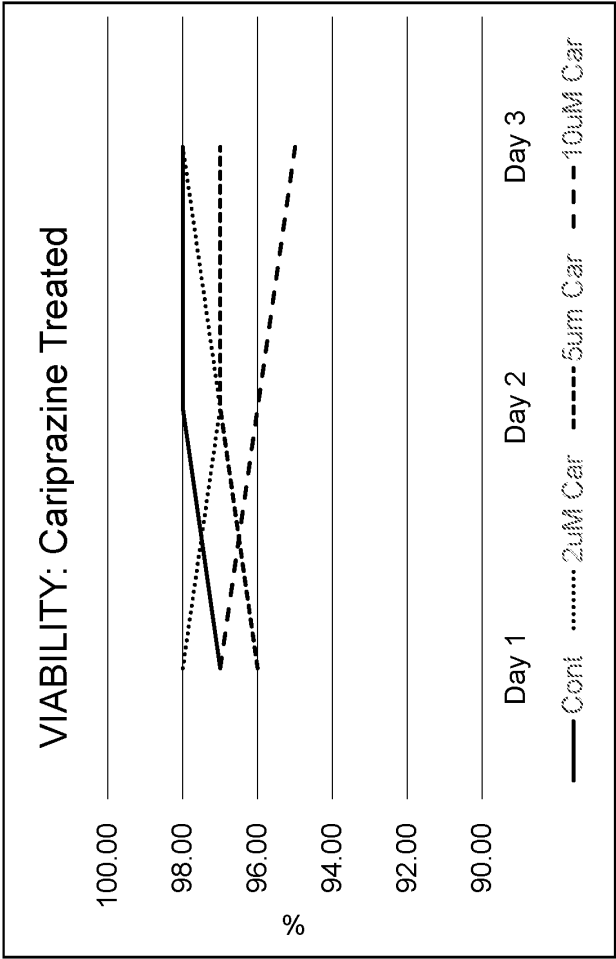


FIG. 3C

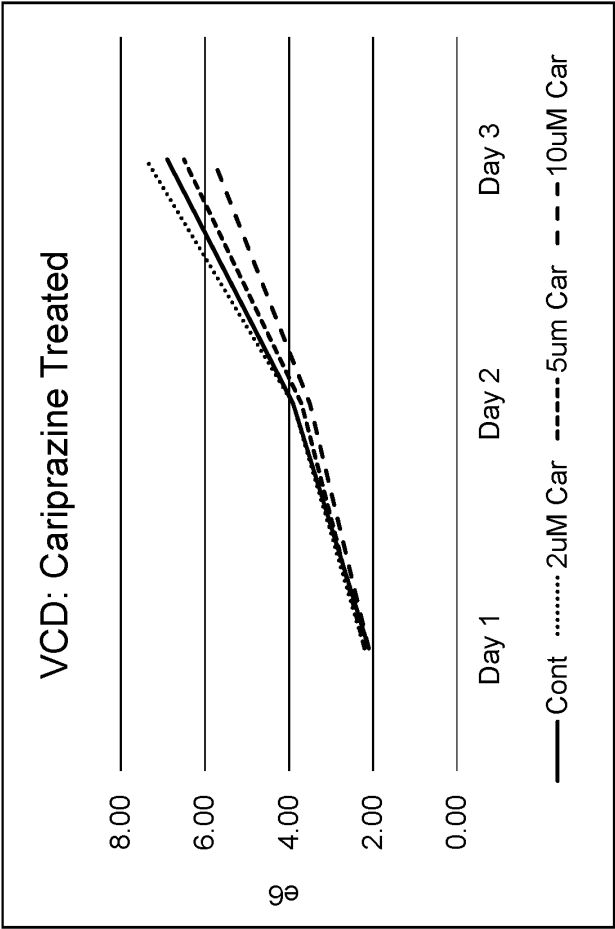


FIG. 3D

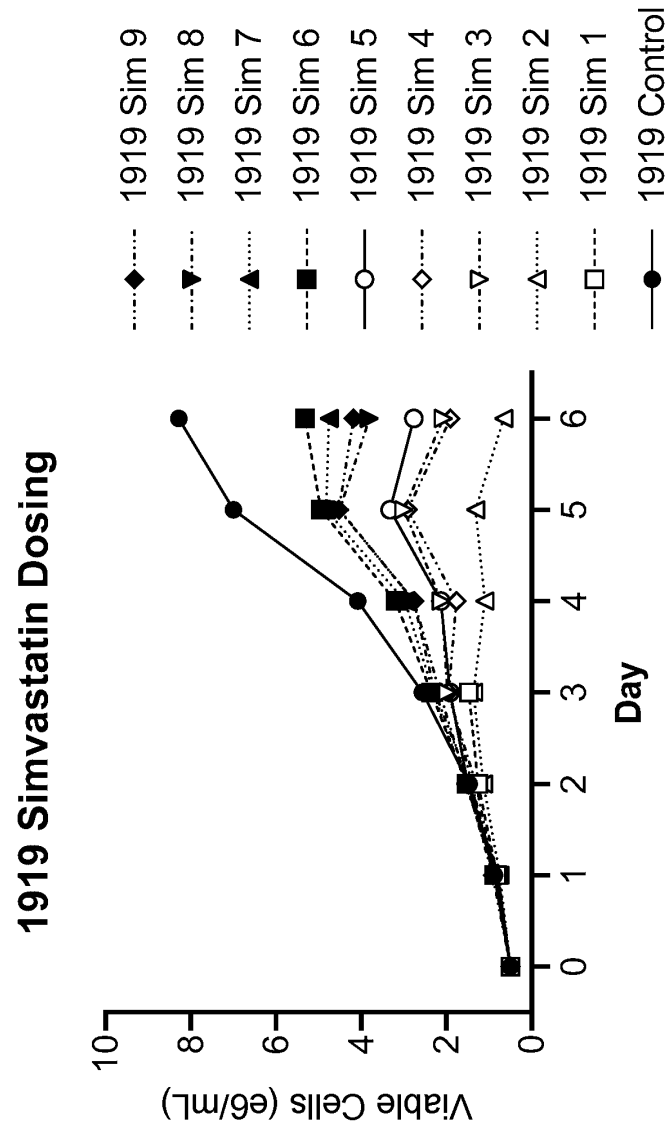


FIG. 4A

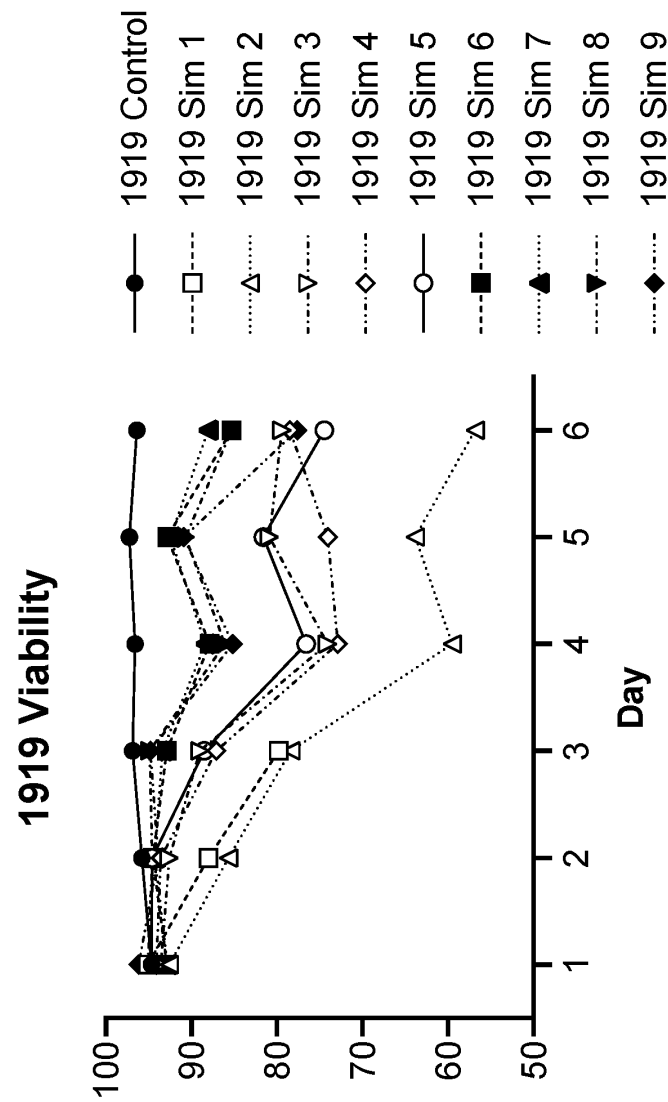


FIG. 4B

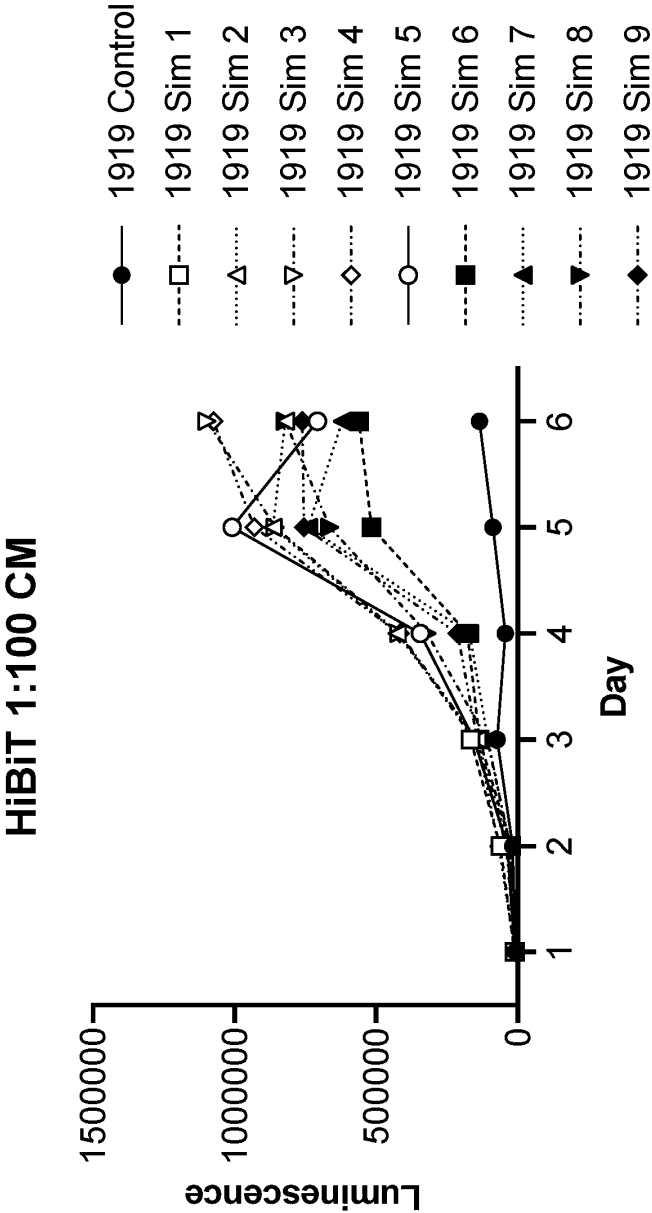


FIG. 4C

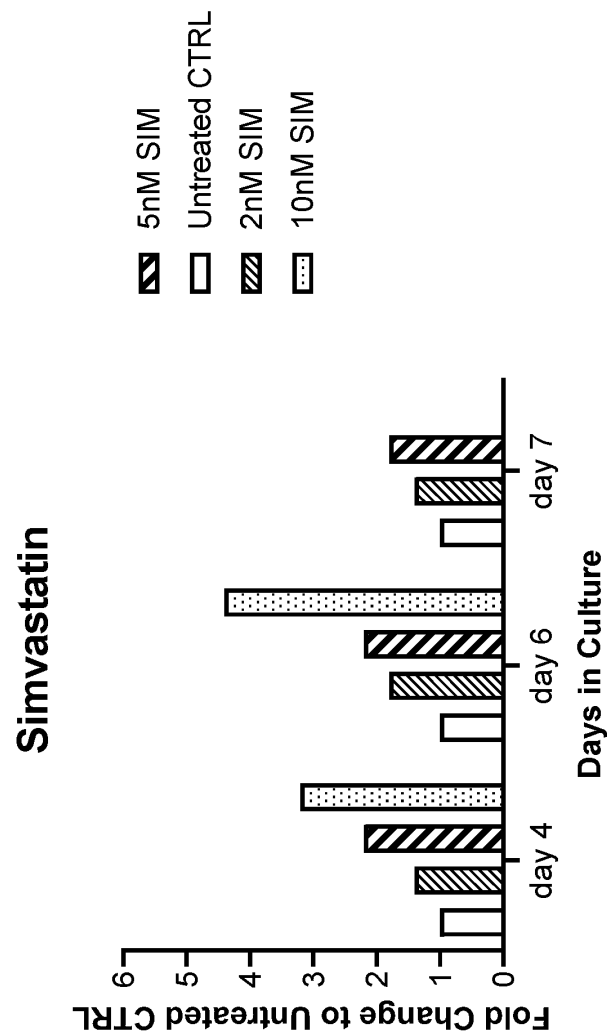


FIG. 4D

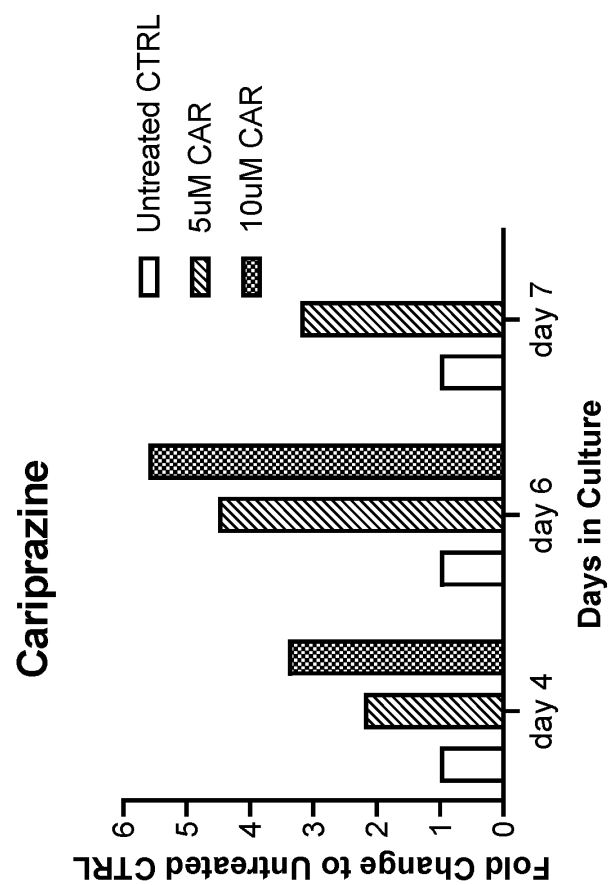


FIG. 4E

PrX PrX IL12 IL12 IL12 IL12

Untreated 5nM SIM Untreated 5nM SIM 10nM SIM

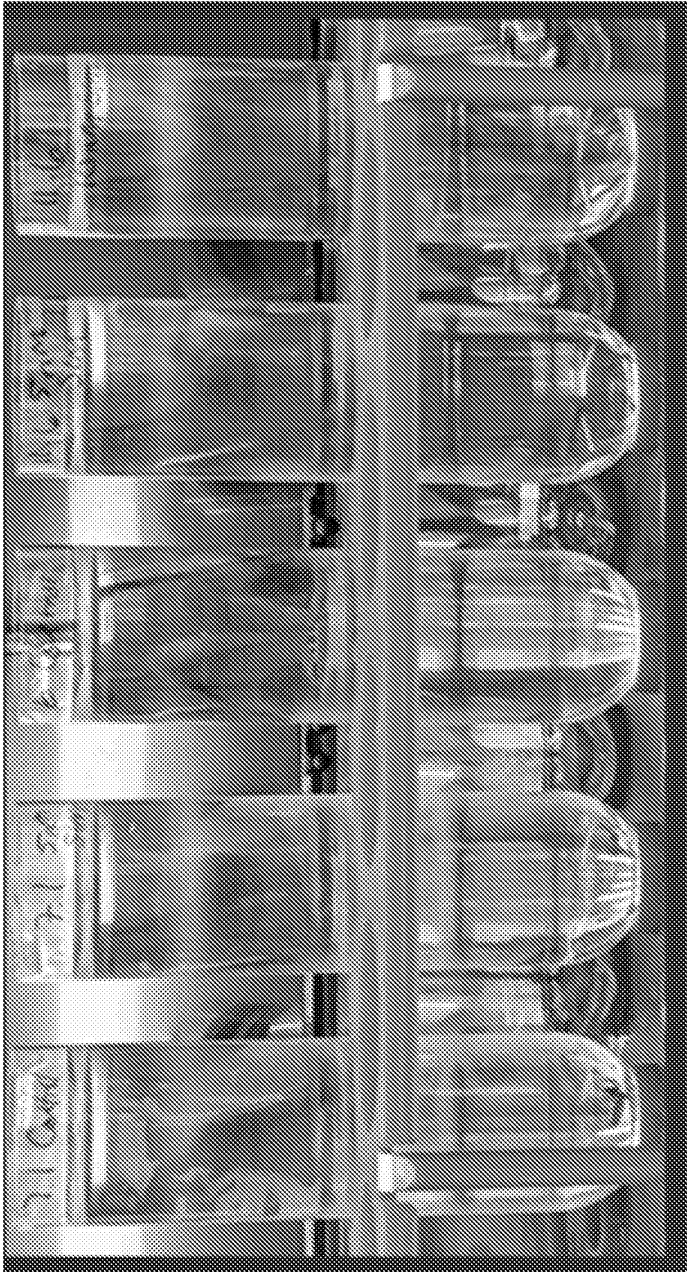


FIG. 5A

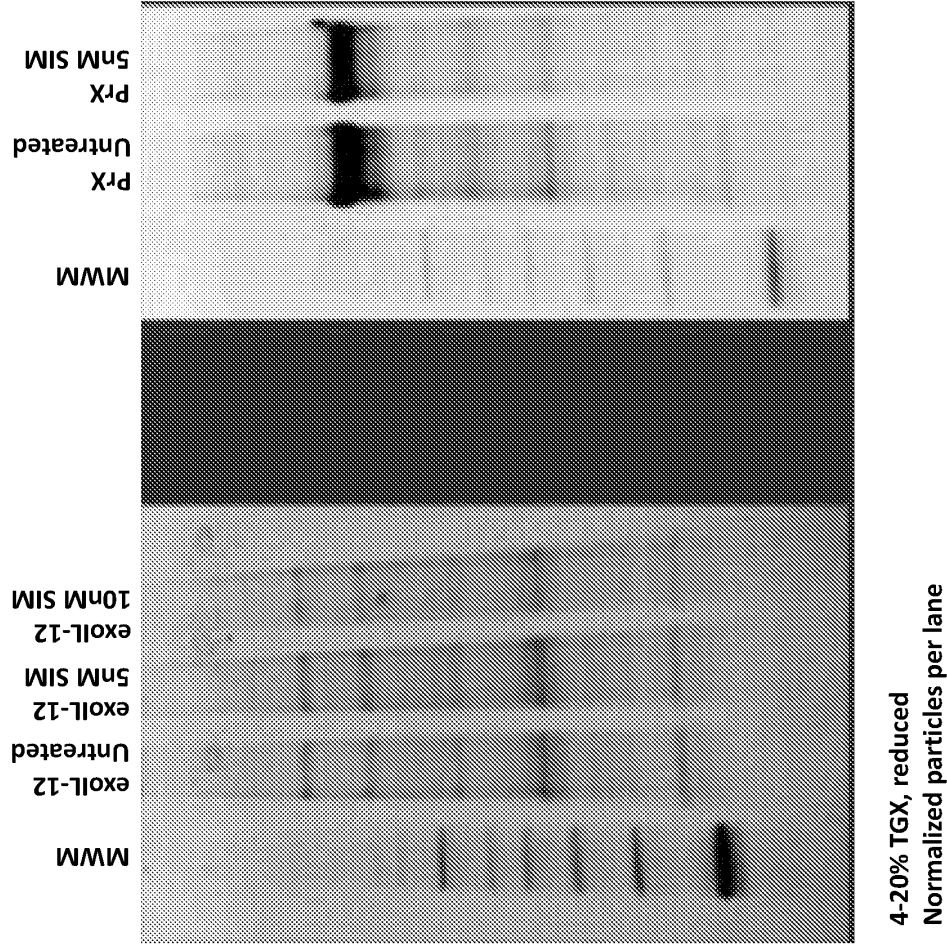


FIG. 5B

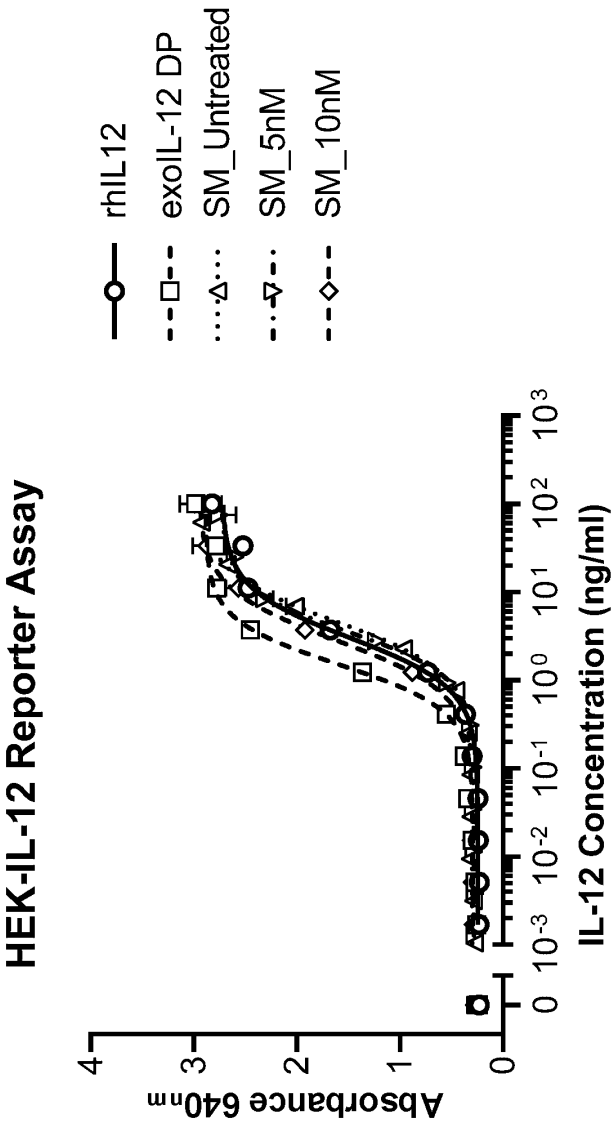


FIG. 6A



FIG. 6B

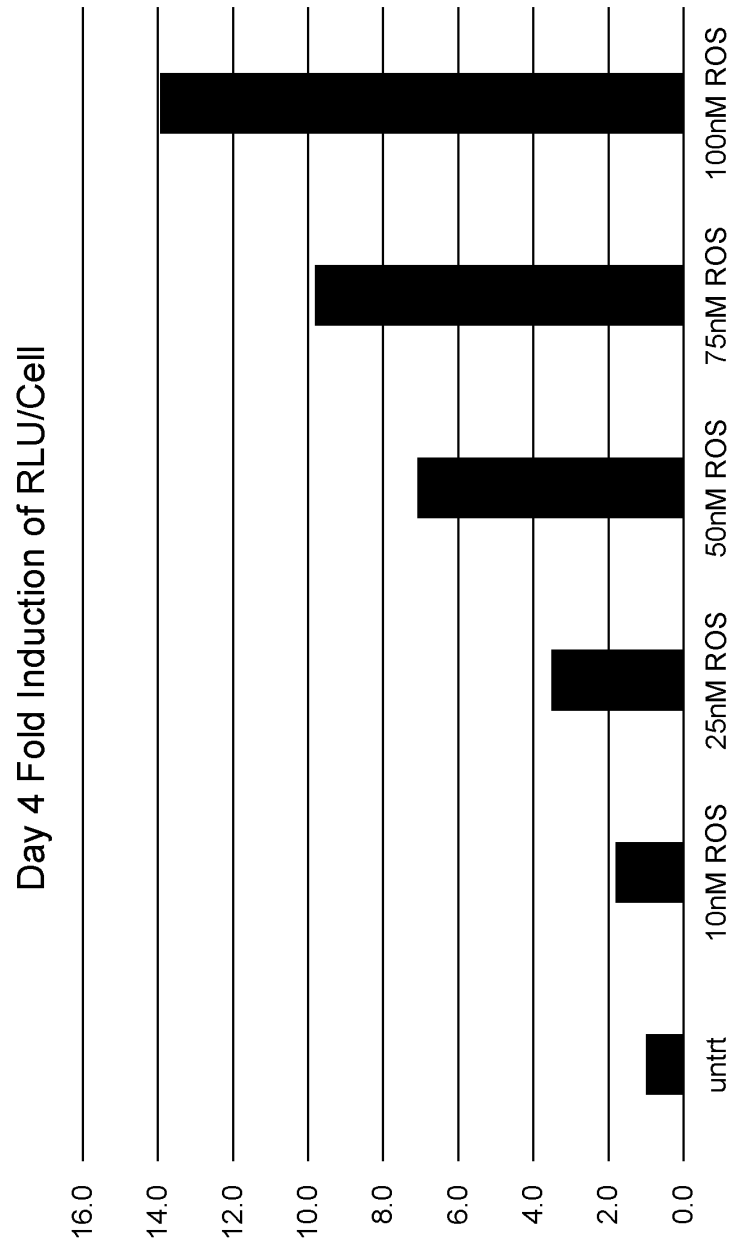


FIG. 7

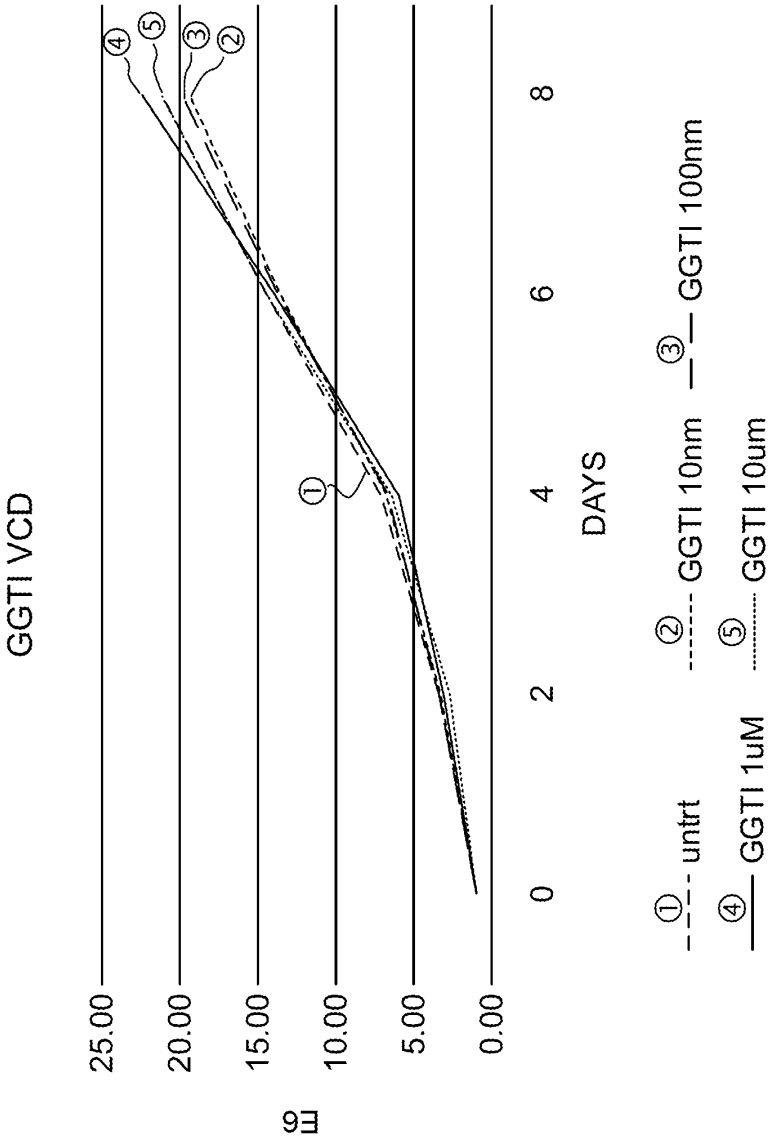


FIG. 8A

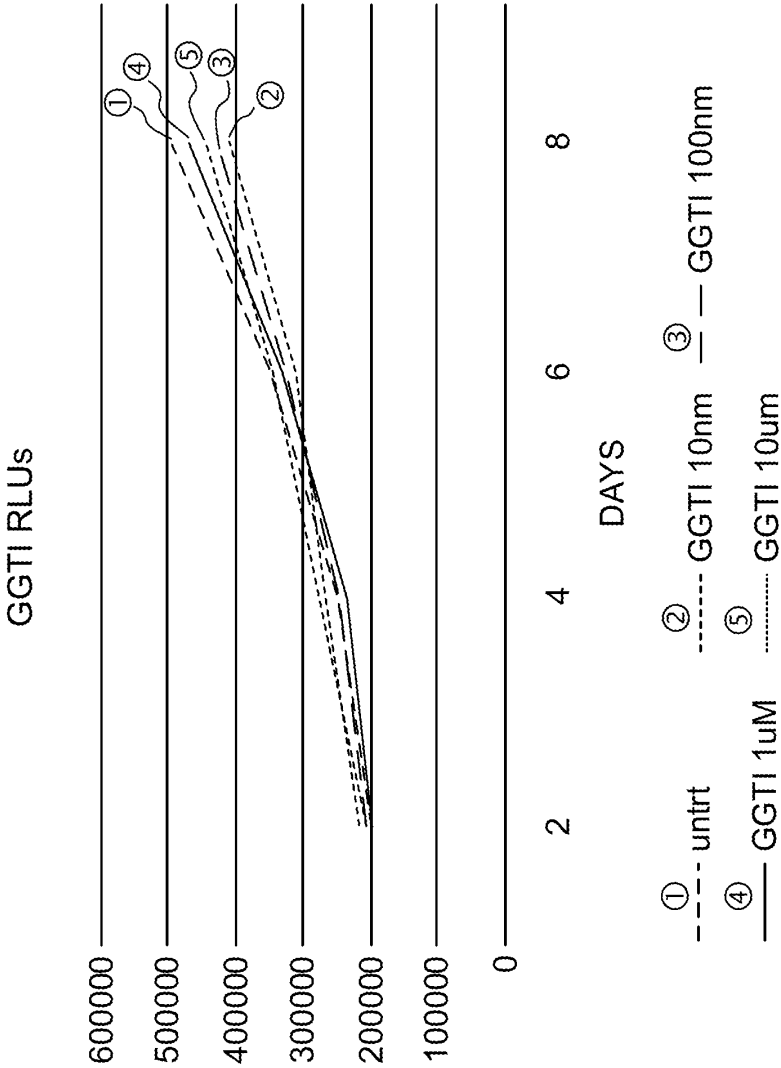


FIG. 8B

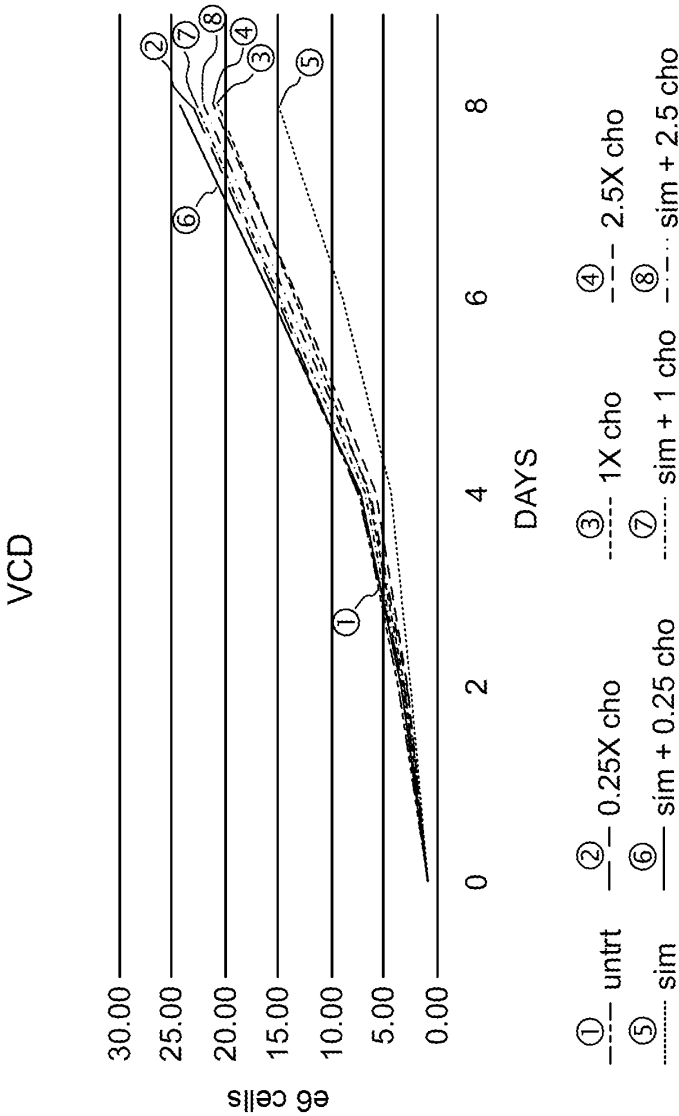


FIG. 9A

RLUs

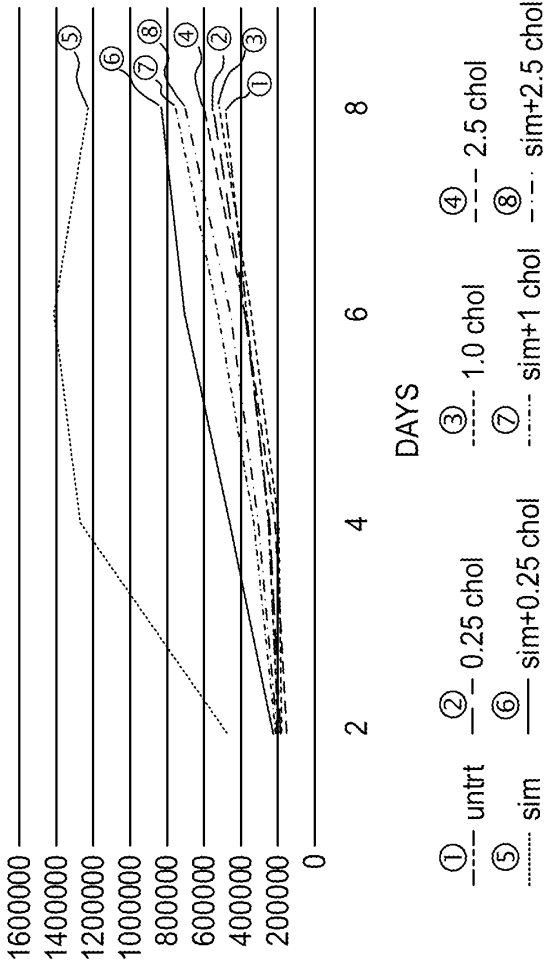


FIG. 9B