

A METHOD AND APPARATUS FOR PRE-CODING FREQUENCY DIVISION DUPLEXING SYSTEM

FIELD OF INVENTION

[0001] The present description relates generally to a pre-coding technique, more particularly, pre-coding for multiple input and multiple output (MIMO) system that uses a frequency division duplexing (FDD).

BACKGROUND

[0002] Wireless communication systems are widely deployed to provide various types of communication content such as voice, data, and so on. These systems may be multiple-access systems capable of supporting communication with multiple users by sharing the available system resources (e.g., bandwidth and transmit power). Examples of such multiple-access systems include code division multiple access (CDMA) systems, time division multiple access (TDMA) systems, frequency division multiple access (FDMA) systems, and orthogonal frequency division multiple access (OFDMA) systems.

[0003] Generally, a wireless multiple-access communication system can simultaneously support communication for multiple wireless terminals. Each terminal communicates with one or more base stations via transmissions on the forward and reverse links. The forward link (or downlink) refers to the communication link from the base stations to the terminals, and the reverse link (or uplink) refers to the communication link from the terminals to the base stations. This communication link may be established via a single-in-single-out, multiple-in-single-out or a multiple-in-multiple-out (MIMO) system.

[0004] A MIMO system employs multiple (N_T) transmit antennas and multiple (N_R) receive antennas for data transmission. A MIMO channel formed by the N_T transmit and N_R receive antennas may be decomposed into N_S independent channels, which are also referred to as spatial channels, where $N_S \leq \min\{N_T, N_R\}$. Each of the N_S independent channels corresponds to a dimension. The MIMO system can provide improved performance (e.g., higher throughput, greater reliability, best spectral

efficiency, etc.) if the additional dimensionalities created by the multiple transmit and receive antennas are utilized.

[0005] A MIMO system supports a time division duplex (TDD) and frequency division duplex (FDD) systems. In a TDD system, the forward and reverse link transmissions are on the same frequency region so that the reciprocity principle allows the estimation of the forward link channel from the reverse link channel. This enables the access point to extract transmit eigen-beamforming gain on the forward link when multiple antennas are available at the access point. However, in a frequency division duplex (FDD) system, the forward and reverse link transmissions are on widely separated frequencies. As a result, the forward link channel and the reverse link channel may fade independently. A direct consequence is that the reverse link channel estimates do not provide instantaneous channel knowledge of the forward link. This problem is further complicated in a system with multiple transmit and multiple receive antennas, also known as MIMO.

[0006] Thus, there is a need for method of pre-coding wherein the receiver transmits beam vector information over the reverse link and then the transmitter uses this information to transmit data in the preferred direction to receiver.

BRIEF SUMMARY OF THE INVENTION

[0007] In an embodiment, an apparatus comprises plurality of electronic devices, each having a logic, wherein the apparatus is configured use one or more electronic devices to determine a preferred rank value and a matrix index. The apparatus is further configured to transmit the rank value and matrix index to another electronic device.

[0008] In an embodiment, an apparatus comprises plurality of electronic devices, each having a logic, wherein the apparatus is configured use one or more electronic devices to receive a message comprising a rank value and a matrix index. The apparatus is further configured to determine if the received matrix may be used or discarded.

[0009] A more complete appreciation of all the advantages and scope of the invention can be obtained from the accompanying drawings, the description and the appended claims.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] The features, nature, and advantages of the present disclosure will become more apparent from the detailed description set forth below when taken in conjunction with the drawings in which like reference characters identify correspondingly throughout and wherein:

[0011] Fig. 1 illustrates a multiple access wireless communication system according to one embodiment;

[0012] FIG. 2 a block diagram of a communication system;

[0013] FIG. 3 illustrate a process executed by the access terminal; and

[0014] FIG. 4 illustrates a process executed by the access point.

DETAILED DESCRIPTION

[0015] Referring to Fig. 1, a multiple access wireless communication system according to one embodiment is illustrated. A access point 100 (AP) includes multiple antenna groups, one including 104 and 106, another including 108 and 110, and an additional including 112 and 114. In Fig. 1, only two antennas are shown for each antenna group, however, more or fewer antennas may be utilized for each antenna group. Access terminal 116 (AT) is in communication with antennas 112 and 114, where antennas 112 and 114 transmit information to access terminal 116 over forward link 120 and receive information from access terminal 116 over reverse link 118. Access terminal 122 is in communication with antennas 106 and 108, where antennas 106 and 108 transmit information to access terminal 122 over forward link 126 and receive information from access terminal 122 over reverse link 124. In a FDD system, communication links 118, 120, 124 and 126 may use different carrier frequency for communication. For example, forward link 120 may use a different carrier frequency than that used by reverse link 118.

[0016] Each group of antennas and/or the area in which they are designed to communicate is often referred to as a sector of the access point. In the embodiment, antenna groups each are designed to communicate to access terminals in a sector, of the areas covered by access point 100.

[0017] In communication over forward links 120 and 126, the transmitting antennas of access point 100 utilize eigen-beamforming in order to improve the spectral efficiency of forward links for the different access terminals 116 and 124. Also, an

access point using eigen-beamforming to transmit to access terminals scattered randomly through its coverage causes less interference to access terminals in neighboring cells than a access point transmitting through a single antenna to all its access terminals.

[0018] An access point may be a fixed station used for communicating with the terminals and may also be referred to as an access point, a Node B, or some other terminology. An access terminal may also be called a mobile terminal, a user equipment (UE), a wireless communication device, terminal, access terminal or some other terminology.

[0019] FIG. 2 is a block diagram of an embodiment of a transmitter system 210 (also known as the access point) and a receiver system 250 (also known as access terminal) in a MIMO system 200. At the transmitter system 210, traffic data for a number of data streams is provided from a data source 212 to a transmit (TX) data processor 214.

[0020] In an embodiment, each data stream is transmitted over a respective transmit antenna. TX data processor 214 formats, codes, and interleaves the traffic data for each data stream based on a particular coding scheme selected for that data stream to provide coded data. In some embodiments, TX data processor 214 applies eigen-beamforming weights to the symbols of the data streams using a pre-coding matrix.

[0021] The coded data for each data stream may be multiplexed with pilot data using OFDM techniques. The pilot data is typically a known data pattern that is processed in a known manner and may be used at the receiver system to estimate the channel response. The multiplexed pilot and coded data for each data stream is then modulated (i.e., symbol mapped) based on a particular modulation scheme (e.g., BPSK, QSPK, M-PSK, or M-QAM) selected for that data stream to provide modulation symbols. The data rate, coding, and modulation for each data stream may be determined by instructions performed by processor 230.

[0022] The modulation symbols for all data streams are then provided to a TX MIMO processor 220, which may further process the modulation symbols (e.g., for OFDM). TX MIMO processor 220 then provides N_T modulation symbol streams to N_T transmitters (TMTR) 222a through 222t. In certain embodiments, TX MIMO processor 220 applies eigen-beamforming weights to the symbols of the data streams and to the antenna from which the symbol is being transmitted. These eigen-beamforming weights

are determined using one of plurality of antenna by layer matrix, which may be retrieved from memory 232.

[0023] Each transmitter 222 receives and processes a respective symbol stream to provide one or more analog signals, and further conditions (e.g., amplifies, filters, and upconverts) the analog signals to provide a modulated signal suitable for transmission over the MIMO channel. N_T modulated signals from transmitters 222a through 222t are then transmitted from N_T antennas 224a through 224t, respectively.

[0024] At receiver system 250, the transmitted modulated signals are received by N_R antennas 252a through 252r and the received signal from each antenna 252 is provided to a respective receiver (RCVR) 254a through 254r. Each receiver 254 conditions (e.g., filters, amplifies, and downconverts) a respective received signal, digitizes the conditioned signal to provide samples, and further processes the samples to provide a corresponding "received" symbol stream.

[0025] An RX data processor 260 then receives and processes the N_R received symbol streams from N_R receivers 254 based on a particular receiver processing technique to provide approximately N_T "detected" symbol streams. The RX data processor 260 then demodulates, deinterleaves, and decodes each detected symbol stream to recover the traffic data for the data stream. The processing by RX data processor 260 is complementary to that performed by TX MIMO processor 220 and TX data processor 214 at transmitter system 210.

[0026] A processor 270 periodically determines which pre-coding matrix to use (discussed below). Processor 270 formulates a reverse link message comprising a matrix index portion and a rank value portion. The reverse link message may comprise various types of information regarding the communication link and/or the received data stream. The reverse link message is then processed by a TX data processor 238, which also receives traffic data for a number of data streams from a data source 236, modulated by a modulator 280, conditioned by transmitters 254a through 254r, and transmitted back to transmitter system 210.

[0027] At transmitter system 210, the modulated signals from receiver system 250 are received by antennas 224, conditioned by receivers 222, demodulated by a demodulator 240, and processed by a RX data processor 242 to extract the reserve link message transmitted by the receiver system 250. Processor 230 then determines which

pre-coding matrix to use for determining the eigen-beamforming weights then processes the extracted message.

[0028] While Fig. 2 discusses a MIMO system, the same system may be applied to a multi-input single-output system where multiple transmit antennas, e.g., those on a base station, transmit one symbol streams to a single antenna device, e.g., a mobile station. Also, a single output to single input antenna system may be utilized in the same manner as described with respect to Fig. 2.

[0029] In a MIMO system that allows pre-coding, a set of pre-coding matrices are used for eigen-beamforming. A 2^N Matrices are generated (N is number of bits used, for example 6), each matrix being $M \times L$ wherein M is the number of antenna and L is number of layers (also referred to as a rank). Each $M \times L$ entry presents eigen-beamforming weight used by the transmitter system (also referred to the access point). Generally, prior to deployment of the system, these matrices are calculated and stored in both, the access terminal and access point memory. In as aspect, these matrices may be updated in real-time over period of time. Also, each matrix is given an index number. When the AT 116 wants to request a use of matrix, the AT 116 simply transmits the matrix index. Depending on the deployed system, 6 bits may be used to index the matrix, thus indexing 64 matrices. It should be noted that number of bits used for indexing varies based on the system operator desire to use more or less than 64 matrices.

[0030] FIG. 3 illustrates a process 300, executed by the AT's processor 270. At block 302, a rank determining logic is executed by the processor for determining the rank value to provide to the AP. This rank value is determined based on several factors, for example channel estimation measurements, amount of interference or the geometry of AT 116 (i.e. the number of antennas, arrangements of the antennas, etc.). At 304, a matrix determining logic is executed by the processor 270 for determining a pre-coding matrix. This matrix is determined base on, for example, the highest supportable spectral efficiency. Both the rank value and precoding matrix may also be determined in conjunction with each other. For example, processor cycles through all the possible ranks and computes the possible spectral efficiency based a particular matrix associated with each rank. Then processor selects the rank and matrix that provide the highest spectral efficiency.

[0031] At block 306, the processor 270 executes a message building logic for generating a message that having a matrix index portion (e.g., 6 bits) and a rank portion (e.g., 2 bits for 4x4 MIMO). The matrix index portion is used to provide the matrix index associated with the selected matrix. The rank portion is used to provide rank value preferred to be used by the AT. Depending on the deployed system, if a lower rank value is used, then the indexes for matrices associated with lower rank value will be such that not all the 6 bits of the matrix index portion are used. For example, this may be achieved by indexing the pre-coding matrices such that a selected group of matrices that will only occupy 3 bits matrix index portion for a layer 1. Which means that for layer 1 the range for matrix index is $0 - 2^{*3} - 1$. If this type of system is deployed, then the AP 100 will first determine the rank value and only process those bits that are required to determine the pre-coding matrix index. At block 308, the processor 270 executes a transmit logic to transmit the message build at block 306 on the reverse link.

[0032] FIG. 4 illustrates a process 400, executed by processor 230 of the AP. At block 402, the processor 230 executes a pre-coding message processing logic for processing a message received on the reverse link comprising a matrix index portion and a rank portion. This message is received periodically, thus the pre-coding message processing logic is executed periodically. At block 404, the processor 230 determines if a proper matrix index and rank value were received on the reverse link. Depending on the condition of environment, the message containing matrix index and rank value may have been crashed or it did not properly reach the AP or it got corrupted. Various methods may be used to authenticate that the AP 100 has received a proper message on the reverse link. If determined at block 404 that the message was not authenticated or the matrix index portion was not authenticated, then at block 406 the AP 100 selects an appropriate pre-coding matrix. The AP 100 will either continue to use the current matrix or select a new matrix if processor 230 determines that the current matrix was no longer valid. The processor 230 may use some predetermined methods/thresholds stored in memory 232 to select a matrix or randomly select a matrix from the memory 232.

[0033] However, if determined, at block 404, that a message comprising matrix index and rank information was received and authenticated, then, at block 408, the processor 230 executes extraction logic to extract the rank information and determines the rank value. At block 410, the processor 230 executes the message extraction logic for extracting the pre-coding matrix index bits. In an embodiment, after extracting,

demodulating all the bits that make up the matrix index portion to determine the matrix index. In another embodiment, the processor 230 uses the rank value, determined at block 408, to determine the number of bits of matrix index portion to demodulate. For example, if the rank value is 1 and all matrices associated rank value 1 may range from 0 to 3 (e.g. 00000 to 000011). Thus, only bits that are needed to interpret the high range value, here 3, are demodulated. In this example only the two least significant bits of the matrix index portion would need be demodulated. Other bits are ignored or used for various other purposes, such as providing data. Once, the proper bits are demodulated and a matrix index is derived, at block 412, the processor 230 executes matrix use logic for determining if the matrix associated with derived matrix index can be used. In a multi-user system, the AP 100 receives the pre-coding requests from several users. The AP 100 is provided with predetermined criteria for determining the use of a particular matrix. In aspect, the AP 100 may determine if a requested matrix can be used or not based on current condition of each user. At block 414, if determined that the matrix associated with the received matrix index can not be used, then the processor 230 executes alternate matrix selection logic for selecting another matrix for eigen-beamforming. Otherwise, at block 416, the processor 230 using the matrix associated with extracted matrix index for eigen-beamforming.

[0034] The techniques described herein may be implemented by various means. For example, these techniques may be implemented in hardware, software, or a combination thereof. For a hardware implementation, the processing units (e.g., processor 230 and 270, TX and RX processors 214 and 260, and so on) for these techniques may be implemented within one or more electronic devices such as application specific integrated circuits (ASICs), digital signal processors (DSPs), digital signal processing devices (DSPDs), programmable logic devices (PLDs), field programmable gate arrays (FPGAs), processors, controllers, micro-controllers, microprocessors, other electronic units designed to perform the functions described herein, or a combination thereof.

[0035] For a software implementation, the techniques described herein may be implemented with modules (e.g., procedures, functions, and so on) that perform the functions described herein. The software codes may be stored in memory units (e.g., memory 232 and 272 in FIG. 2) and executed by processors (e.g., controllers 230). The memory unit may be implemented within the processor or external to the processor, in

which case it can be communicatively coupled to the processor via various means as is known in the art.

[0036] Headings are included herein for reference and to aid in locating certain sections. These headings are not intended to limit the scope of the concepts described therein under, and these concepts may have applicability in other sections throughout the entire specification.

[0037] The previous description of the disclosed embodiments is provided to enable any person skilled in the art to make or use the present invention. Various modifications to these embodiments will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other embodiments without departing from the spirit or scope of the invention. Thus, the present invention is not intended to be limited to the embodiments shown herein but is to be accorded the widest scope consistent with the principles and novel features disclosed herein.

CLAIMS**WHAT IS CLAIMED IS:**

1. An apparatus operable in a wireless communication system, the apparatus comprising:
means for determining a rank value;
means for determining a matrix index; and
means for transmitting said reverse link message comprising a matrix index and said rank value.
2. The apparatus as claimed in claim 1, wherein said means for determining said rank value comprises means for measuring channel estimates.
3. The apparatus as claimed in claim 1, wherein said means for determining said rank value comprises means for measuring amount of interference.
4. The apparatus as claimed in claim 1, wherein said means for determining said matrix index comprises means for using said determined rank value.
5. The apparatus as claimed in claim 1, wherein said means for determining said matrix index comprises means for analyzing each matrix of a plurality of matrices stored in memory.
6. The apparatus as claimed in claim 1, wherein said means for determining said matrix index comprises means for selecting highest spectral efficiency.
7. An apparatus operable in a wireless communication system, the apparatus comprising:
receiving a rank value and a matrix index;
extracting a matrix comprising beamforming weight values using said rank value and said matrix index; and
determining if said extracted matrix should be used.
8. The apparatus as claimed in claim 7, further comprising means for extracting said matrix index using said received rank value.
9. The apparatus as claimed in claim 7, further comprising means for determining number of bits to demodulate to determine said matrix index.

10. The apparatus as claimed in claim 9, wherein means for determining number of bits to demodulate comprises means for analyzing said rank value.
11. A method of pre-coding in a wireless communication system, the method comprising:
 - determining a rank value;
 - determining a matrix index; and
 - transmitting said reverse link message comprising a matrix index and said rank value.
12. The method as claimed in claim 11, wherein said determining said rank value comprises of measuring channel estimates.
13. The method as claimed in claim 11, wherein said determining said rank value comprises of measuring amount of interference.
14. The method as claimed in claim 11, wherein said determining said matrix index comprises using said determined rank value.
15. The method as claimed in claim 11, wherein said determining said matrix index comprises analyzing each matrix of a plurality of matrices stored in memory.
16. The method as claimed in claim 11, wherein said determining said matrix index comprises of selecting highest spectral efficiency.
17. The method of pre-coding in a wireless communication system, the method comprising:
 - receiving a rank value and a matrix index;
 - extracting a matrix comprising beamforming weight values using said rank value and said matrix index; and
 - determining if said extracted matrix should be used.
18. The method as claimed in claim 17, further comprising extracting said matrix index using said received rank value.
19. The method as claimed in claim 17, further comprising determining number of bits to demodulate to determine said matrix index.
20. The method as claimed in claim 19, wherein determining number of bits to demodulate comprises of analyzing said rank value.
21. In a wireless communication, an apparatus comprising:
 - a processor, said processor configured to determine a rank value; said processor further configured to determine a matrix index; and said processor

further configured to transmit said reverse link message comprising a matrix index and said rank value.

22. The apparatus as claimed in claim 21, wherein said processor further configured to measure channel estimates wherein said measured channel estimates are used to determine rank value.
23. The apparatus as claimed in claim 21, wherein said processor further configured to measure amount of interference wherein said measured channel estimates are used to determine rank value.
24. The apparatus as claimed in claim 21, wherein said processor further configured to use said determined rank value to determine said matrix index.
25. The apparatus as claimed in claim 21, wherein said processor further configured to analyze each matrix of a plurality of matrices stored in memory to determine said matrix index.
26. The apparatus as claimed in claim 21, wherein said processor further configured to select highest spectral efficiency to determine said matrix index.
27. In a wireless communication, an apparatus comprising:
 - a processor, said processor configured to receive a rank value and a matrix index; said processor further configured to extract a matrix comprising beamforming weight values using said rank value and said matrix index; and said processor further configured to determine if said extracted matrix should be used.
28. The apparatus as claimed in claim 27, said processor further configured to extract said matrix index using said received rank value.
29. The apparatus as claimed in claim 27, said processor further configured to further to determine number of bits to demodulate to determine said matrix index.
30. The apparatus as claimed in claim 29, said processor further configured to analyze said rank value to determining number of bits to demodulate.
31. A machine-readable medium comprising instructions which, when executed by a machine, cause the machine to perform operations including:
 - means for determining a rank value;
 - means for determining a matrix index; and
 - means for transmitting said reverse link message comprising a matrix index and said rank value.

32. The machine-readable medium of claimed in claim 31, wherein said means for determining said matrix index comprises means for using said determined rank value.
33. The machine-readable medium of as claimed in claim 31, wherein said means for determining said matrix index comprises means for analyzing each matrix of a plurality of matrices stored in memory.
34. An apparatus operable in a wireless communication system, the apparatus comprising:
receiving a rank value and a matrix index;
extracting a matrix comprising beamforming weight values using said rank value and said matrix index; and
determining if said extracted matrix should be used.
35. The machine-readable medium of claimed in claim 34, wherein means for determining number of bits to demodulate comprises means for analyzing said rank value.