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16/855,229 22 April 2020 (22.04.2020) US(71) Applicant: **INTERNATIONAL BUSINESS MACHINES CORPORATION** [US/US]; New Orchard Road, Armonk, New York 10504 (US).(71) Applicant (for MG only): **IBM (CHINA) CO., LIMITED** [CN/CN]; 7F, Bldg 10, Zhangjiang Innovation Park, 399 Keyuan Road, Zhangjiang High-Tech Campus, Pudong New Area, Shanghai 201203 (CN).(72) Inventors: **GUPTA, Lokesh Mohan**; 9000 S. Rita Rd., Tucson, Arizona 85744 (US). **HARDY, Clint A.**; 9000 S. Rita Rd., Tucson, Arizona 85744 (US). **RINALDI, Brian Anthony**; 9000 S. Rita Rd., Tucson, Arizona 85744 (US).**NIELSEN, Karl Allen**; 9000 S. Rita Rd., Tucson, Arizona 85744 (US).(74) Agent: **CCPIT PATENT AND TRADEMARK LAW OFFICE**; 10/F, Ocean Plaza, 158 Fuxingmennei Street, Beijing 100031 (CN).

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(54) Title: PREEMPTIVE STAGING FOR FULL-STRIDE DESTAGE

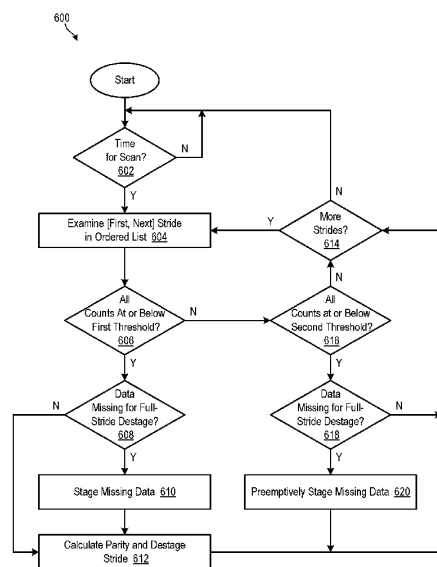


Fig. 6

(57) Abstract: There is a method for improving destage performance to a RAID array. The method periodically scans a cache for first strides that are ready to be destaged to a RAID array. While scanning the cache, the method identifies second strides that are not currently ready to be destaged to the RAID array, but will likely be ready to be destaged during a subsequent scan of the cache. The method initiates preemptive staging of any missing data of the second strides from the RAID array into the cache in preparation for the subsequent scan. Upon occurrence of the subsequent scan, the method destages, from the cache, the second strides from the cache to the RAID array.

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