

DATA PROCESSOR HAVING DATA PROCESSING UNIT INCORPORATING
SCRAMBLE AND DESCRAMBLE MEANS

ABSTRACT

A data processor comprises a data memory (301) and a data processing unit (302) which is connected to the data memory (301) through a data bus (303) and through an address bus (304) and which writes the data to the data memory (301) or reads data from the data memory (301). A circuit (305) for scrambling data to be written to the data memory (301) and a circuit (306) for descrambling the scrambled data read from the data memory (301) are provided in the data processing unit (302). Therefore, probing the data bus (303) or the stored states of memory cells in the data memory (301) allows only the scrambled data to be acquired.

FIG 3