

SPLIT FLASH ANALOG TO DIGITAL CONVERTER DIFFERENTIAL DRIVER**ABSTRACT**

A high speed differential analog to digital converter (ADC) is provided. The high speed differential ADC includes a driver section 102, a comparator section 104 and a decoder section 106. The driver section 102 includes a pair of series connected resistor ladders (R1-R32, R33-R64) with a positive phase voltage source connected at the top and bottom of one of the pair of series connected resistor ladders and a negative phase voltage source connected at the top and bottom of the other one of the pair of series connected resistor ladders; both the positive phase voltage source and the negative phase voltage source including a predetermined first DC voltage value.

Fig. 4

This Sheet Has Been Replaced :

