

ABSTRACT:

The device according to the invention comprises a frequency synthesizer (30) of the phase-locked loop type. For obtaining a fast locking, a phase discriminator (40) featuring an optimum characteristic is included. When there is no phase locking, the output voltage of the phase discriminator remains constant, which provides considerable gain for the loop for obtaining the capturing. When there is phase locking, the phase discriminator produces an error proportional to the phase difference.

Application to radiotelephones.

Fig. 6.

This Sheet Has Been Replaced

