30 SEP 2004

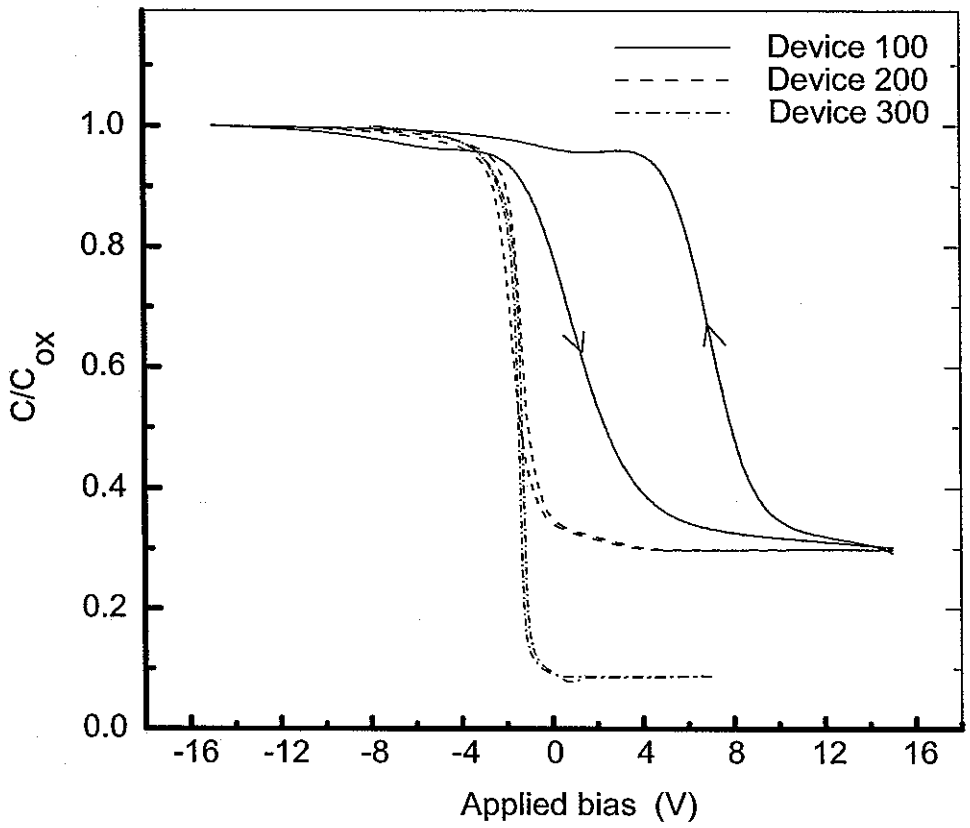


FIG. 1

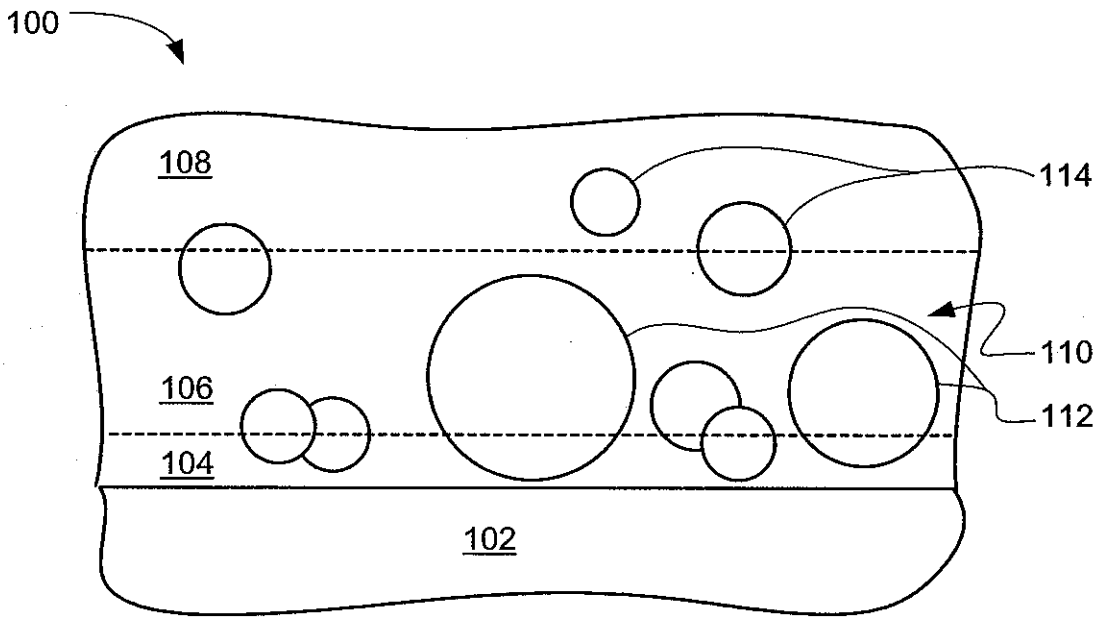


FIG. 2

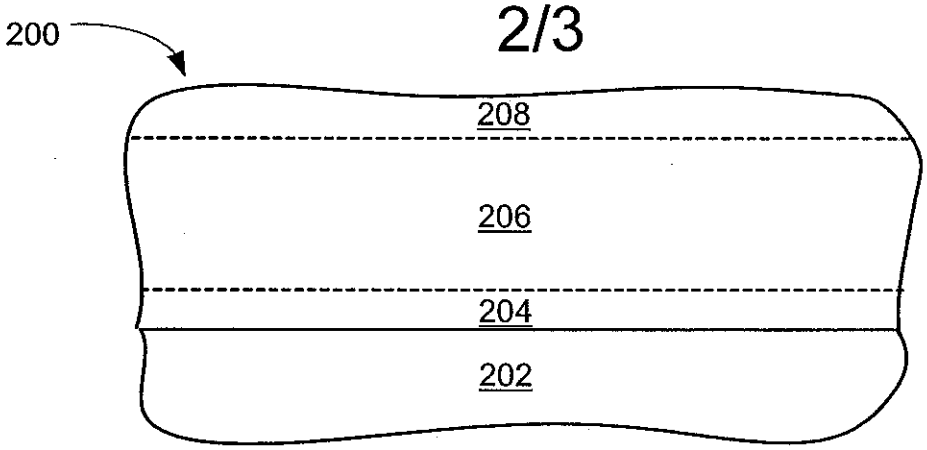


FIG. 3

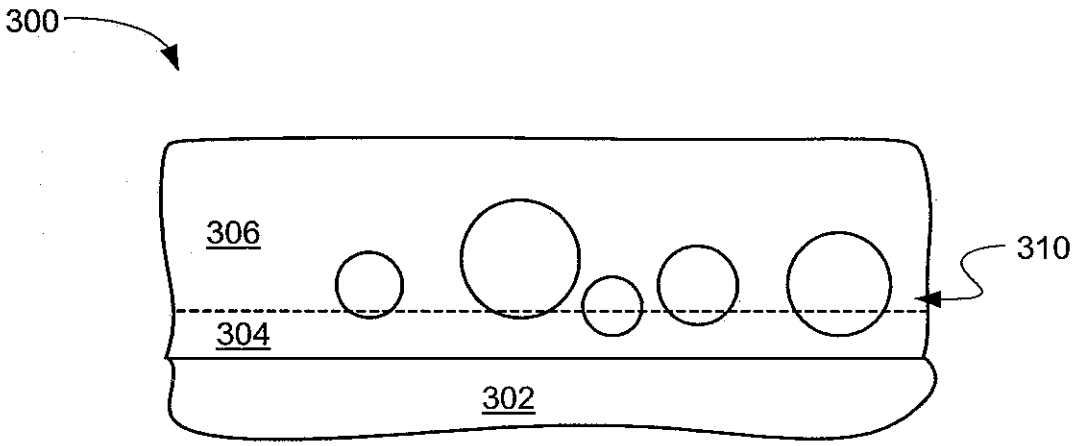


FIG. 4

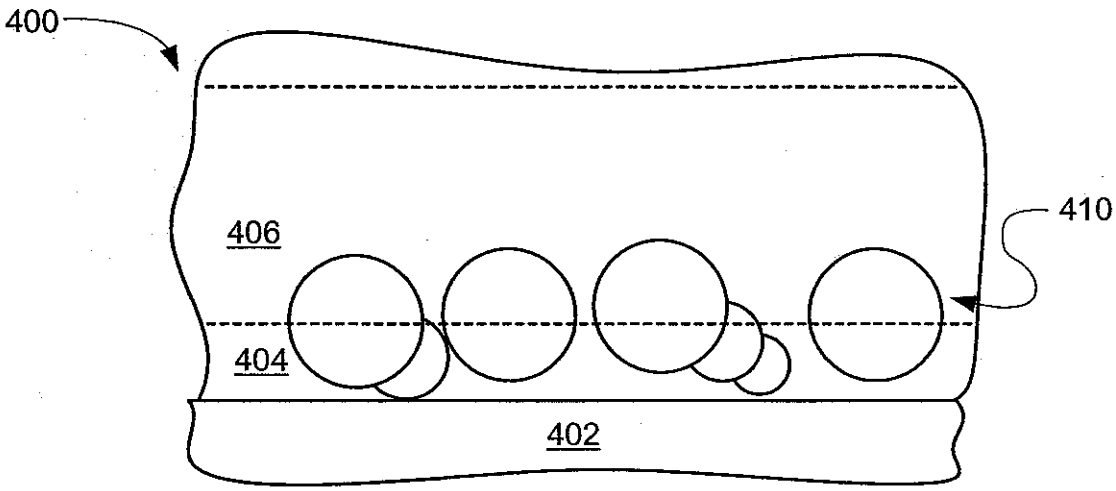


FIG. 5

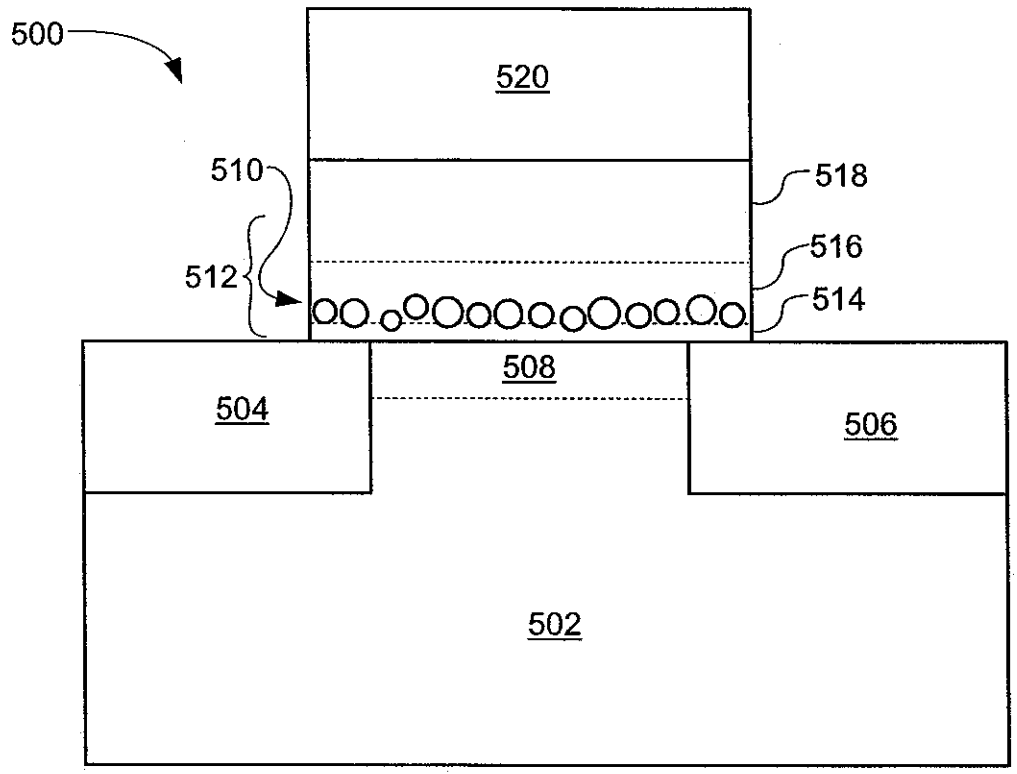
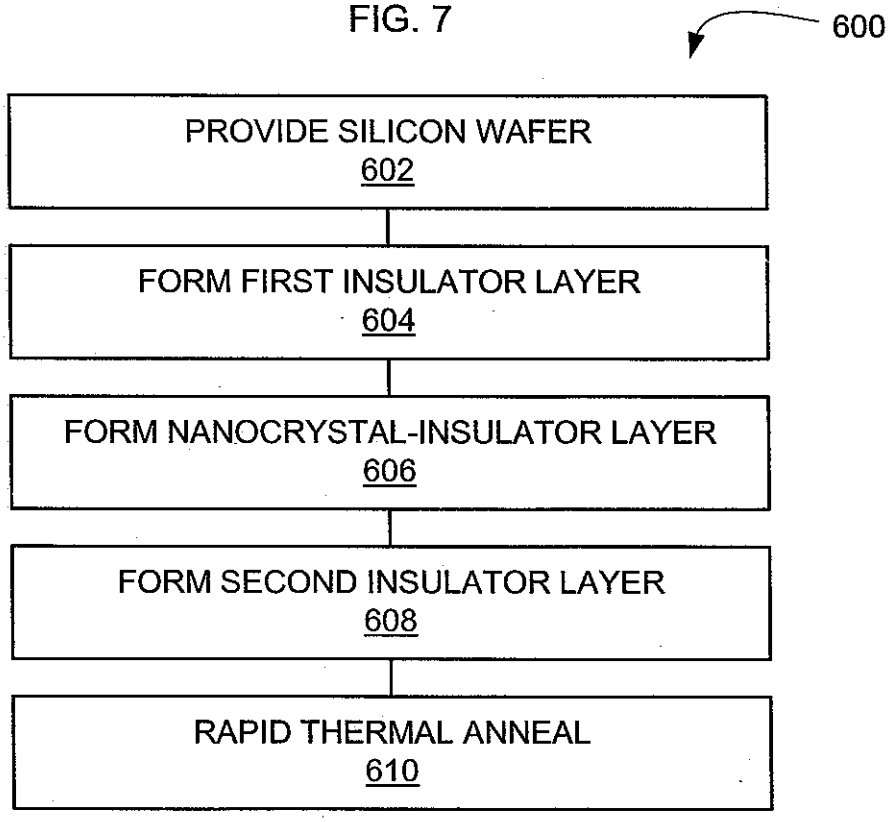

500
520
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512
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504
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502

FIG. 6

FIG. 7


600
PROVIDE SILICON WAFER
602
FORM FIRST INSULATOR LAYER
604
FORM NANOCRYSTAL-INSULATOR LAYER
606
FORM SECOND INSULATOR LAYER
608
RAPID THERMAL ANNEAL
610