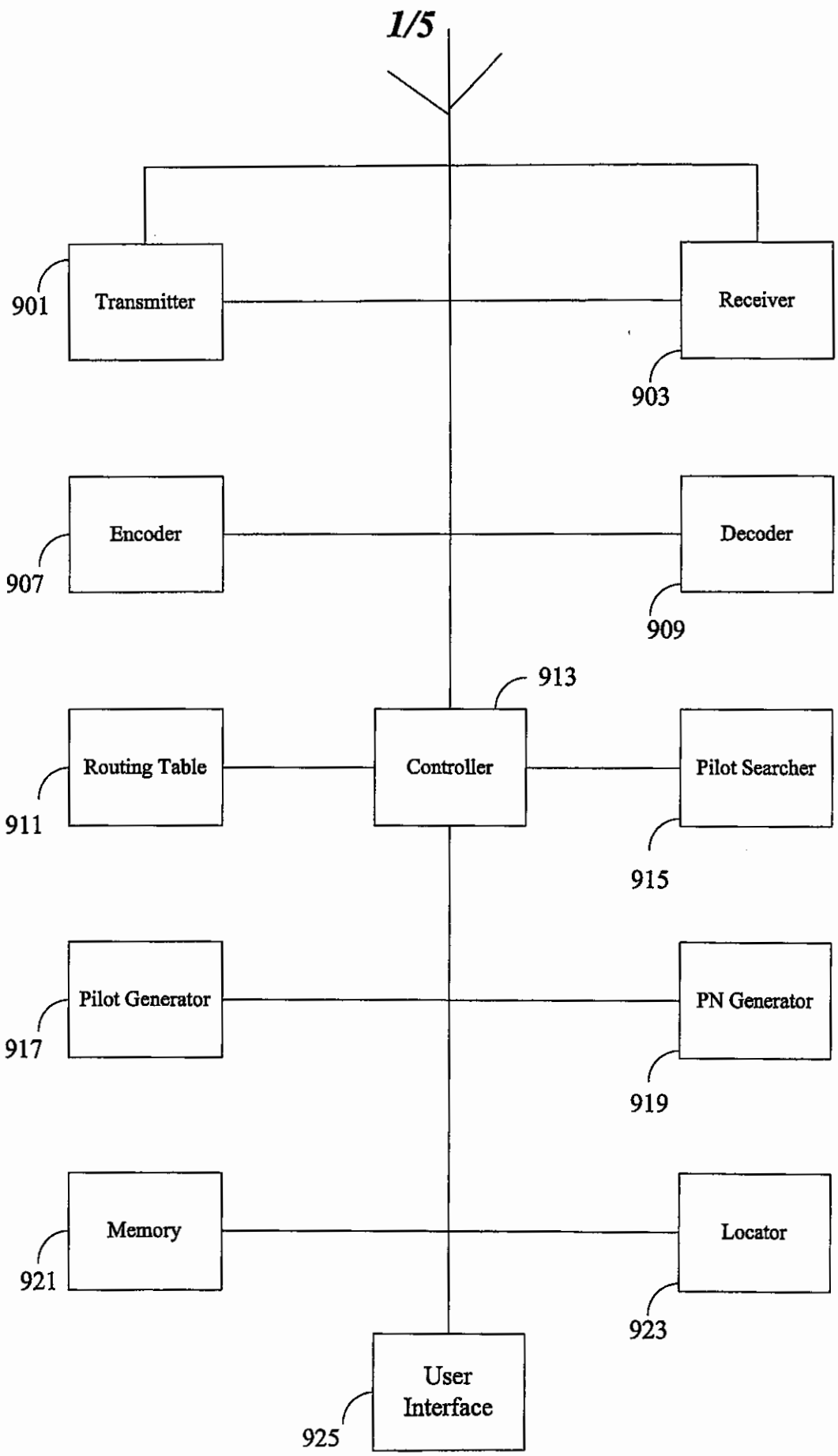

Transmitter
901
Receiver
903
Encoder
907
Decoder
909
913
Routing Table
911
Controller
Pilot Searcher
915
Pilot Generator
917
PN Generator
919
Memory
921
Locator
923
User Interface
925

FIGURE 1

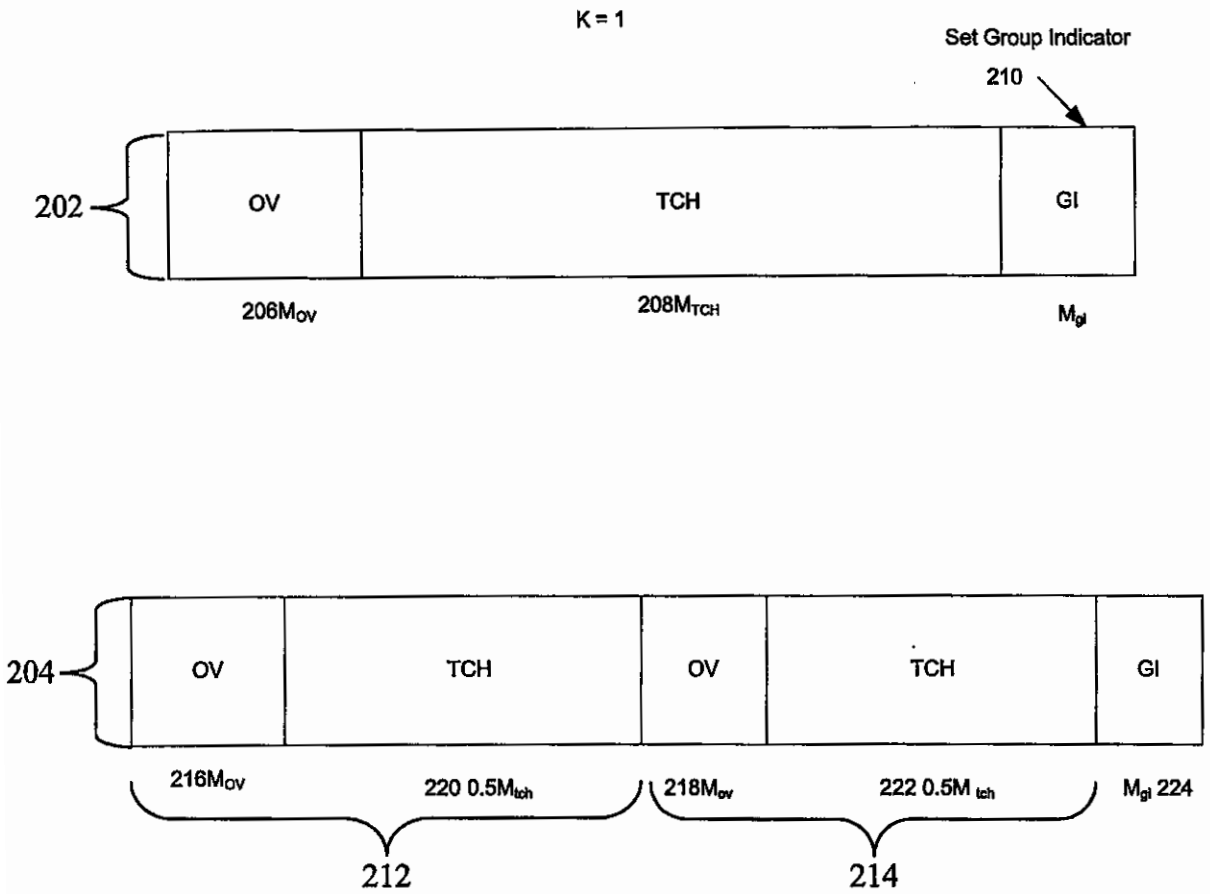

K = 1
Set Group Indicator
210
202
OV
TCH
GI
206M_OV
208M_TCH
M_gi
204
OV
TCH
OV
TCH
GI
216M_OV
220 0.5M_tch
218M_ov
222 0.5M_tch
M_gi 224
212
214

FIGURE 2

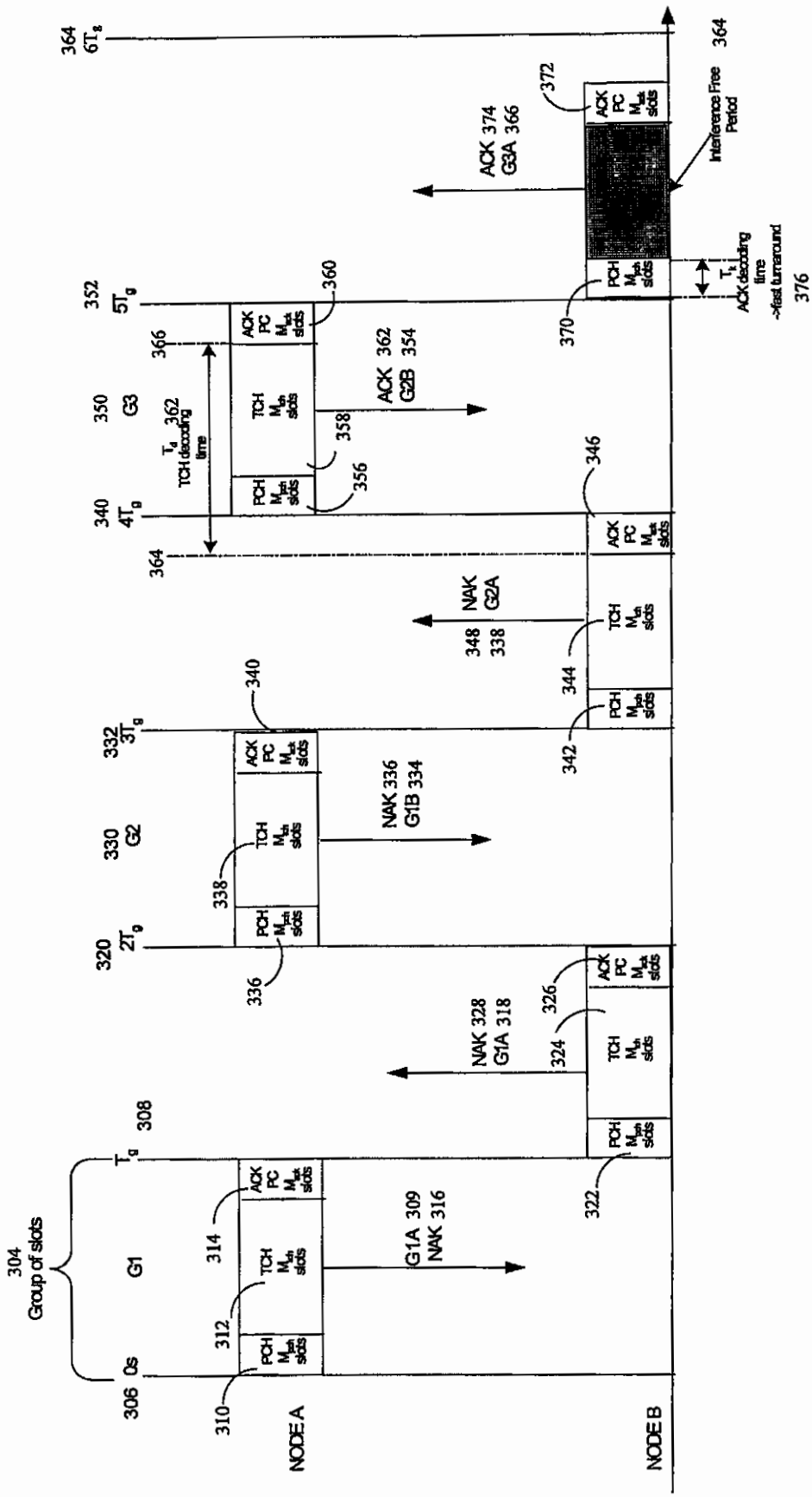
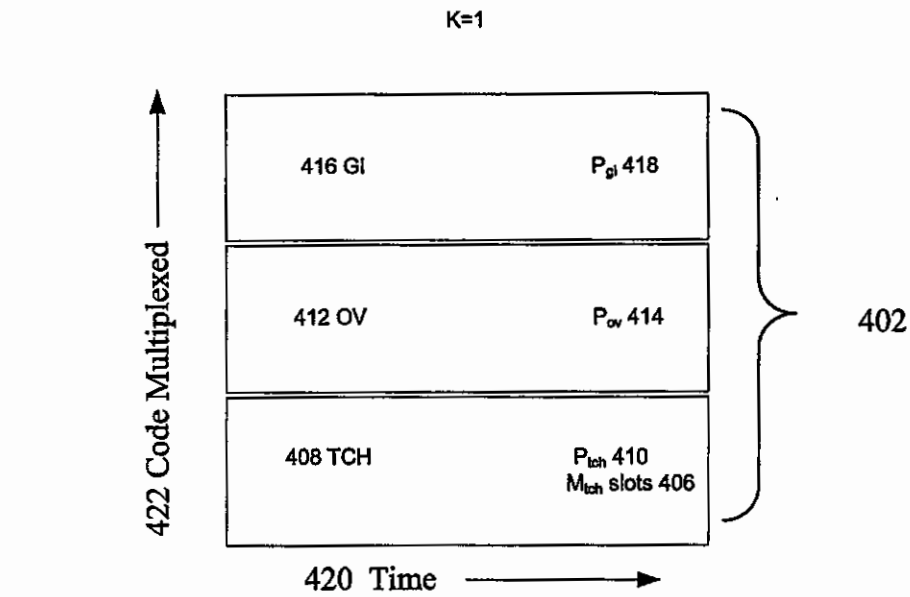

NODE A
NODE B
Group of slots
304
G1
G2
G3
0s
T_g
$2T_g$
$3T_g$
$4T_g$
$5T_g$
$6T_g$
306
308
310
312
314
320
330
332
340
350
352
364
PCH
TCH
ACK
PC
M_{pch} slots
M_{tch} slots
M_{ack} slots
G1A 309
NAK 316
NAK 328
G1A 318
NAK 336
G1B 334
NAK
G2A
348
338
ACK 362
G2B 354
ACK 374
G3A 366
T_d TCH decoding time 362
Interference Free Period
T_i ACK decoding time -fast turnaround 376
322
324
326
336
338
340
342
344
346
356
358
360
366
370
372

FIGURE 3

EARLY TERM. UPDATES 2



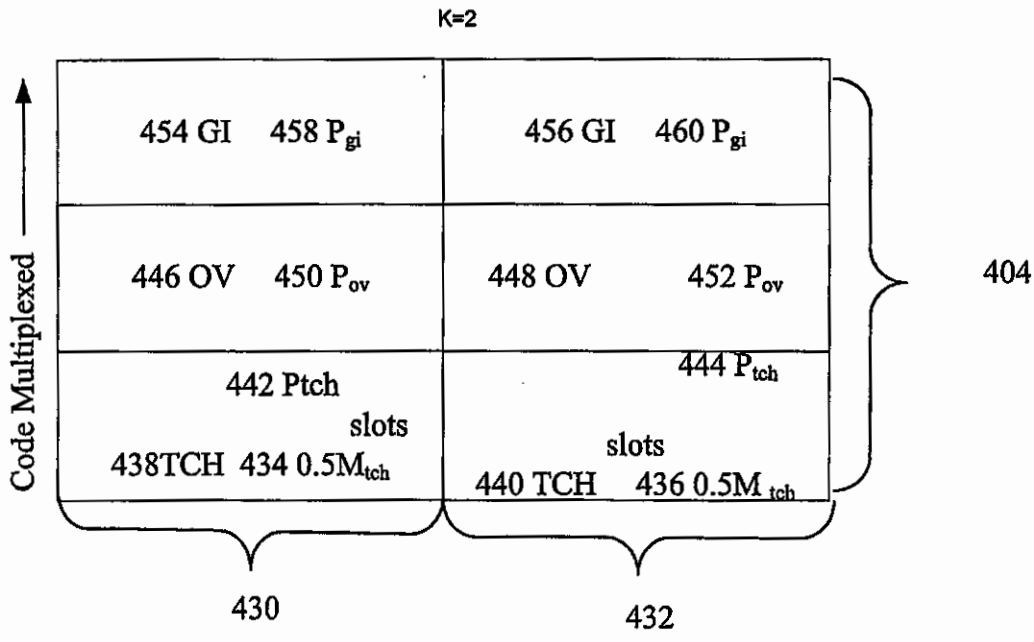


FIGURE 4

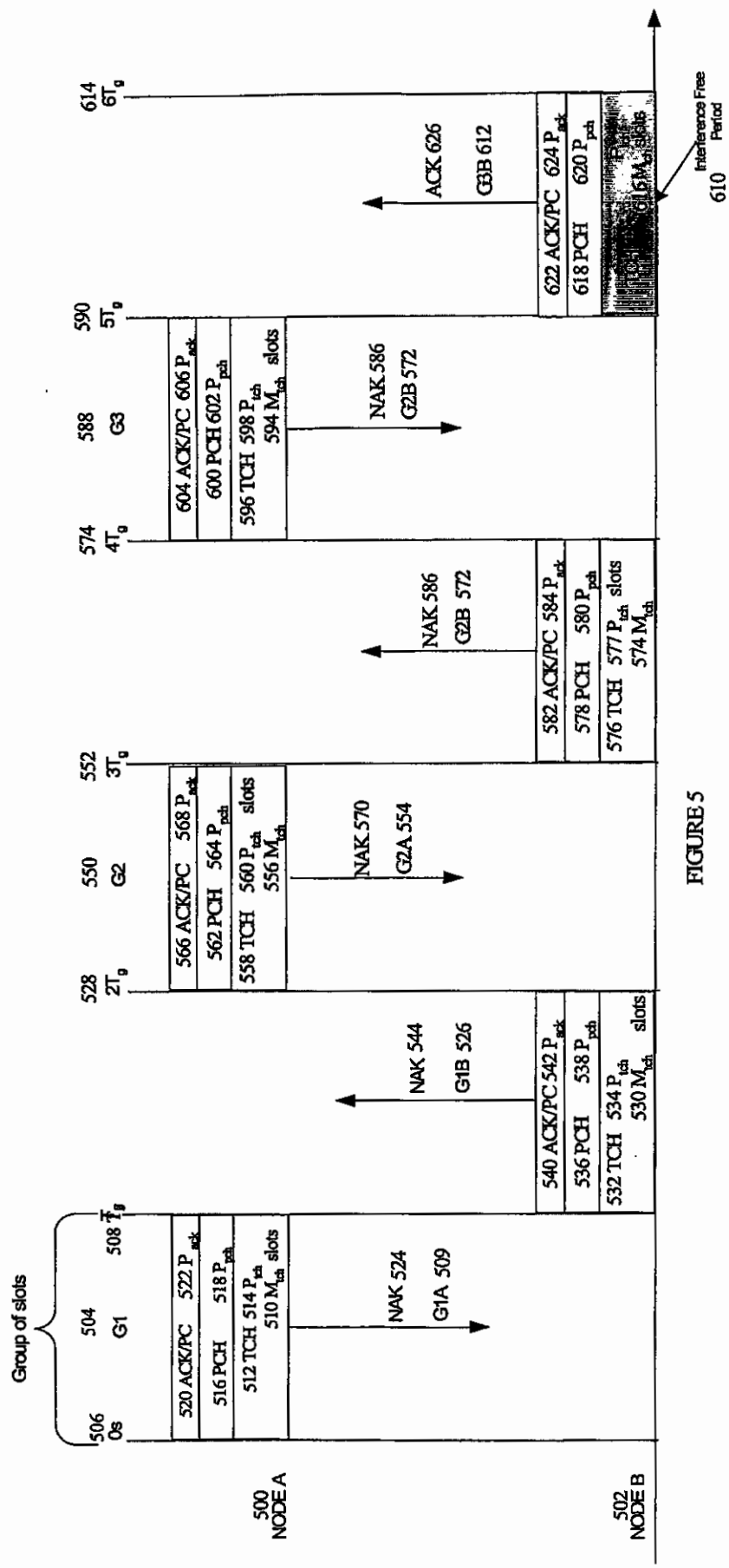

Group of slots
506 0s
504 G1
508 T_g
520 ACK/PC 522 P_{ack}
516 PCH 518 P_{pch}
512 TCH 514 P_{tch} 510 M_{tch} slots
NAK 524
G1A 509
500 NODE A
502 NODE B
528 $2T_g$
550 G2
552 $3T_g$
566 ACK/PC 568 P_{ack}
562 PCH 564 P_{pch}
558 TCH 560 P_{tch} 556 M_{tch} slots
NAK 570
G2A 554
574 $4T_g$
588 G3
590 $5T_g$
604 ACK/PC 606 P_{ack}
600 PCH 602 P_{pch}
596 TCH 598 P_{tch} 594 M_{tch} slots
NAK 586
G2B 572
614 $6T_g$
622 ACK/PC 624 P_{ack}
618 PCH 620 P_{pch}
ACK 626
G3B 612
Interference Free Period
610
540 ACK/PC 542 P_{ack}
536 PCH 538 P_{pch}
532 TCH 534 P_{tch} 530 M_{tch} slots
NAK 544
G1B 526
582 ACK/PC 584 P_{ack}
578 PCH 580 P_{pch}
576 TCH 577 P_{tch} slots 574 M_{tch}
NAK 586
G2B 572

FIGURE 5