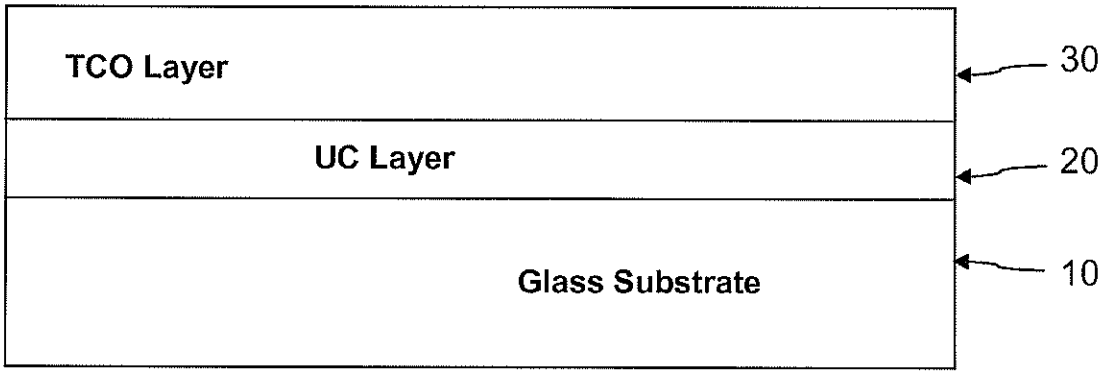


Figure 1

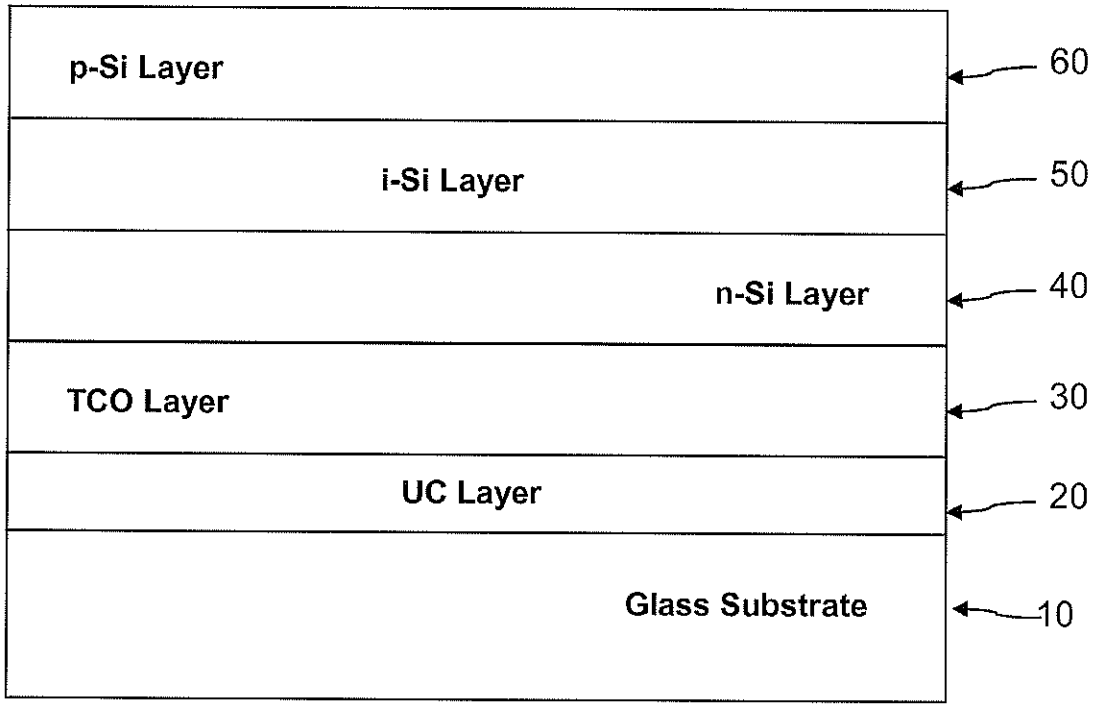

p-Si Layer
60
i-Si Layer
50
n-Si Layer
40
TCO Layer
30
UC Layer
20
Glass Substrate
10

Figure 2

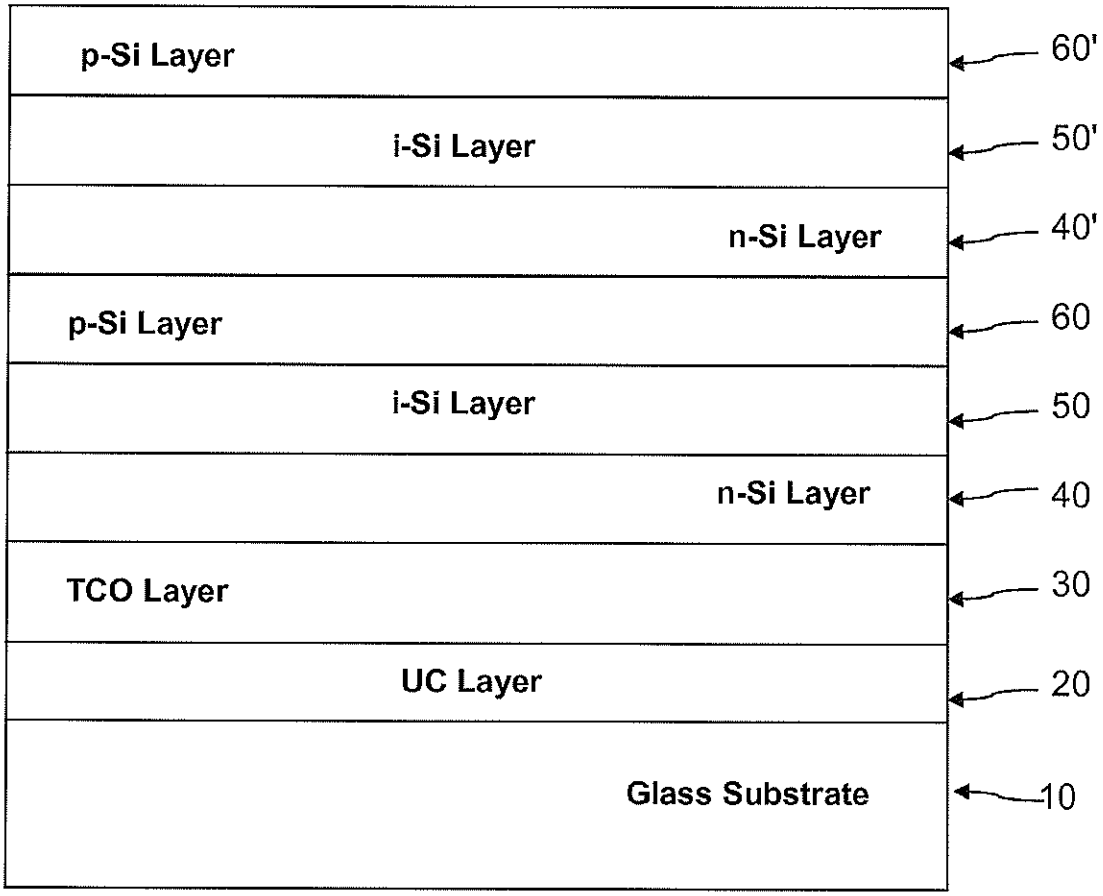

p-Si Layer
60'
i-Si Layer
50'
n-Si Layer
40'
p-Si Layer
60
i-Si Layer
50
n-Si Layer
40
TCO Layer
30
UC Layer
20
Glass Substrate
10

Figure 3

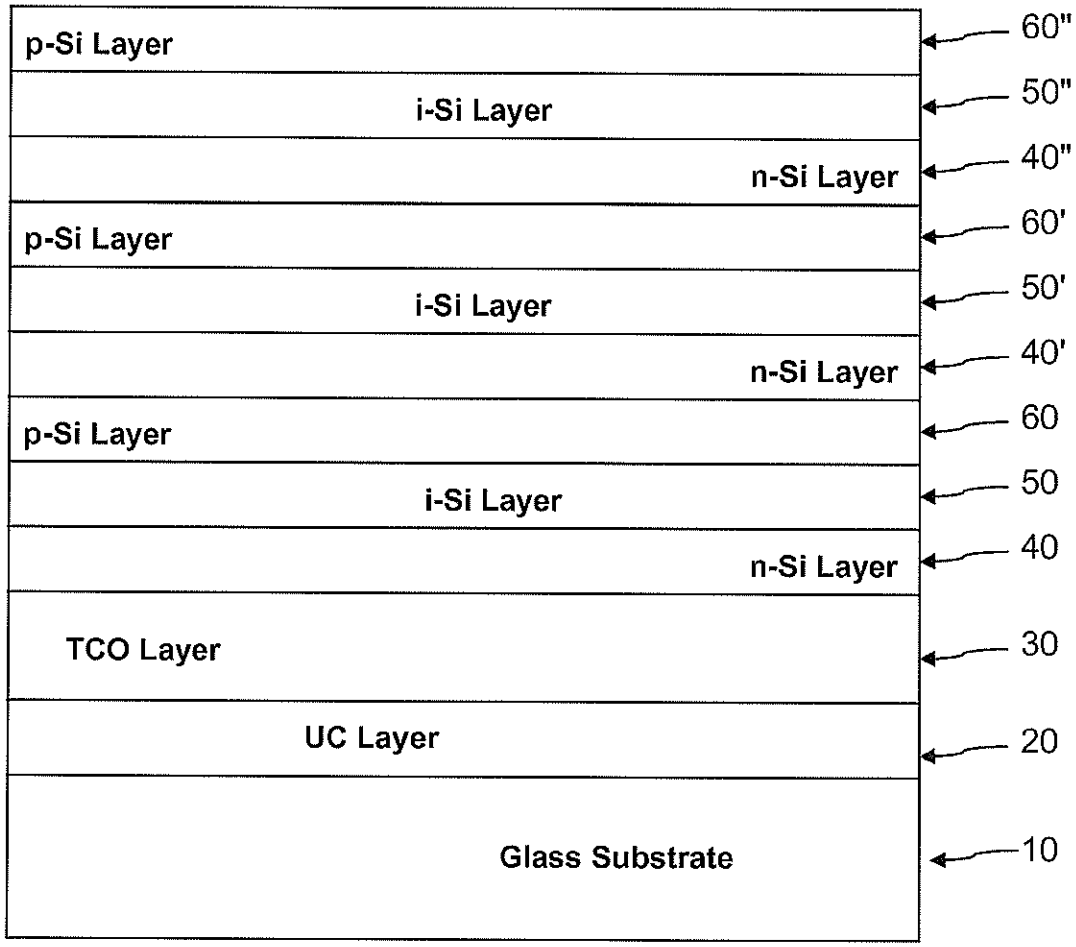

p-Si Layer
i-Si Layer
n-Si Layer
p-Si Layer
i-Si Layer
n-Si Layer
p-Si Layer
i-Si Layer
n-Si Layer
TCO Layer
UC Layer
Glass Substrate
60"
50"
40"
60'
50'
40'
60
50
40
30
20
10

Figure 4

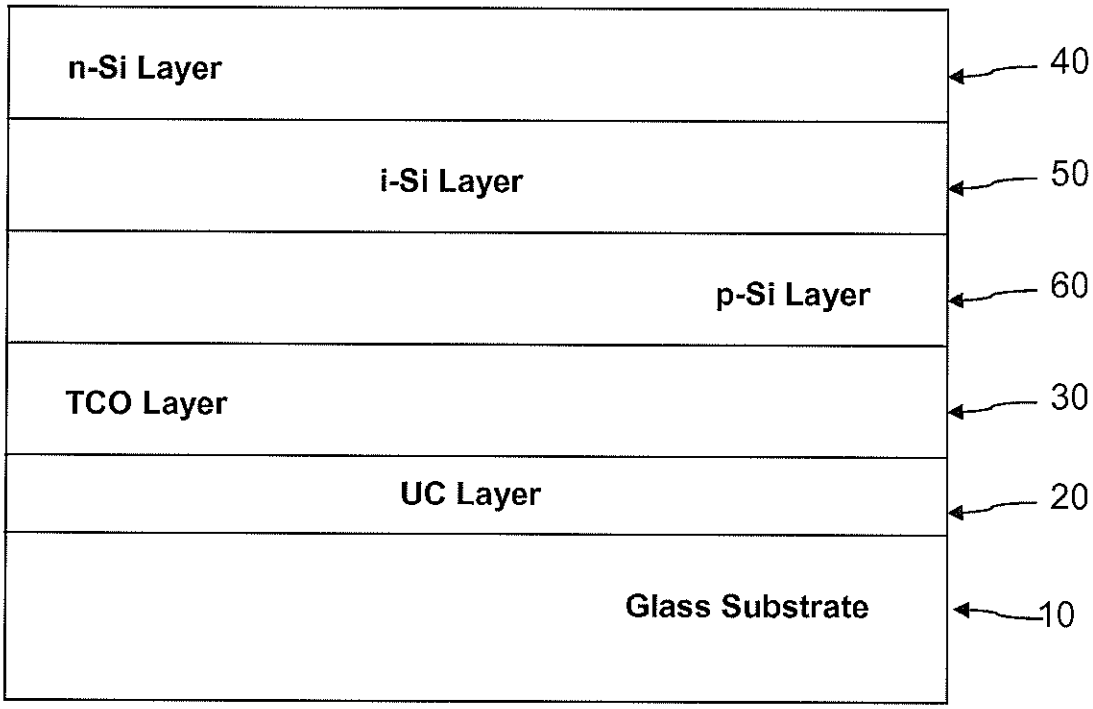

n-Si Layer
40
i-Si Layer
50
p-Si Layer
60
TCO Layer
30
UC Layer
20
Glass Substrate
10

Figure 5

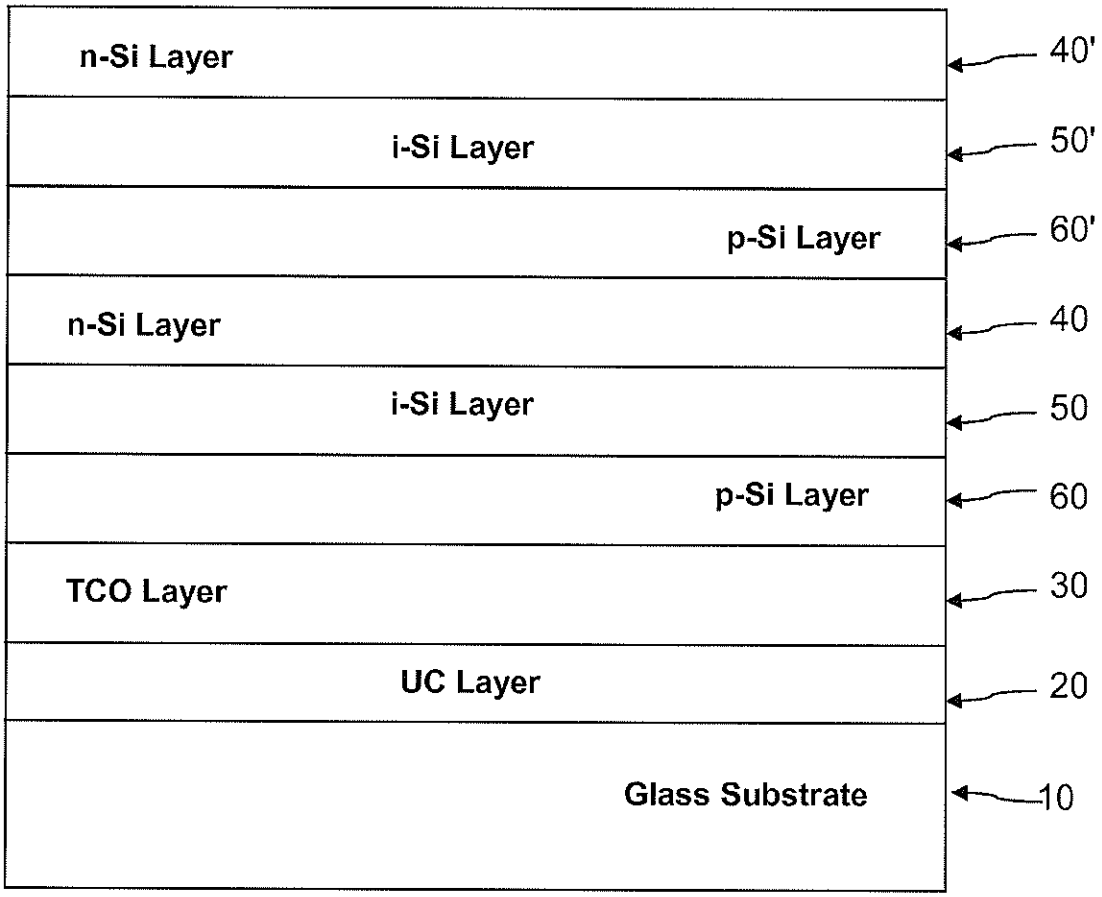

n-Si Layer
40'
i-Si Layer
50'
p-Si Layer
60'
n-Si Layer
40
i-Si Layer
50
p-Si Layer
60
TCO Layer
30
UC Layer
20
Glass Substrate
10

Figure 6

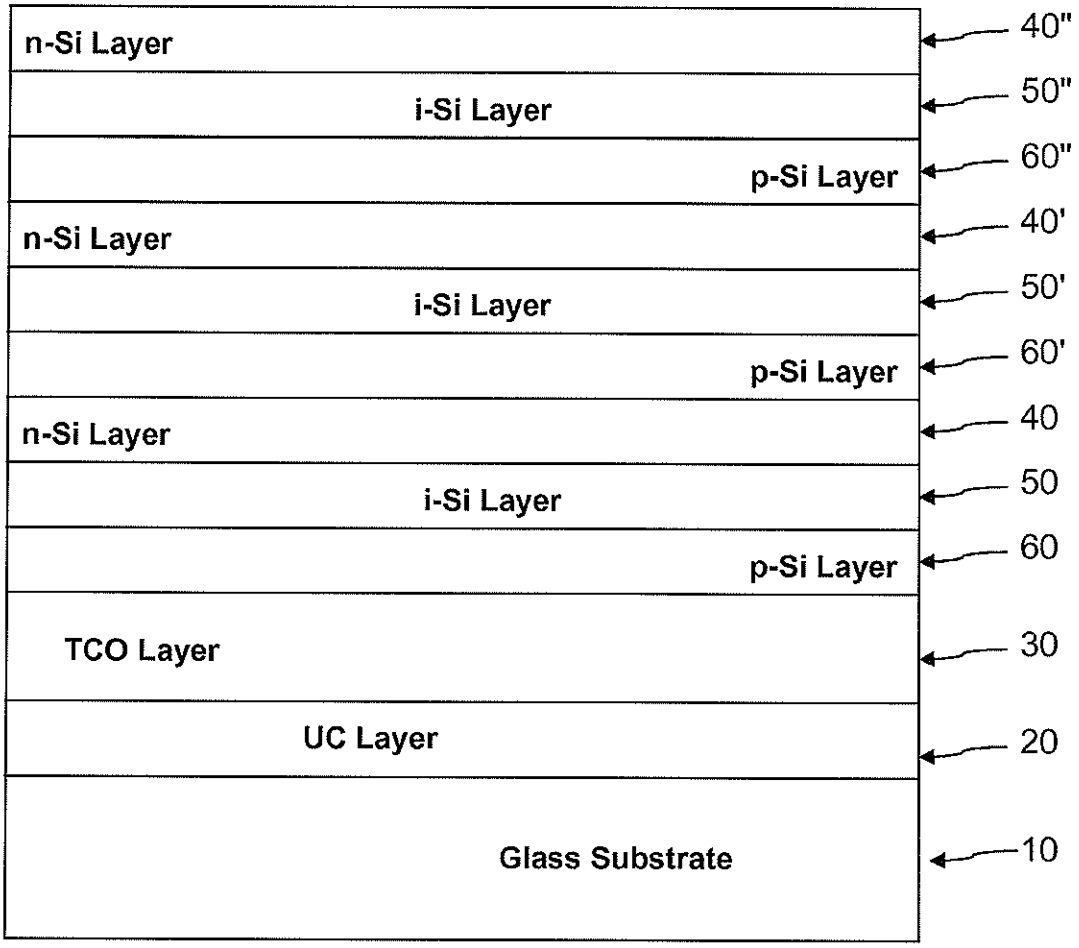

n-Si Layer
i-Si Layer
p-Si Layer
n-Si Layer
i-Si Layer
p-Si Layer
n-Si Layer
i-Si Layer
p-Si Layer
TCO Layer
UC Layer
Glass Substrate
40"
50"
60"
40'
50'
60'
40
50
60
30
20
10

Figure 7