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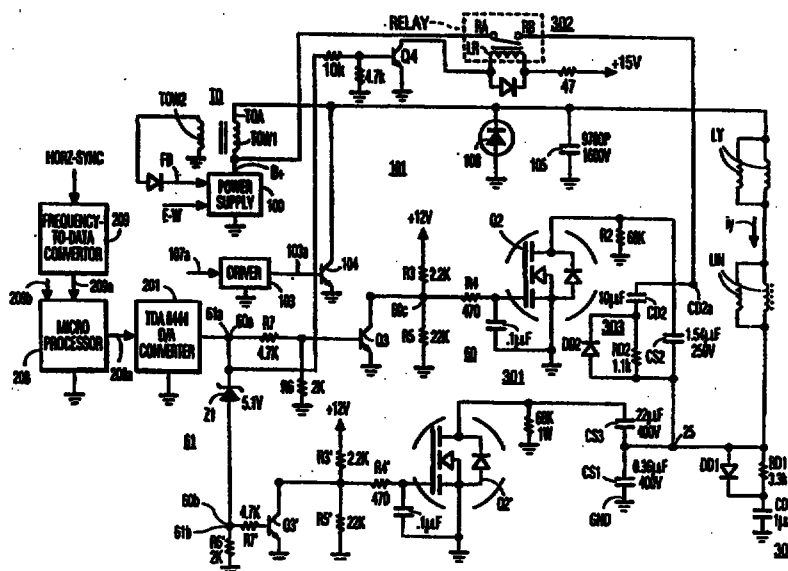
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(54) Title: DYNAMIC DAMPING CLAMPER ARRANGEMENT ASSOCIATED WITH S-SHAPING CAPACITOR



(57) Abstract

A resistor-capacitor-diode clamper is coupled to an S-capacitance of a horizontal deflection circuit output stage for reducing ringing by causing critical damping. The supply voltage of the output stage varies at a vertical rate parabolic manner for modulating the horizontal deflection current to provide East-West correction. A clamper capacitor of the clamper has a terminal that is direct current coupled to the supply voltage. Therefore, each terminal of the clamper capacitor has the same vertical rate parabolic component. Consequently, the DC voltage difference developed between the terminals of the clamper capacitor does not include any vertical rate parabolic component. The result is that the sensitivity of the damping to the supply voltage vertical rate variations is, advantageously, eliminated or reduced.