

ABSTRACT:

Digital-to-analog converter.

A digital-to-analog converter (18) having a ladder resistor portion (20) for generating a plurality of quantization signals of predetermined values based on reference signals of predetermined values based on reference signals (V_{DD} , V_{SS}) and a quantization switch portion (22) for selectively outputting one of the quantization signals
5 in accordance with a digital input value (Y) is provided. The ladder resistor portion is coupled to at least one resistor (R2, R3) for generating a signal whose value corresponds to a desired signal level of a signal to be added (SYNC). A switch means (23) is further provided for supplying the signal of the value corresponding to the desired signal level to an output of the switch portion (22) in response to a timing signal synchronized with
10 the signal to be added.

Fig. 2.

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