[CLAIMS]

[Claim 1]

A communicating apparatus comprising:

a transmitting and receiving part configured to execute transmission and reception of signals with at least one or more other communicating apparatuses through a bus, the bus including a data signal line and a clock signal line; and

an error avoiding part configured to execute a process of avoiding occurrence of a detection error,

wherein the detection error is an error that a communicating apparatus is additionally connected to the bus, but is not detected by at least one of the other communication apparatus that has initiative of communication through the bus, and

the error avoiding part is configured to cause the transmitting and receiving part to transmit a signal requesting additional connection to the bus when the error avoiding part detects that a state where potentials of the data signal line and the clock signal line are each at an H-level continues for a time period longer than a predetermined time period, using a fact that the error avoiding part detects that the potentials of the data signal line and the clock signal line are each at an H-level, as a condition for start of counting.

[Claim 2]

The communicating apparatus according to claim 1, wherein the error avoiding part is configured to execute the process of avoiding occurrence of the detection error after the bus is already configured.

[Claim 3]

A communication method comprising the steps of:

executing transmission and reception of signals with at least one or more other communicating apparatuses through a bus, the bus including a data signal line and a clock signal line;

executing a process of avoiding occurrence of a detection error, wherein the detection error is an error that a communicating apparatus is additionally connected to the bus, but is not detected by at least one of the other communication apparatus that has initiative of communication through the bus;

detecting a state where potentials of the data signal line and the clock signal line are each at an H-level continues for a time period longer than a predetermined time period, using a fact that the potentials of the data signal line and the clock signal line are each at an H-level as a condition for start of counting; and

transmitting a signal requesting additional

connection to the bus when the state is detected.

[Claim 4]

A program causing a computer to execute a process that comprises the steps of:

executing transmission and reception of signals with at least one or more other communicating apparatuses through a bus, the bus including a data signal line and a clock signal line;

executing a process of avoiding occurrence of a detection error, wherein the detection error is an error that a communicating apparatus is additionally connected to the bus, but is not detected by at least one of the other communication apparatus that has initiative of communication through the bus;

detecting a state where potentials of the data signal line and the clock signal line are each at an H-level continues for a time period longer than a predetermined time period, using a fact that the potentials of the data signal line and the clock signal line are each at an H-level as a condition for start of counting; and

transmitting a signal requesting additional connection to the bus when the state is detected.

[Claim 5]

A communication system configured to be able to

execute communication through the bus using a first communicating apparatus that has initiative of communication through the bus and a second communicating apparatus that executes communication in accordance with control by the first communicating apparatus, wherein

the first communicating apparatus and the second communicating apparatus each includes a transmitting and receiving part configured to execute transmission and reception of signals with at least one or more other communicating apparatuses through a bus, the bus including a data signal line and a clock signal line, and

at least one of the first communicating apparatus or the second communicating apparatus include an error avoiding part configured to execute a process of avoiding occurrence of a detection error,

wherein the detection error is an error that the second communicating apparatus is additionally connected to the bus, but is not detected by at least one of the other communication apparatus that has initiative of communication through the bus, and

the error avoiding part is configured to cause the transmitting and receiving part to transmit a signal requesting additional connection to the bus when the error avoiding part detects that a state where potentials of the data signal line and the clock signal line are

each at an H-level continues for a time period longer than a predetermined time period, using a fact that the error avoiding part detects that the potentials of the data signal line and the clock signal line are each at an H-level, as a condition for start of counting.