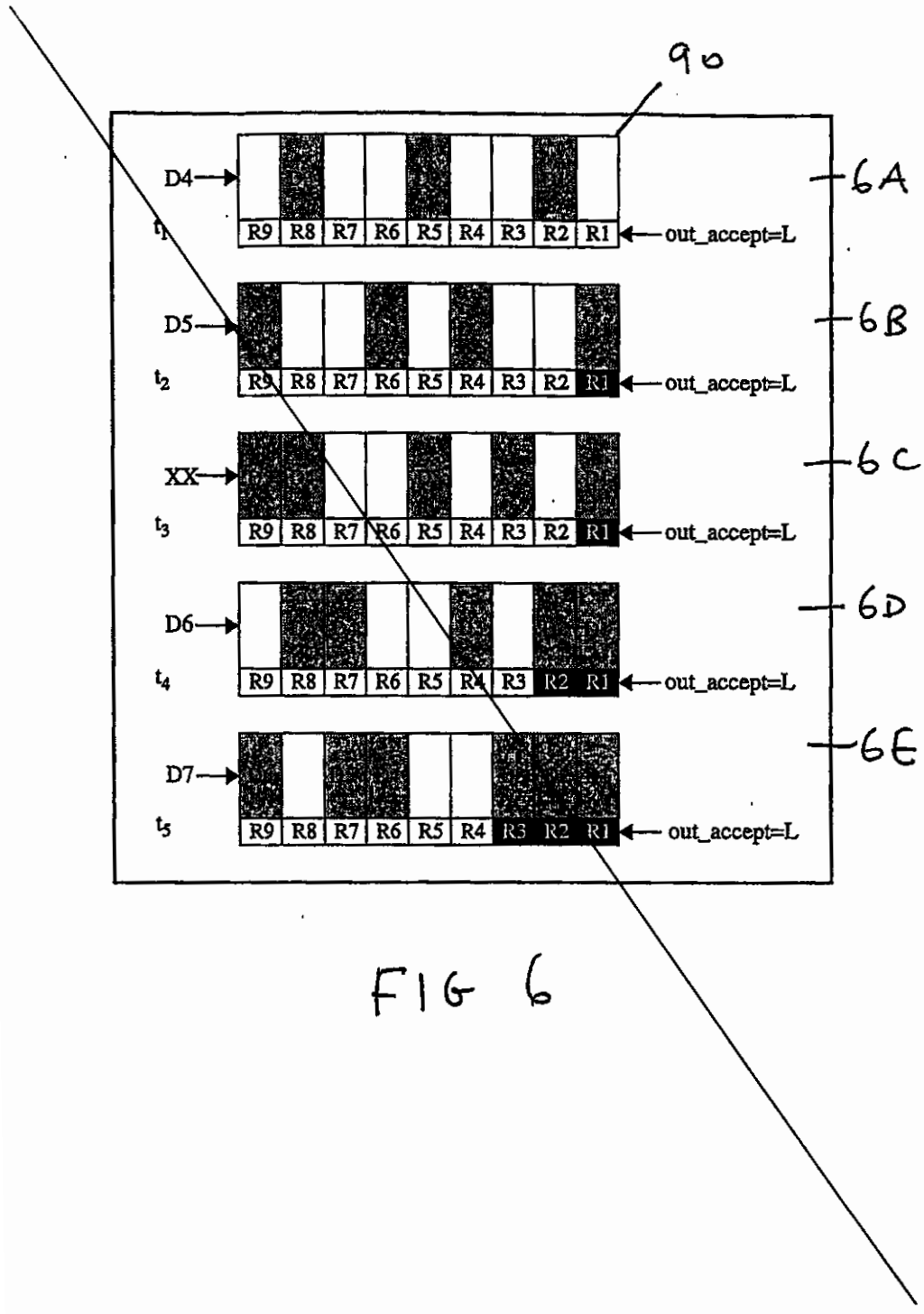
90
D4
R9 R8 R7 R6 R5 R4 R3 R2 R1
out_accept=L
6A
t1
D5
R9 R8 R7 R6 R5 R4 R3 R2 R1
out_accept=L
6B
t2
XX
R9 R8 R7 R6 R5 R4 R3 R2 R1
out_accept=L
6C
t3
D6
R9 R8 R7 R6 R5 R4 R3 R2 R1
out_accept=L
6D
t4
D7
R9 R8 R7 R6 R5 R4 R3 R2 R1
out_accept=L
6E
t5

FIG 6

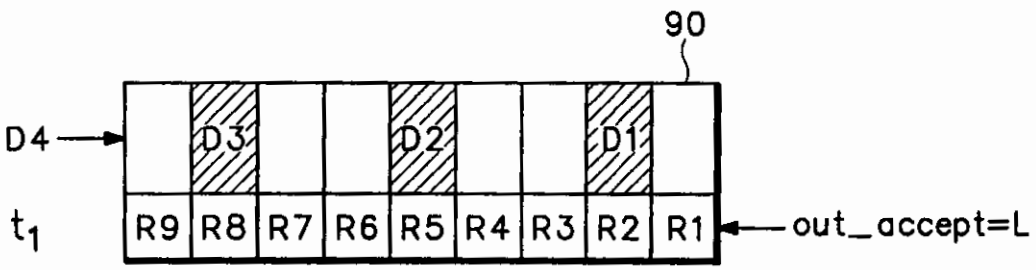

FIG.6A

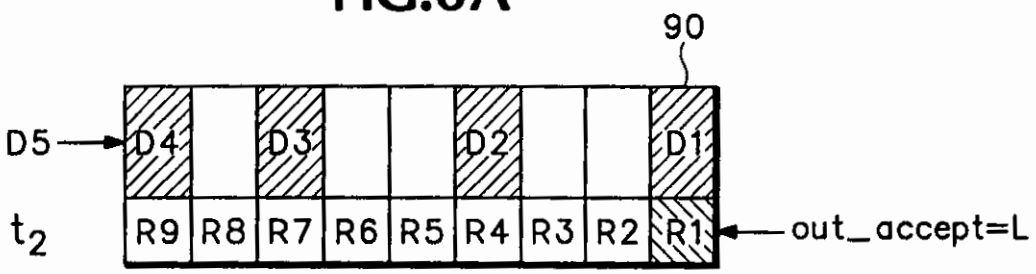

FIG.6B

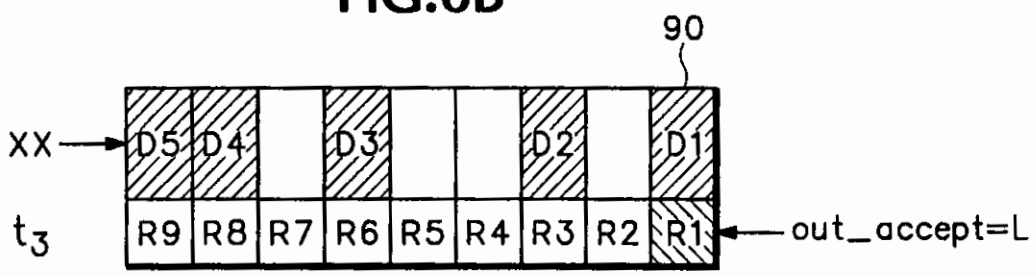

FIG.6C

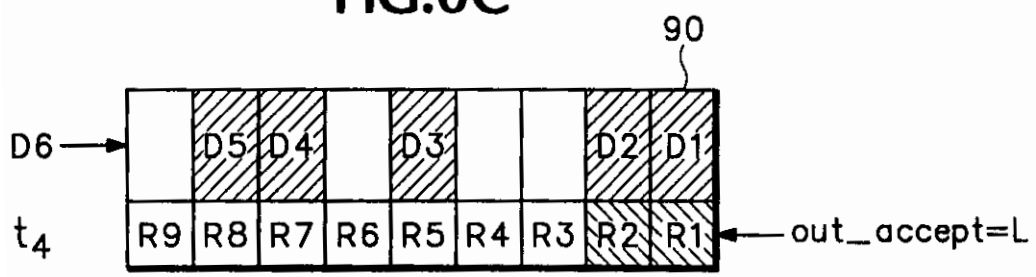

FIG.6D

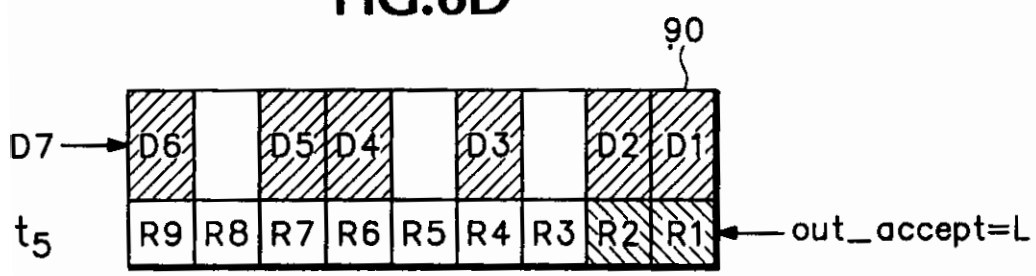

FIG.6E

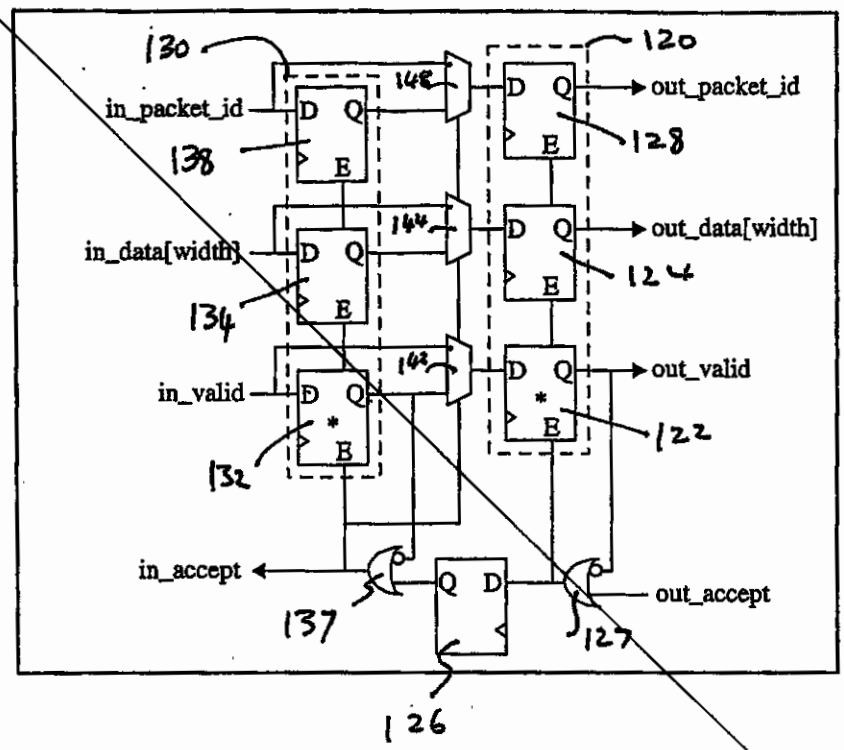
130
120
in_packet_id
D Q
E
148
138
D Q
E
out_packet_id
128
in_data[width]
D Q
E
144
134
D Q
E
out_data[width]
124
in_valid
D Q
*
E
142
132
D Q
*
E
out_valid
122
in_accept
137
Q D
out_accept
127
126

FIG 8

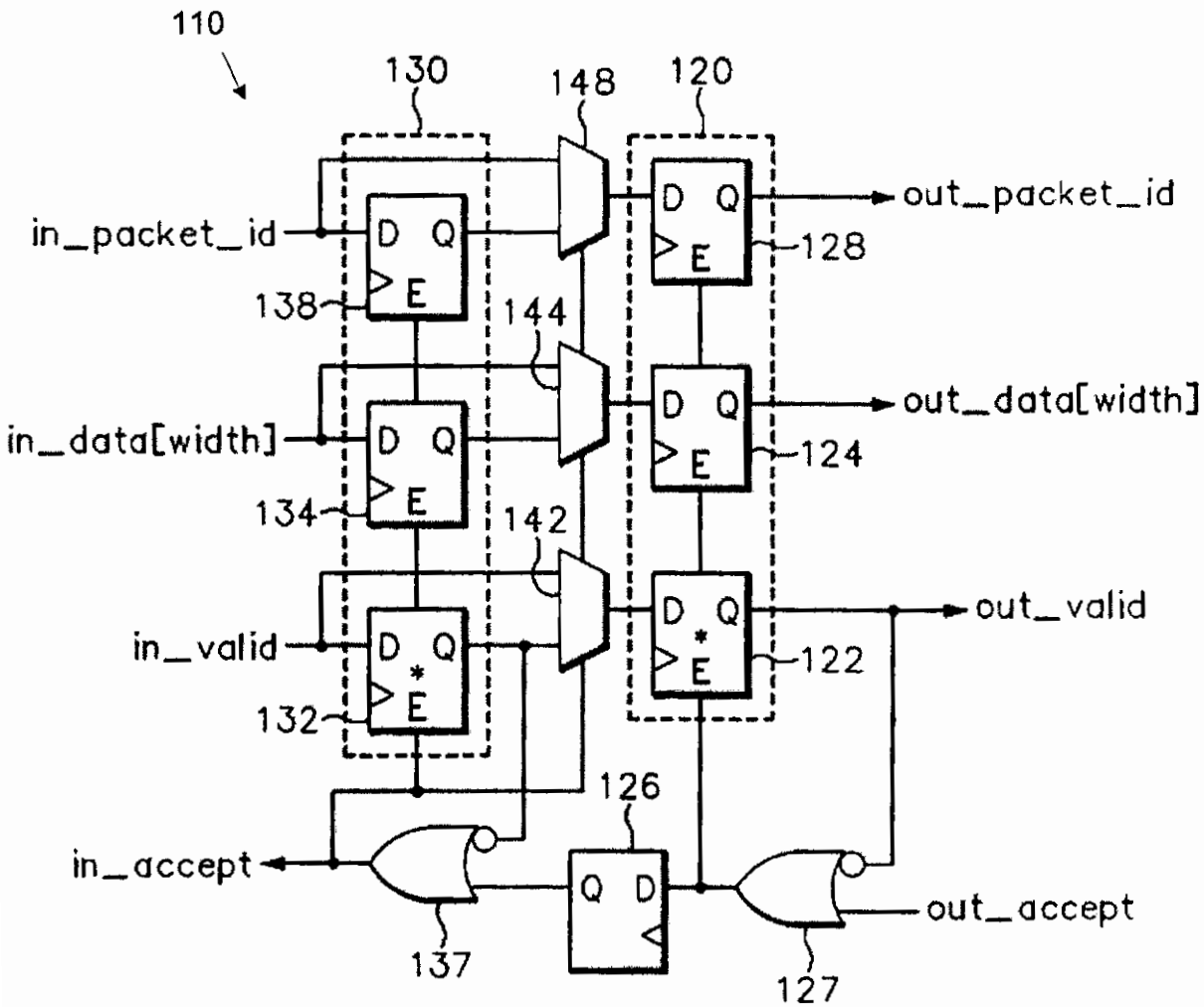
110
130
148
120
in_packet_id
138
144
in_data[width]
134
142
in_valid
132
126
in_accept
137
127
out_packet_id
128
out_data[width]
124
out_valid
122
out_accept
D Q
E

FIG.8

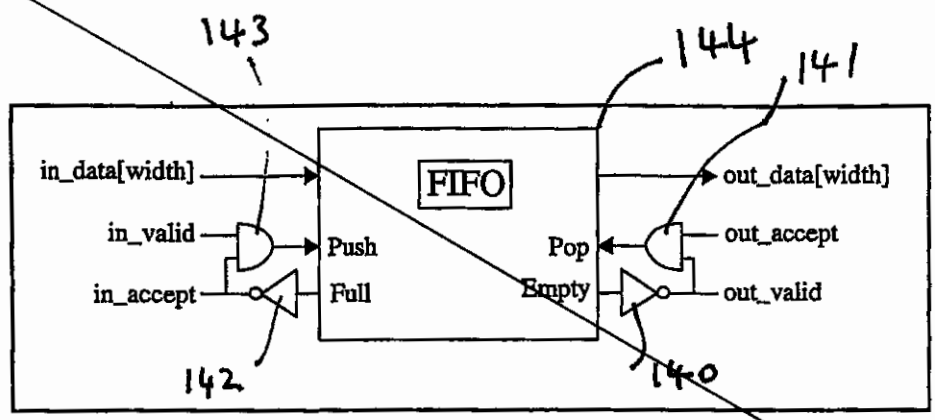
143
144
141
in_data[width]
in_valid
in_accept
FIFO
Push
Full
Pop
Empty
out_data[width]
out_accept
out_valid
142
140

FIG 12

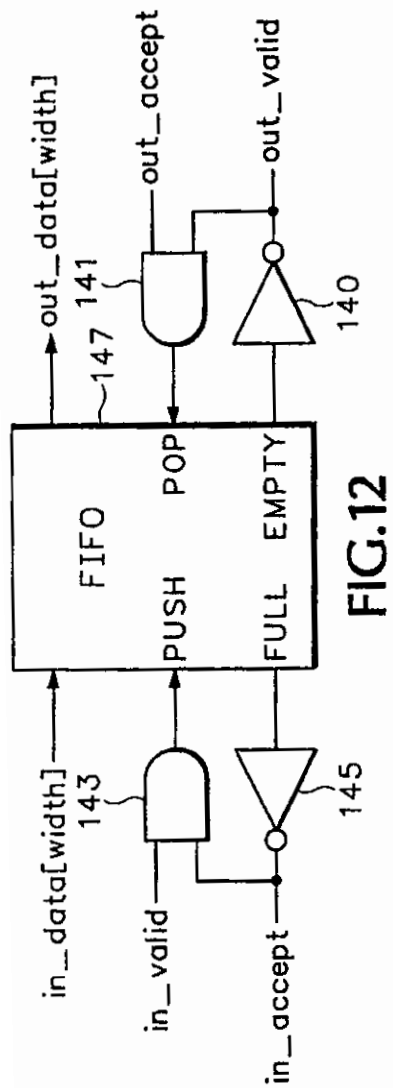
out_data[width]
147
out_accept
141
out_valid
140
FIFO
POP
PUSH
FULL
EMPTY
in_data[width]
143
in_valid
in_accept
145

FIG.12

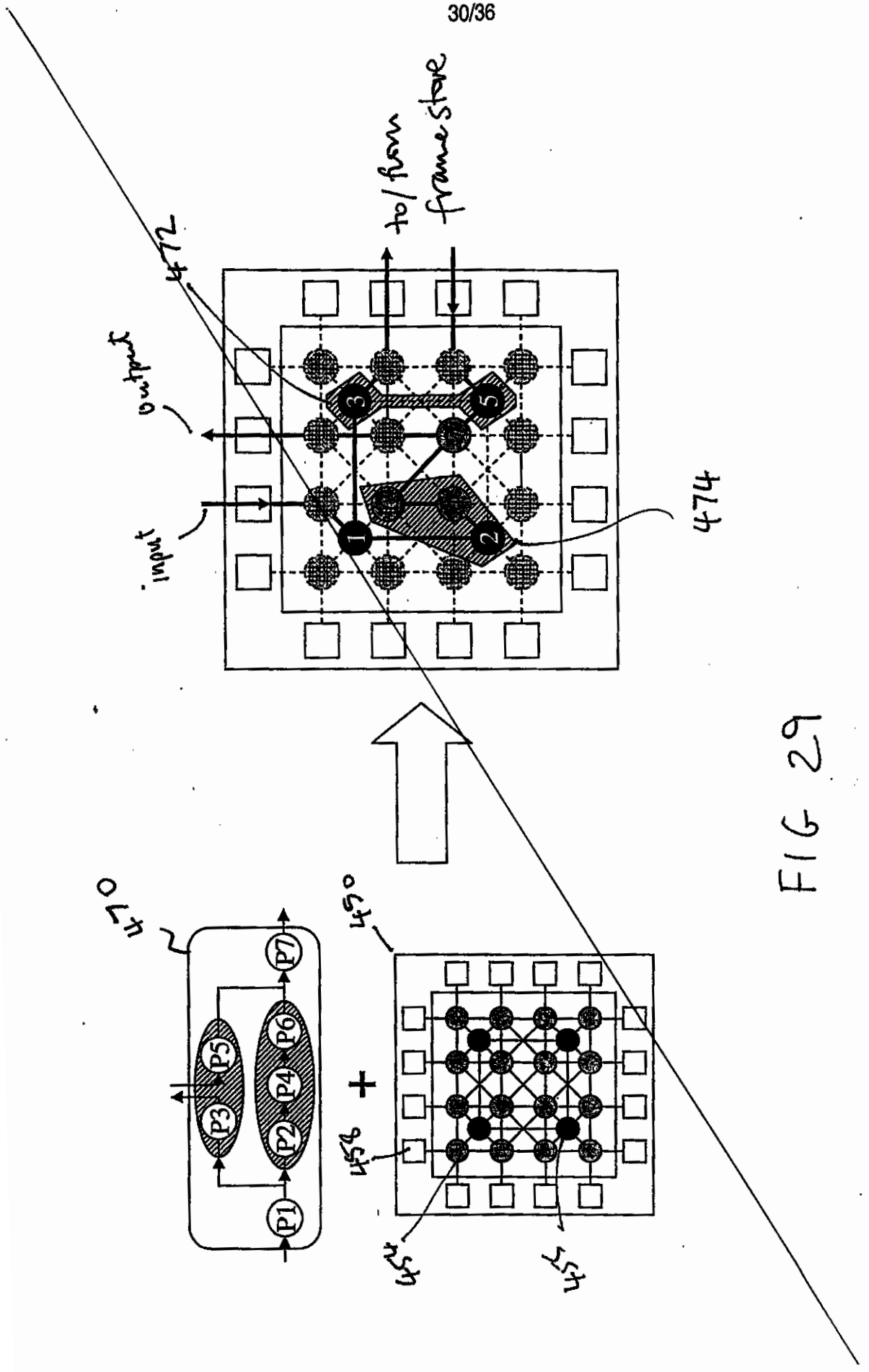
to/from
frame store
output
input
472
474
1
2
3
5
470
P1
P2
P3
P4
P5
P6
P7
450
458
454
455

FIG 29

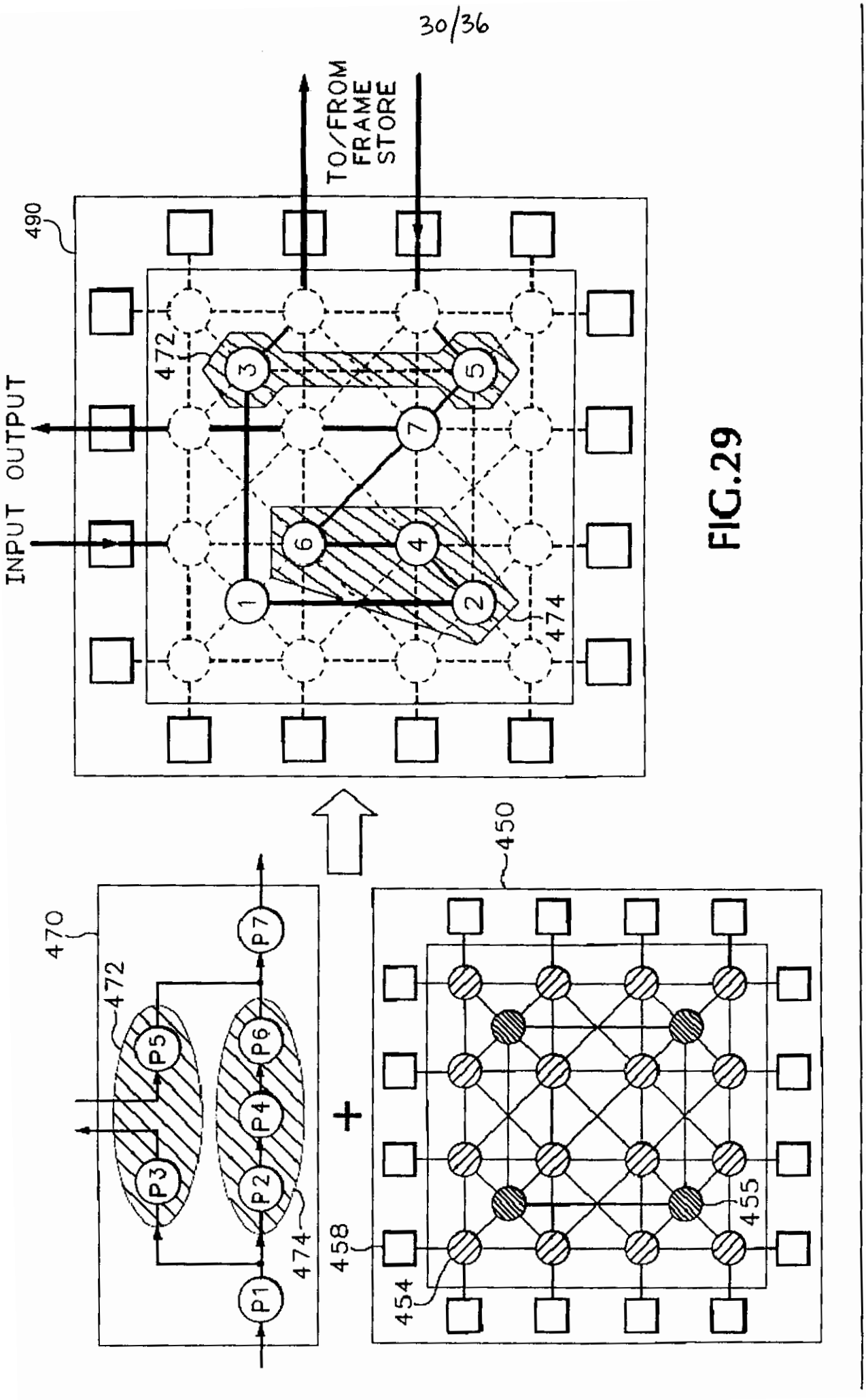
TO/FROM FRAME STORE
490
INPUT OUTPUT
472
474
1
2
3
4
5
6
7
470
P1
P2
P3
P4
P5
P6
P7
450
454
455
458

FIG.29