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(54) **Title:** RESET DYNAMIC ADDRESS TRANSLATION PROTECTION INSTRUCTION

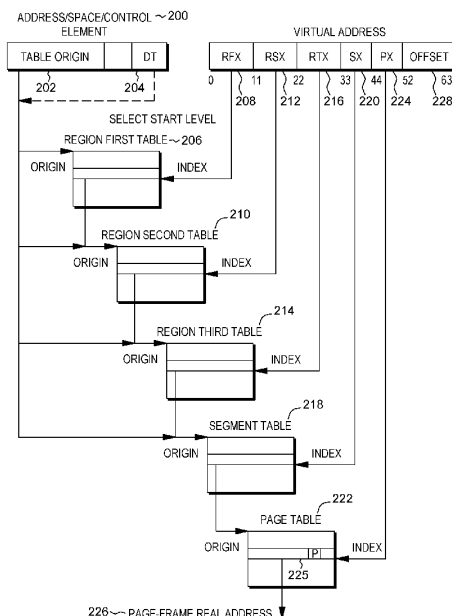


FIG. 2A

(57) **Abstract:** An instruction is provided to perform a reset address translation protection operation when executed. Executing the instruction includes determining, by a processor, that an address translation protection bit in a specified translation table entry associated with a storage block is to be reset. Based on determining that the address translation protection bit is to be reset, executing the instruction includes resetting the address translation protection bit to deactivate write protection for the storage block. The resetting is absent waiting for an action by one or more other processors of the computing environment.

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