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VOLTAGE CONTROLLED OSCILLATOR EMPLOYING NEGATIVE RESISTANCE

Abstract of the Disclosure

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A voltage controlled oscillator circuit has been provided that allows for varying the center frequency of oscillation through an applied voltage signal (V_F). Further, the center frequency of oscillation can be fine tuned around the center
10 frequency via a modulation signal (S_M).

The voltage controlled oscillator circuit utilizes a negative resistance technique for oscillation and includes an output amplifier whose output resistance can be adjusted to match the input impedance of another circuit for maximum drive capability.

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Fig. 1 accompanies this abstract.