

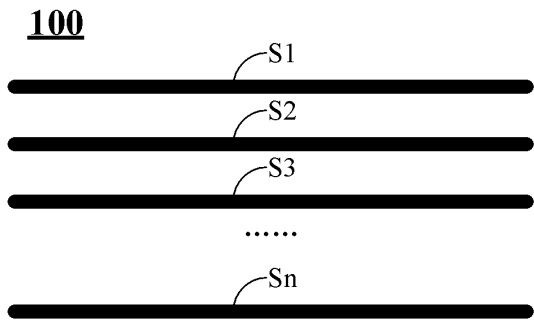


图 1

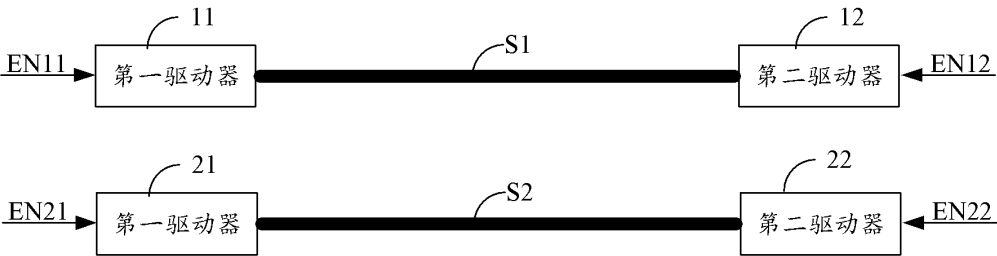


图 2

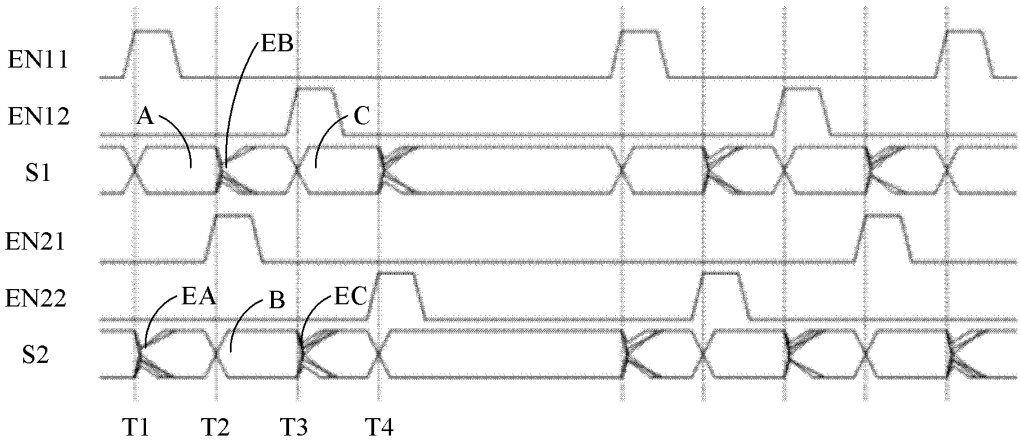


图 3A

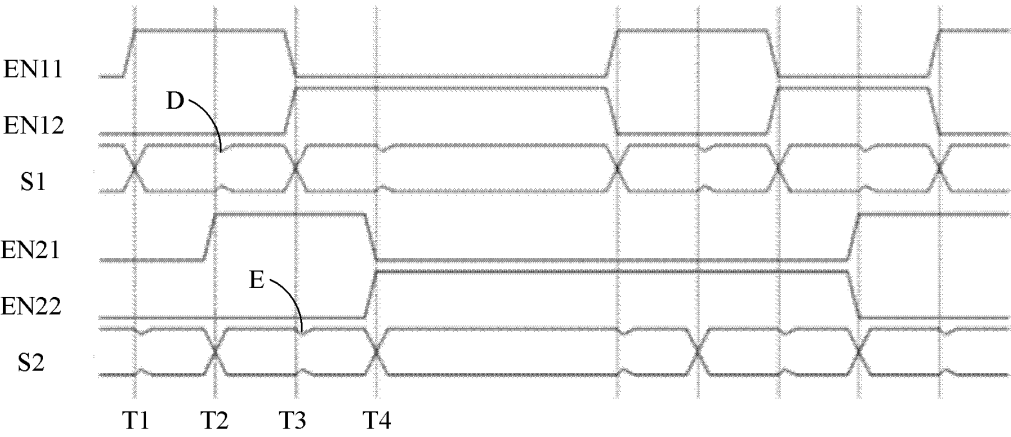


图 3B

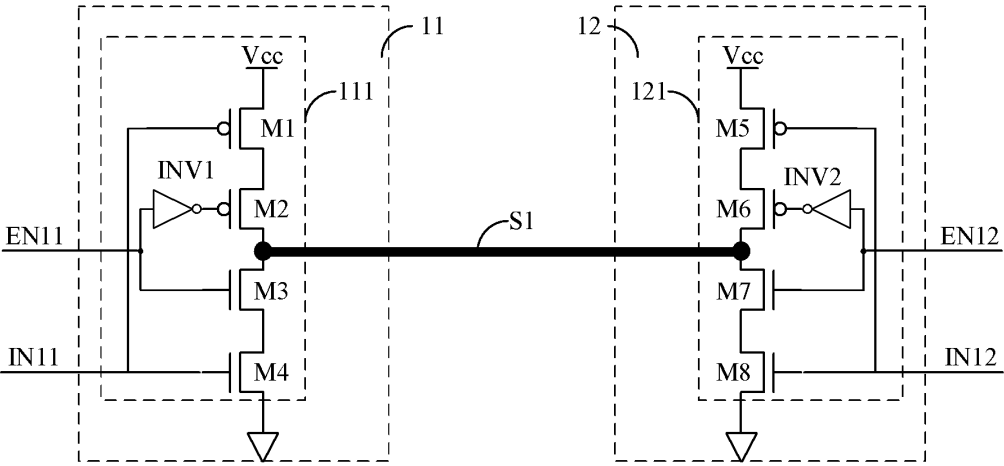


图 4

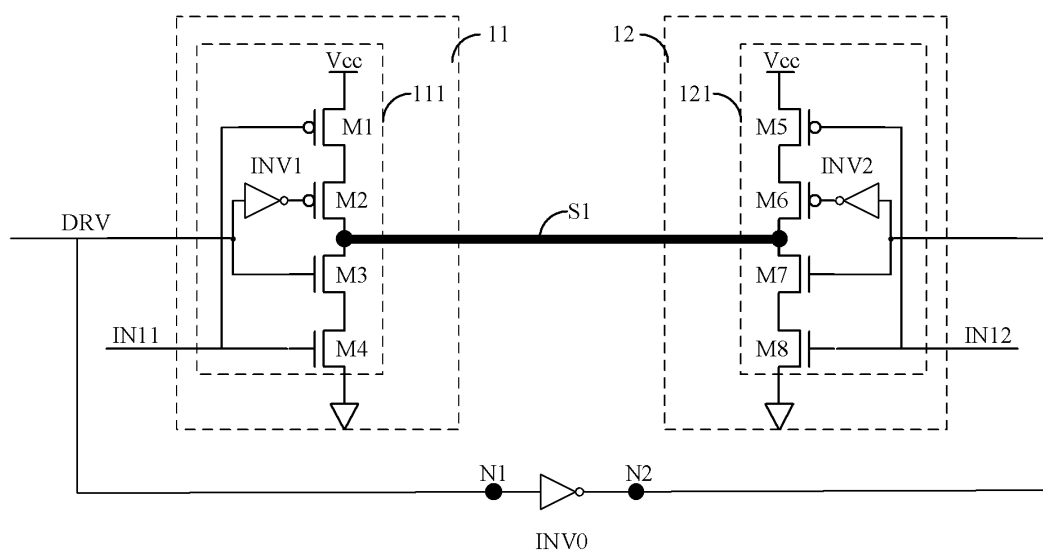


图 5

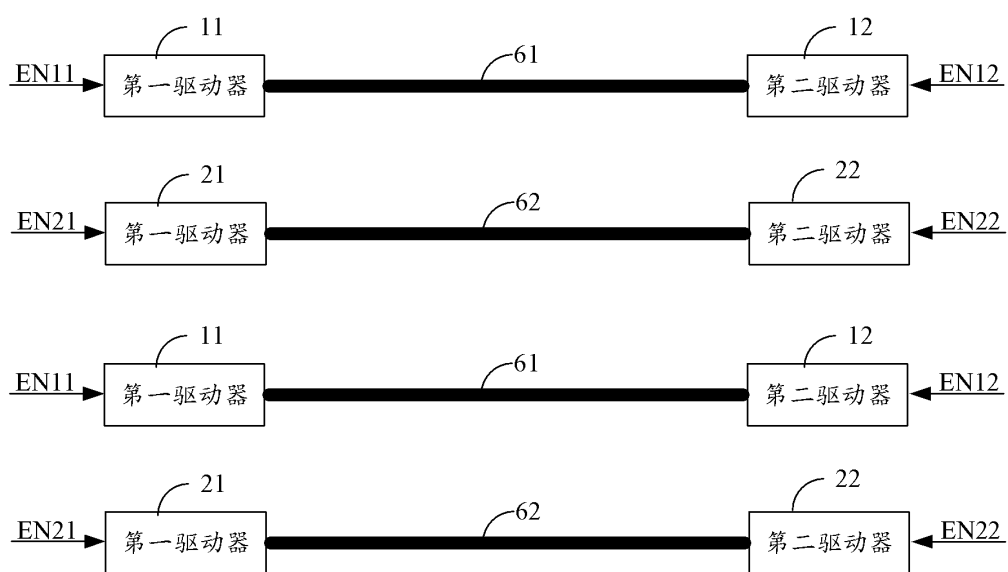


图 6

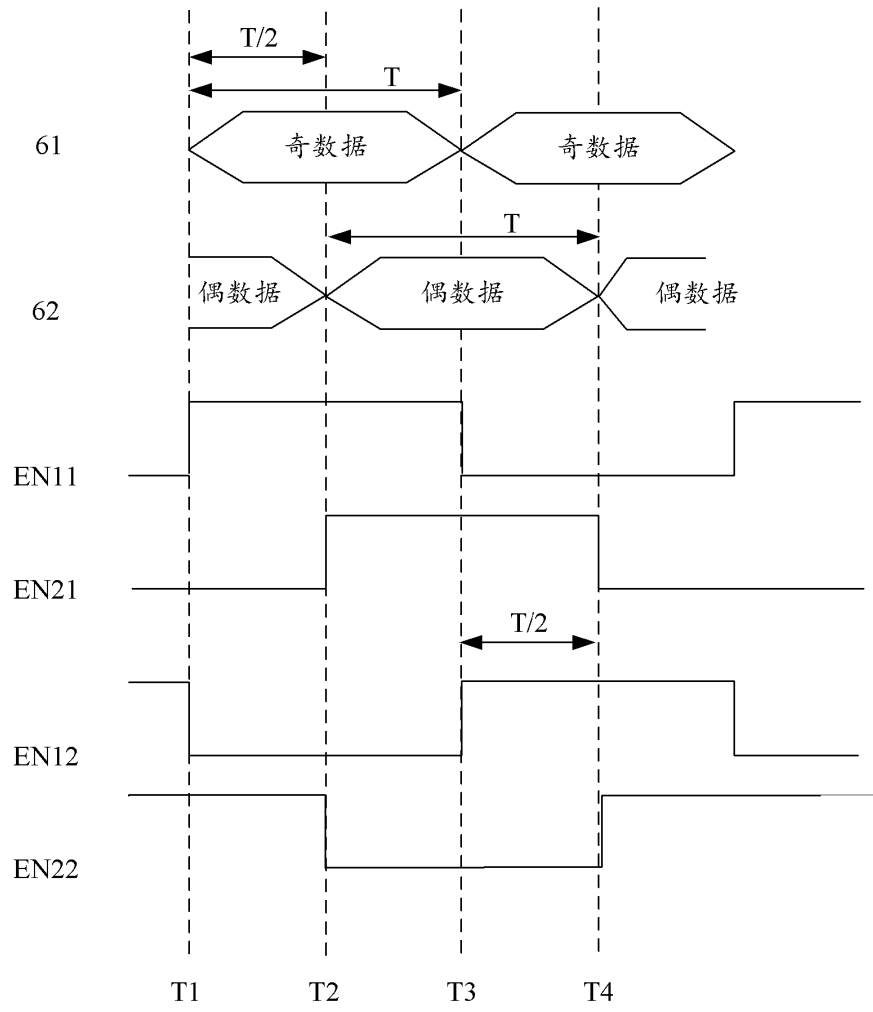


图 7

800

控制平行设置的多条信号线在任一时刻均维持驱动状态

S10

图 8

900

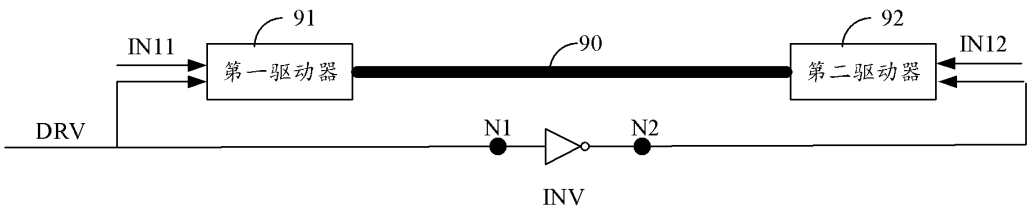


图 9