DATA SLICE CIRCUIT AND DATA SLICE METHOD

ABSTRACT

A data slice circuit 100 to reduce the effect of inter-wave interference on signals read from a storage medium and to precisely digitize the read signals.
A data slice circuit 100, for digitizing a signal read from a storage medium on which data are recorded, comprises: a peak detector 26, for detecting a peak value for an amplitude of a signal read from the storage medium; a threshold value determiner 28, for employing the peak value obtained by the peak detector to determine a compensation value that is used for compensating for the effect of inter-wave interference on the signal, and for employing the threshold value calculated by the conventional method and the compensation value to determine a compensated threshold value; and a digitization circuit 32, for digitizing the signal by using the threshold value determined by the threshold value determiner.
Fig.2