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(71) Applicant (for all designated States except US): **INTEL CORPORATION** [US/US]; 2200 Mission College Boulevard, Santa Clara, California 95052 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **SCHMIDT, Dominik** [US/US]; P.O. Box 20541, Stanford, California 94309 (US).

(74) Agents: **VINCENT, Lester J.** et al.; Blakely Sokoloff Taylor & Zafman, 12400 Wilshire Boulevard 7th Floor, Los Angeles, California 90025 (US).

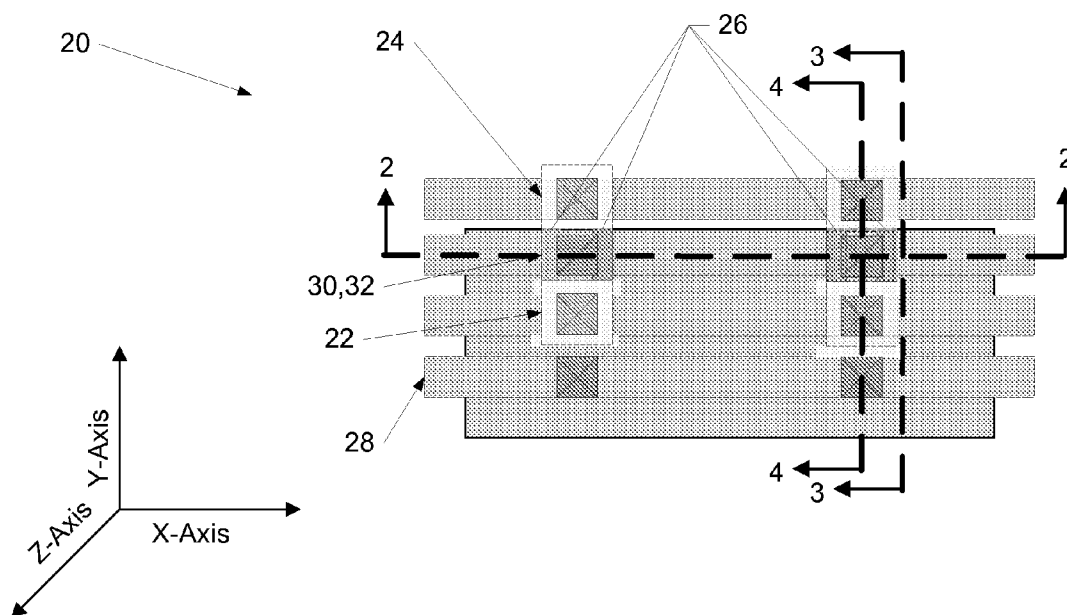
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(54) Title: DEVICE AND METHOD OF MANUFACTURE FOR A LOW NOISE JUNCTION FIELD EFFECT TRANSISTOR



(57) Abstract: A microelectronic product and the method for manufacturing the product are provided. A source and drain are spaced from one another in a first direction and are connected to opposing ends of a channel to provide a set voltage. First and second gates are spaced from one another in a second direction surrounding a portion of the channel to allow for application and removal of a gate voltage. Application of the gate voltage repels majority carriers in the channel to reduce the current that conducts between the source and drain.

WO 2008/034026 A1