

Abstract of the Disclosure

A physically realizable one terabit or more ATM packet switch 10A that has a large number of input interfaces connected to a single stage switching fabric 14A which is in turn connected to a number of output modules 16_0-16_m , according to the generic growable packet switch architecture. This ATM packet switch 10A is different from other growable packet switches in that it has a single stage switch fabric (14A or 14B) controlled by an out-of-band controller 20, yet it has significantly reduced complexity with respect to comparably sized electronic crossbar switches or their isomorphs. This ATM packet switch architecture is so flexible, it can be extended to provide variable length packets, circuit switched connections and fault tolerant redundant circuits using the same switch fabric and out-of-band controller architectures.

This Sheet Has Been Replaced

