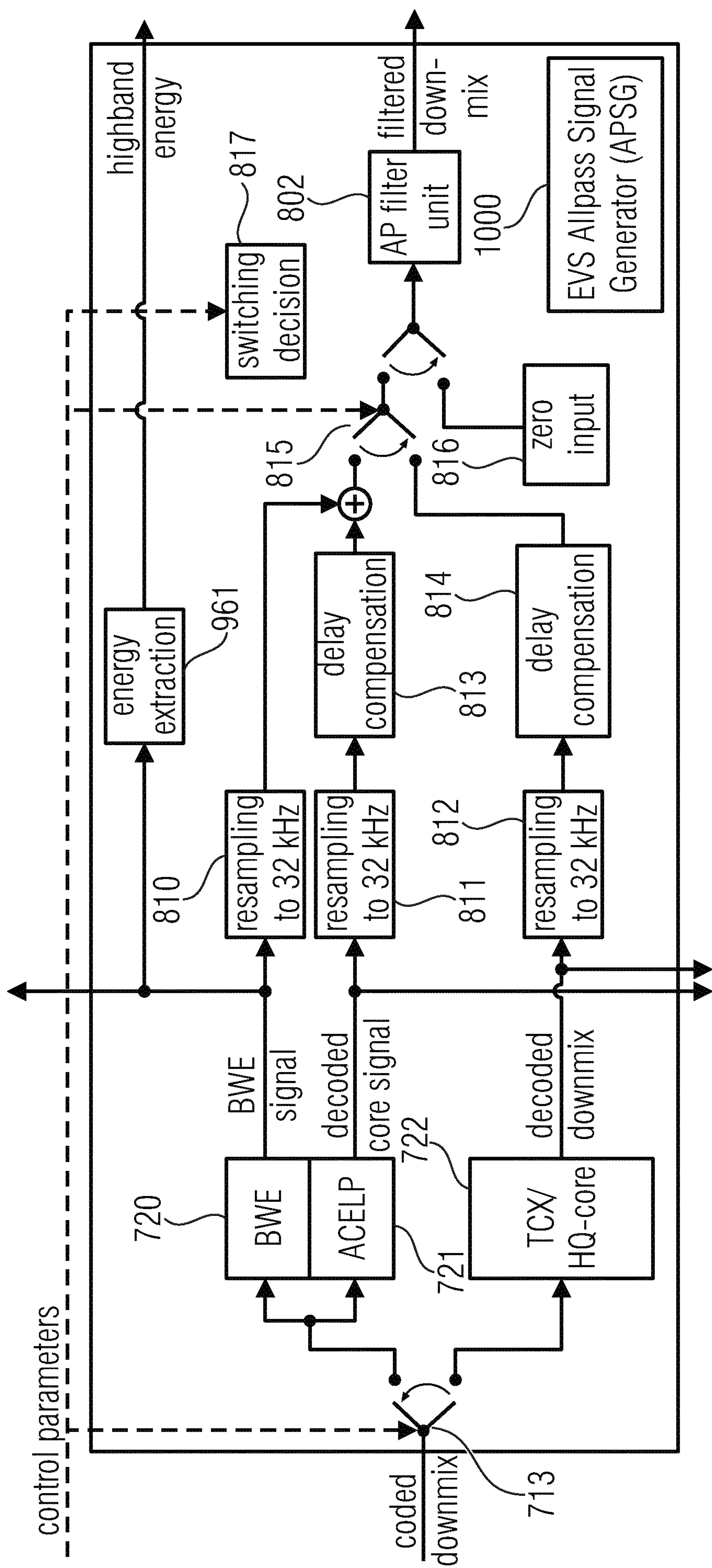




Artificial signal generation when used with EVS core coder
Fig. 1a

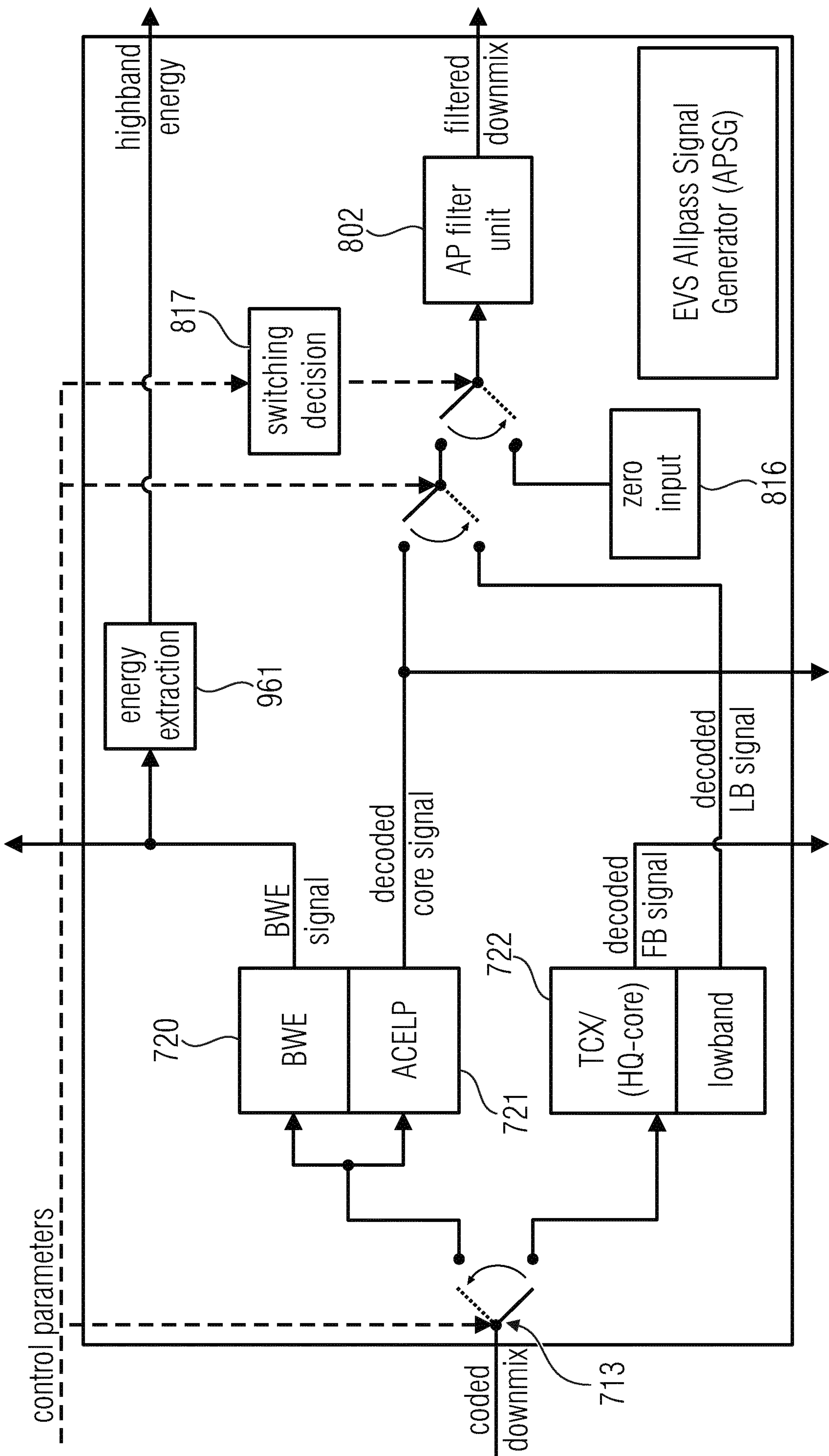
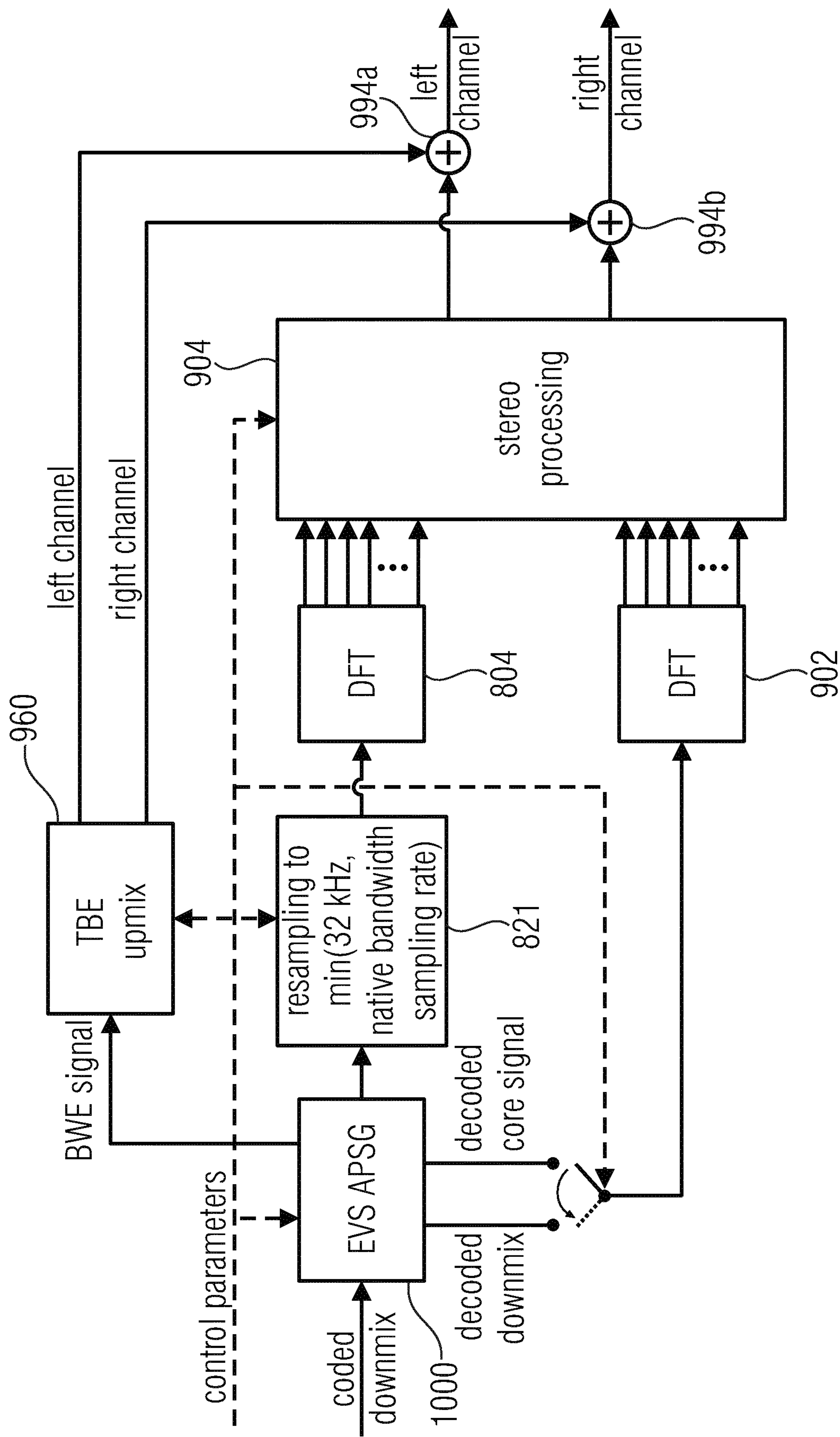


Fig. 1b



Integration into DFT stereo processing including time domain BWE upmix

Fig. 2a

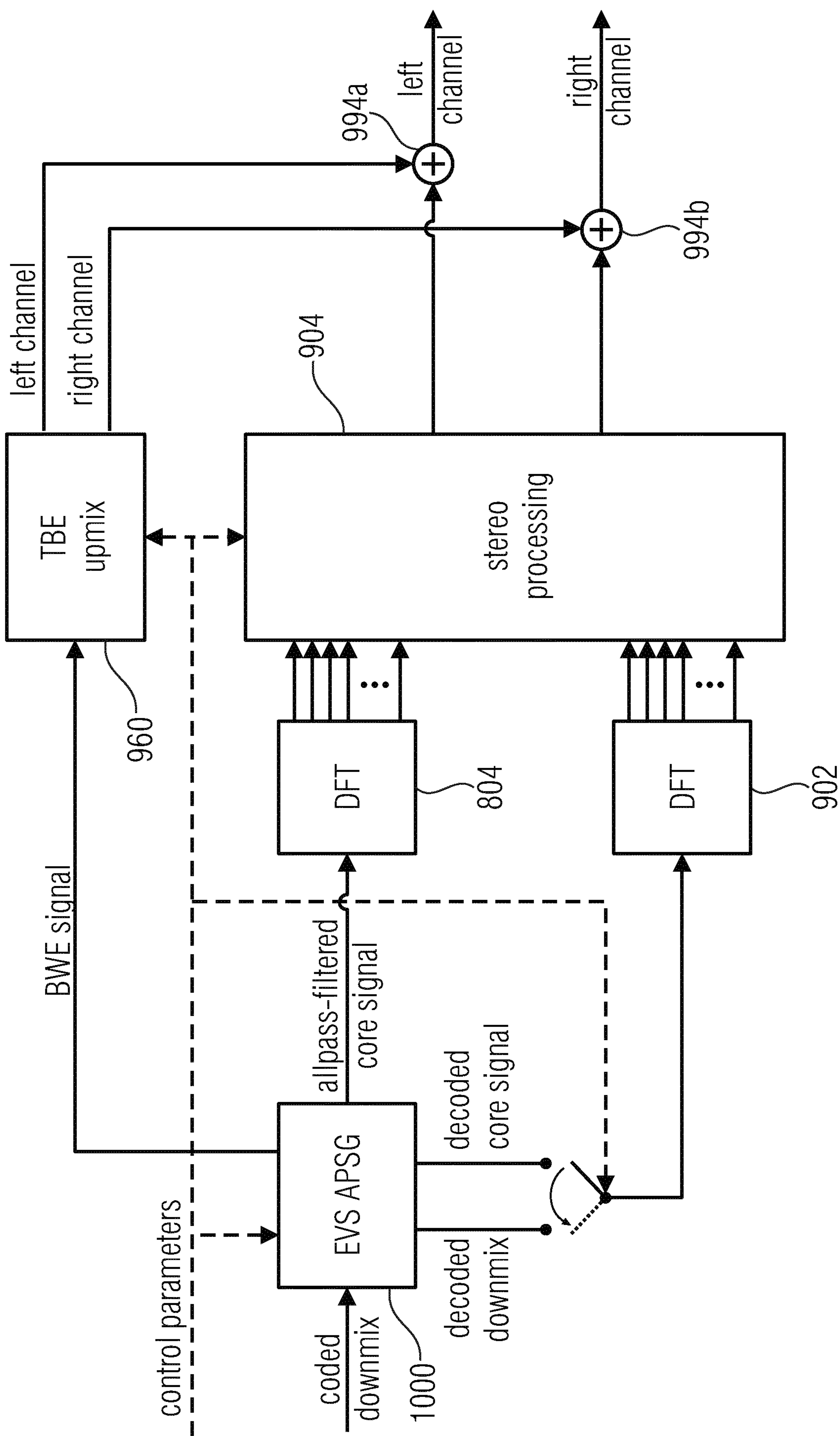
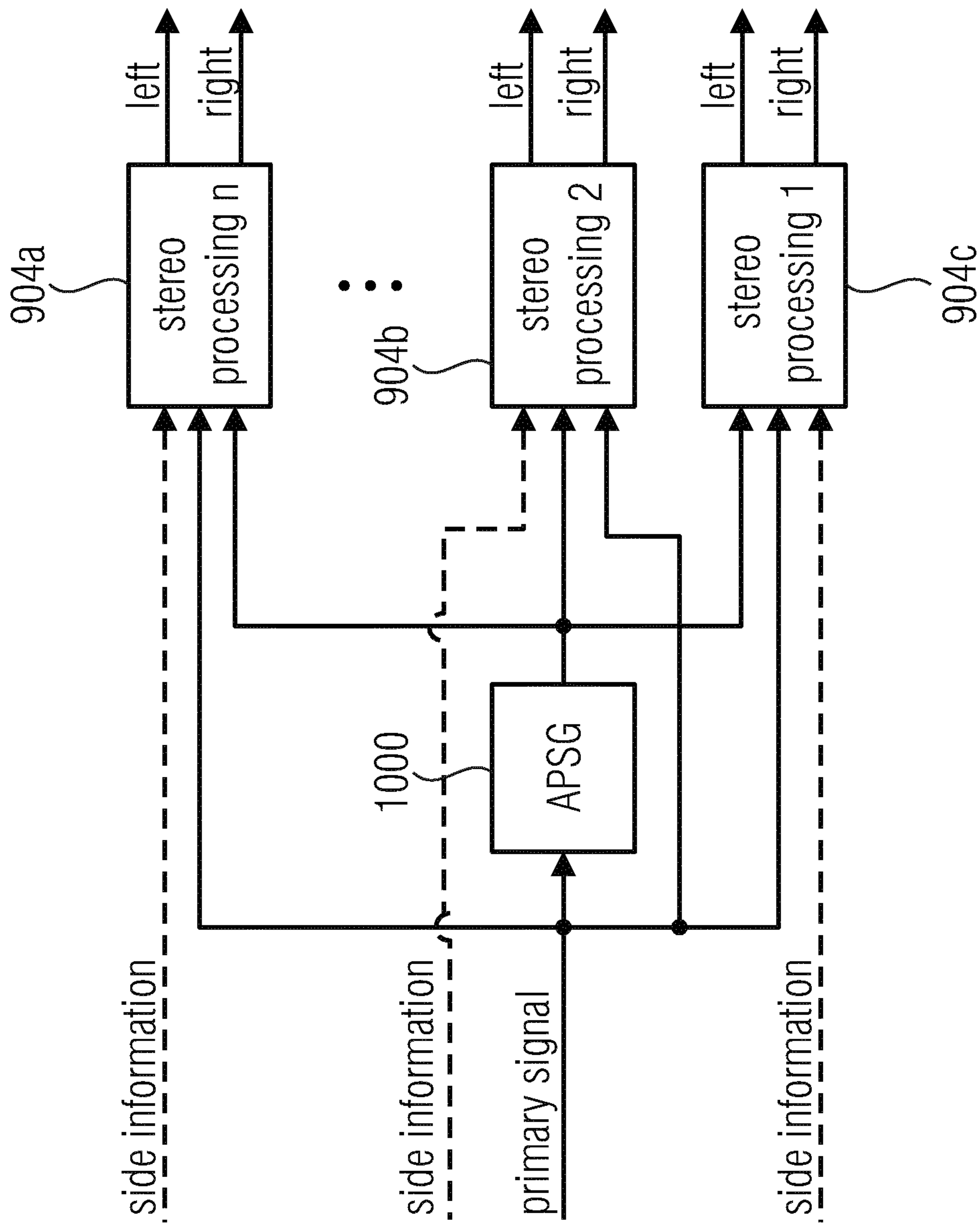
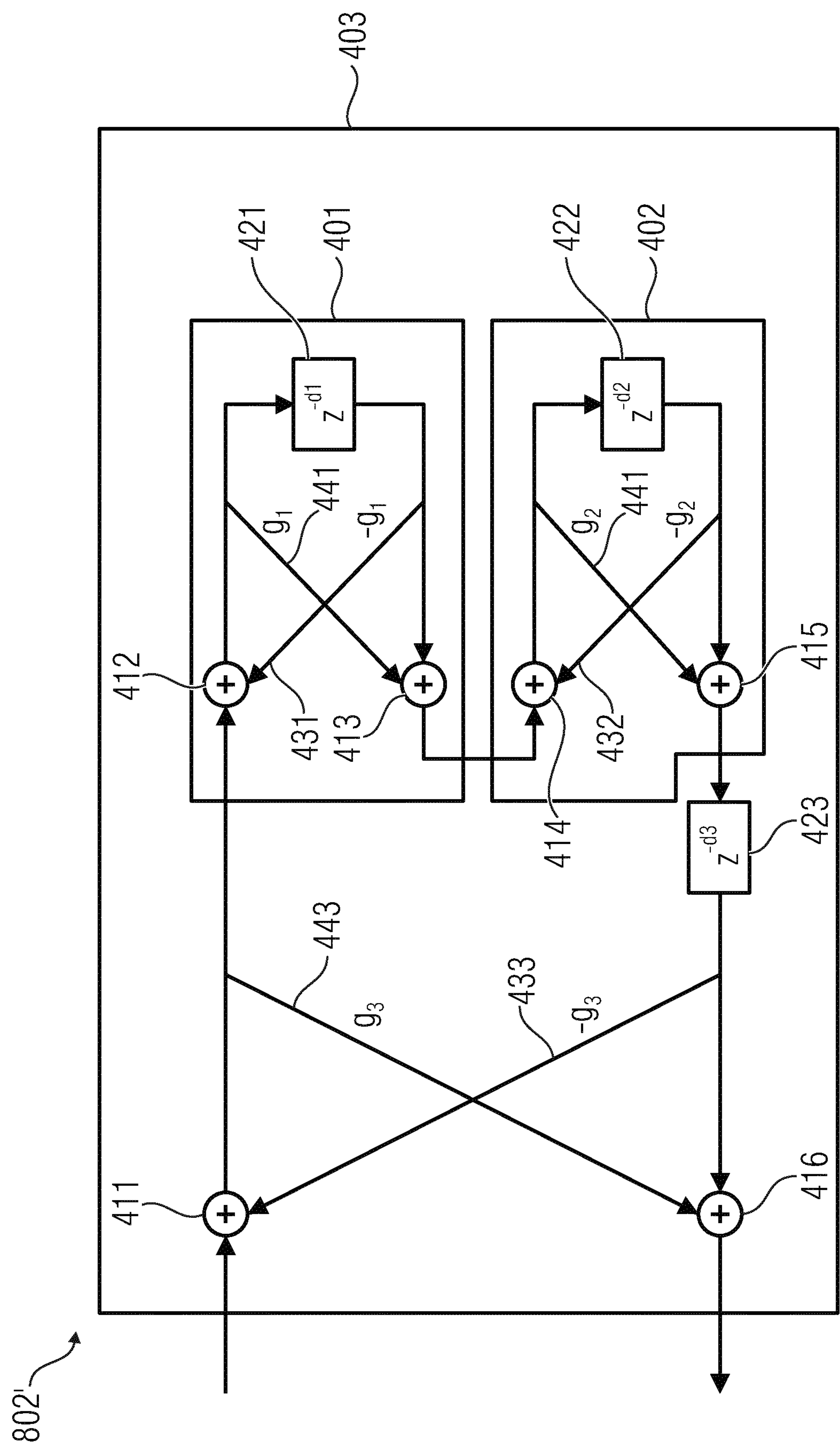


Fig. 2b

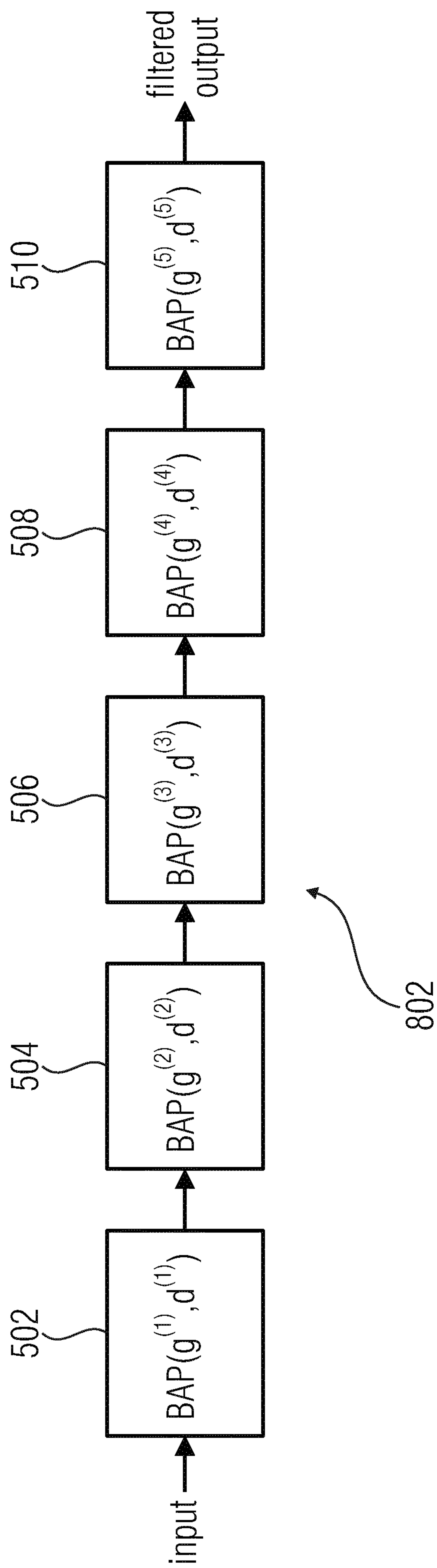


Integration into a system featuring multiple stereo processing units

Fig. 3

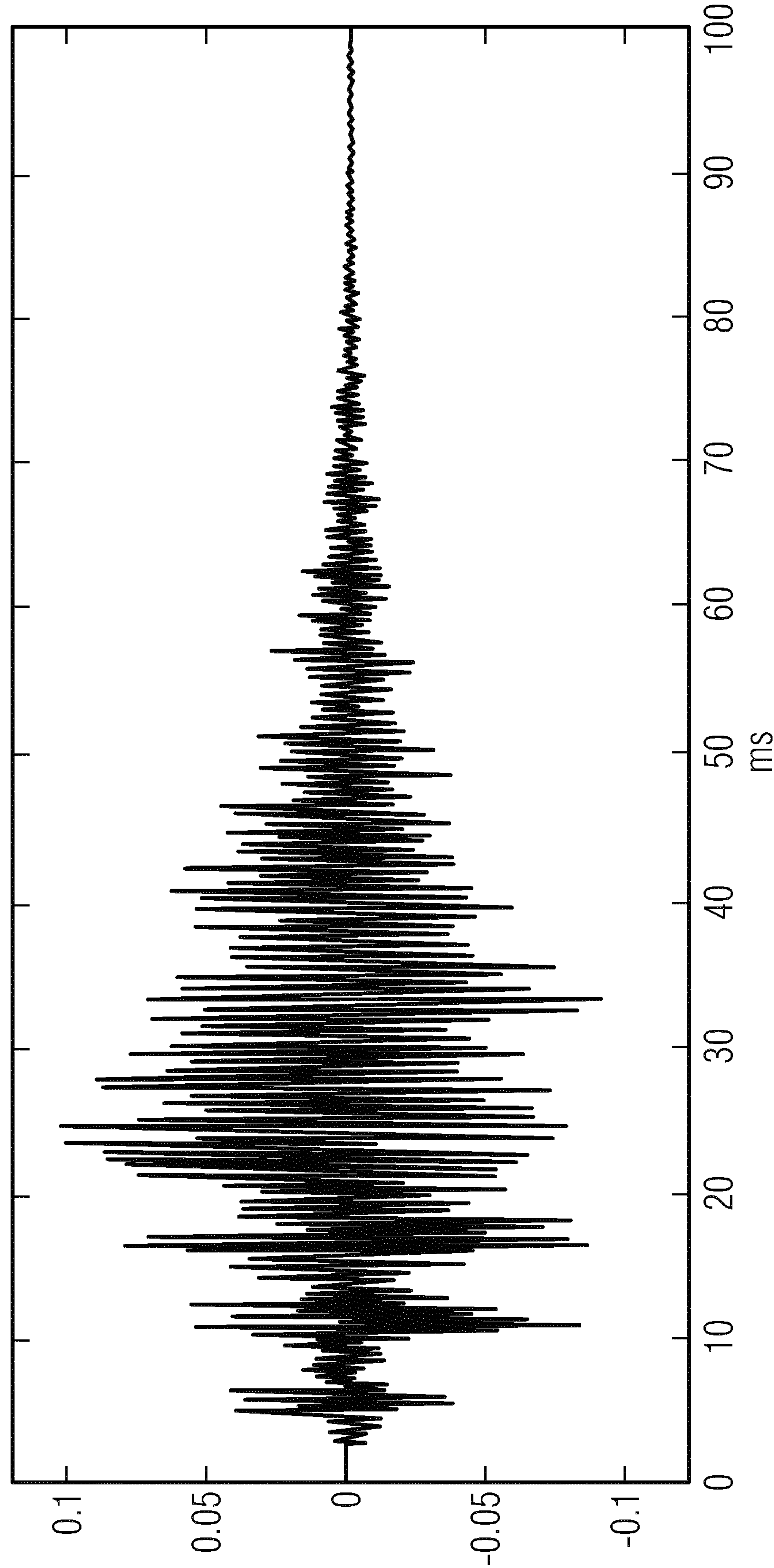


Basic allpass unit
Fig. 4



AP filter unit

Fig. 5



Impulse response of allpass Filter

Fig. 6

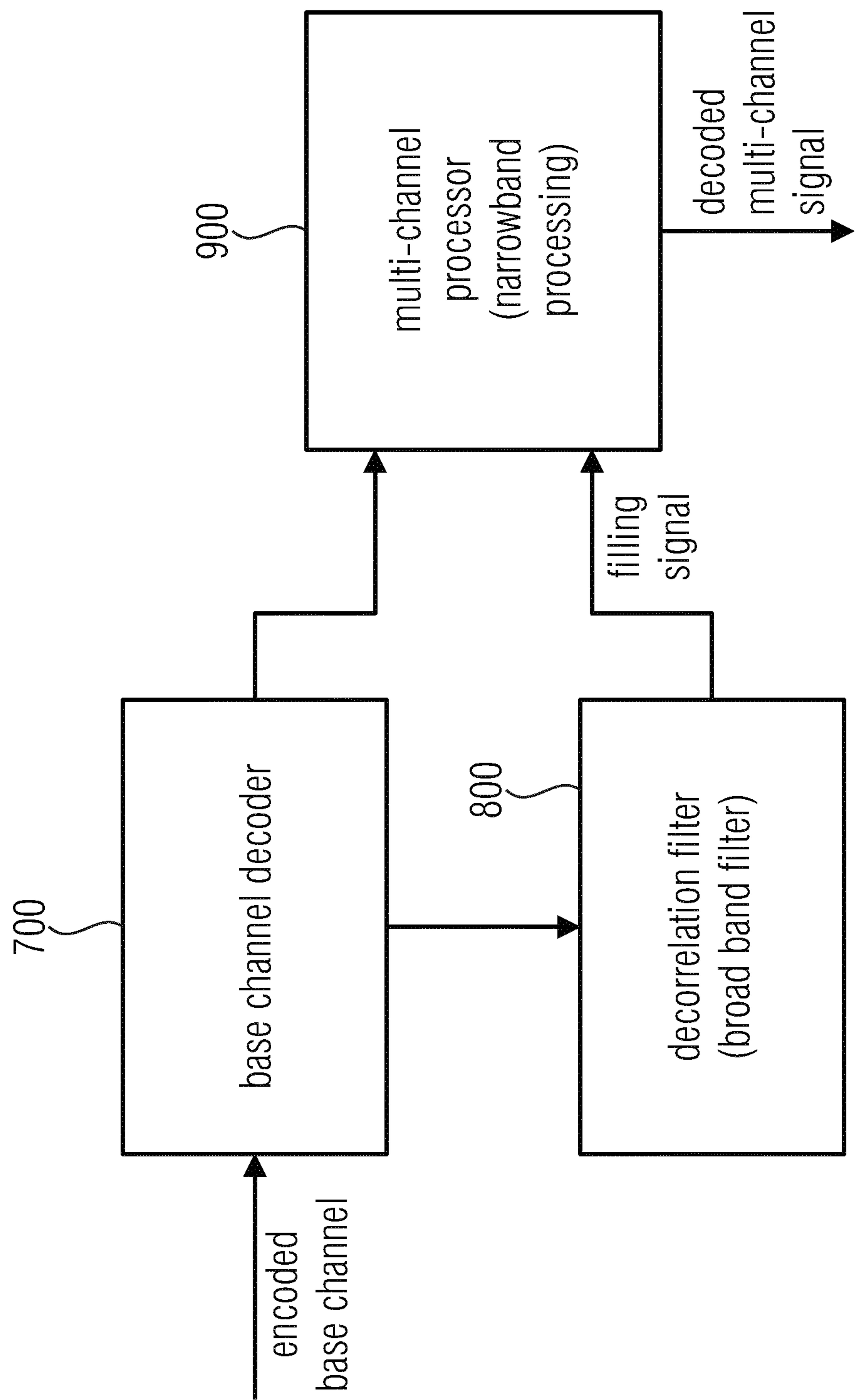


Fig. 7a

10/18

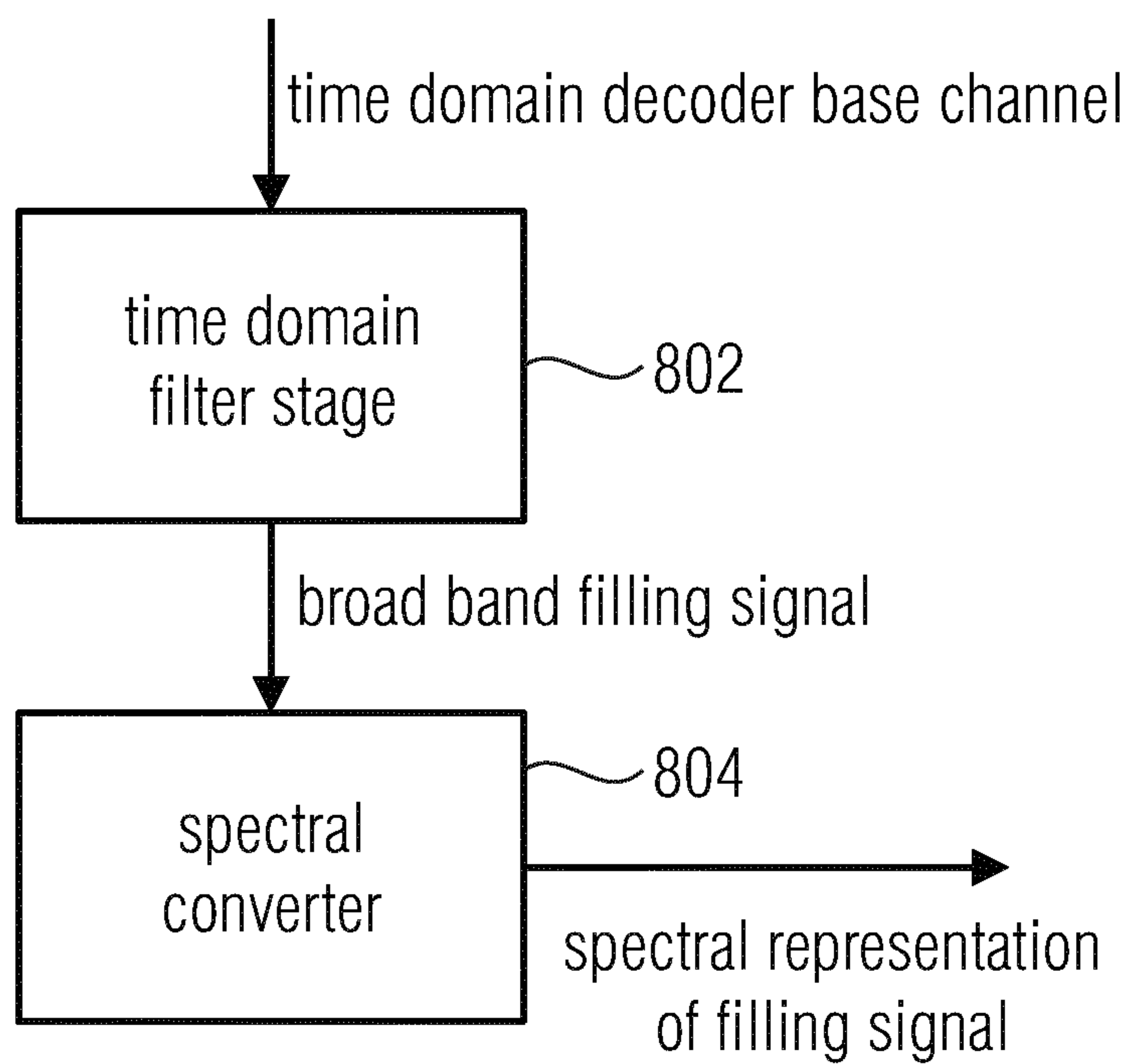


Fig. 7b

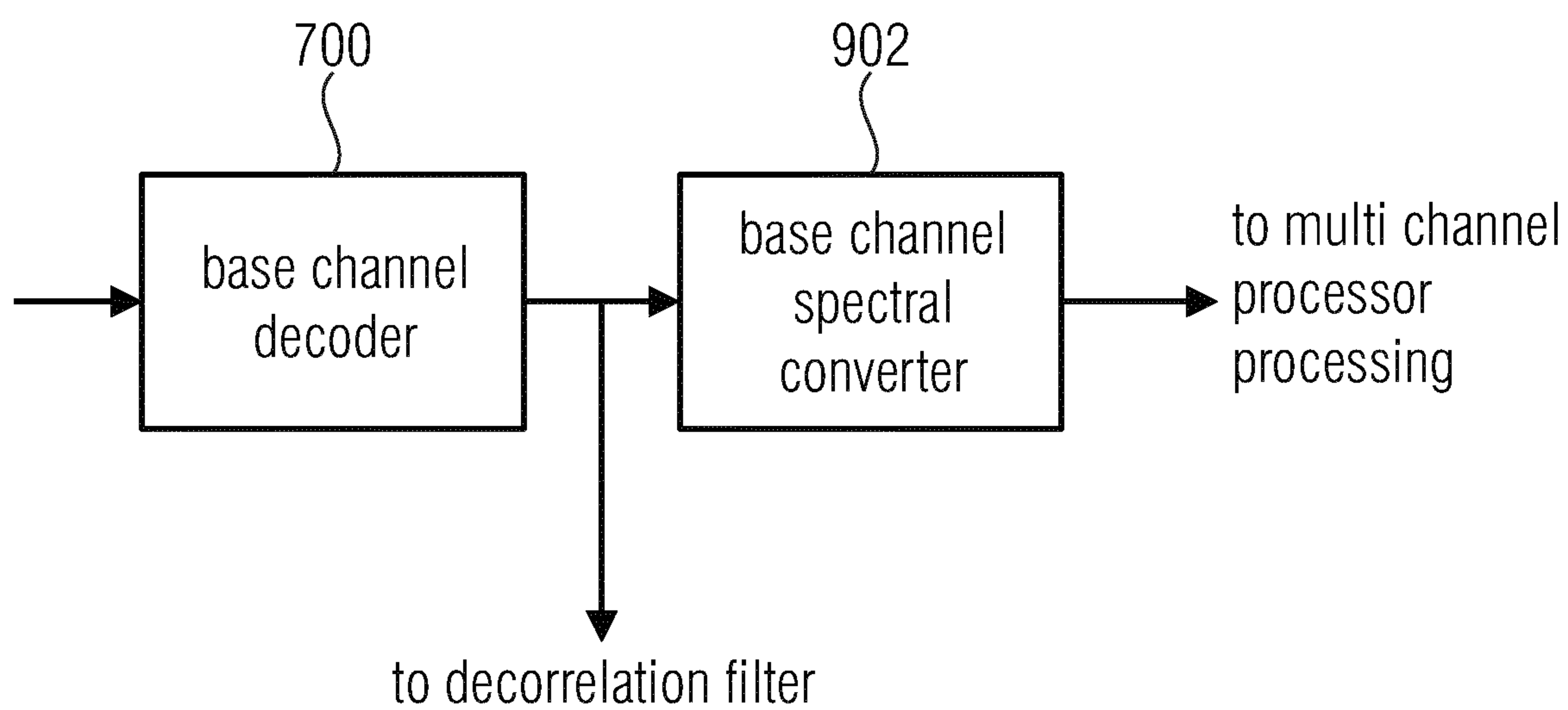


Fig. 7c

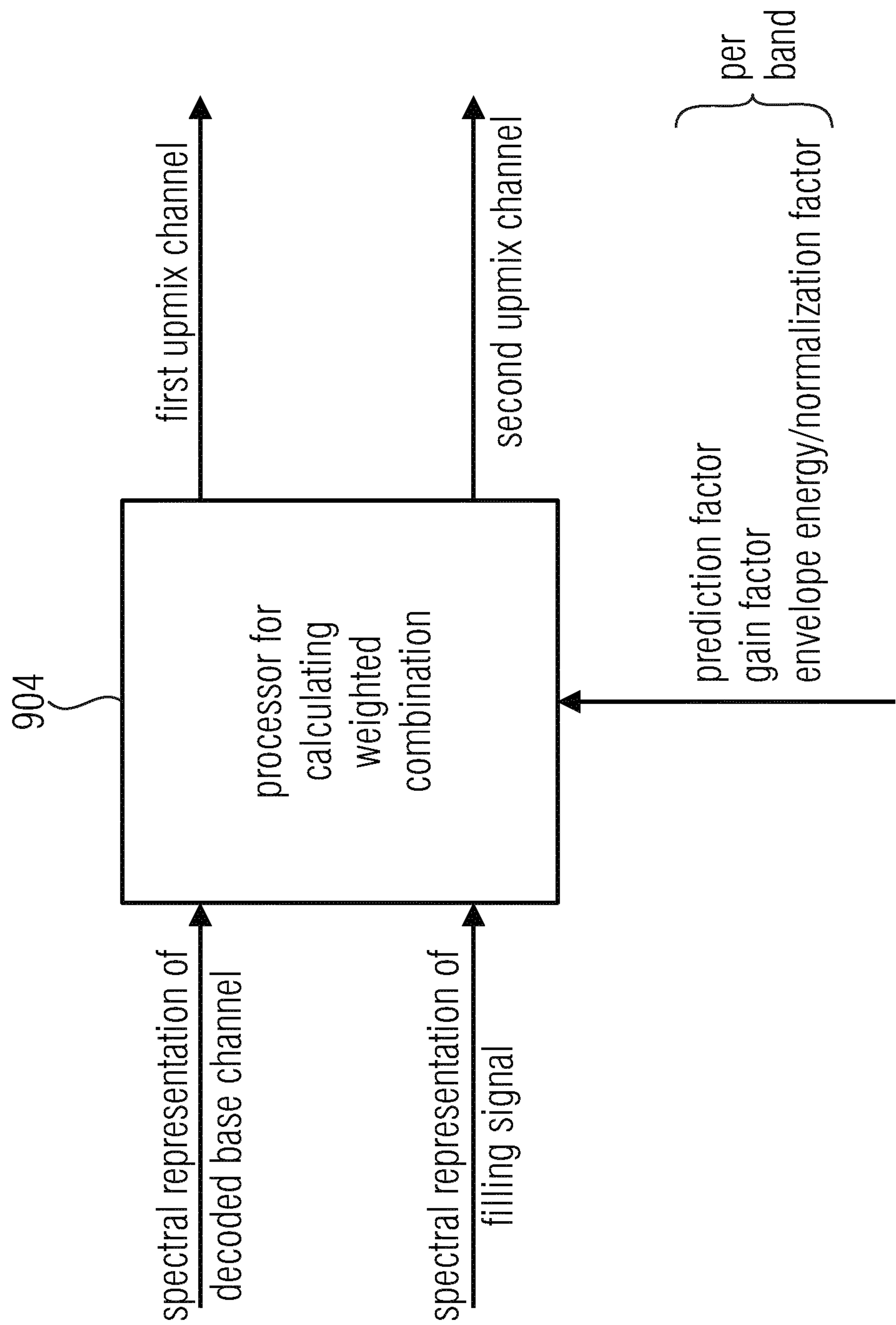


Fig. 8

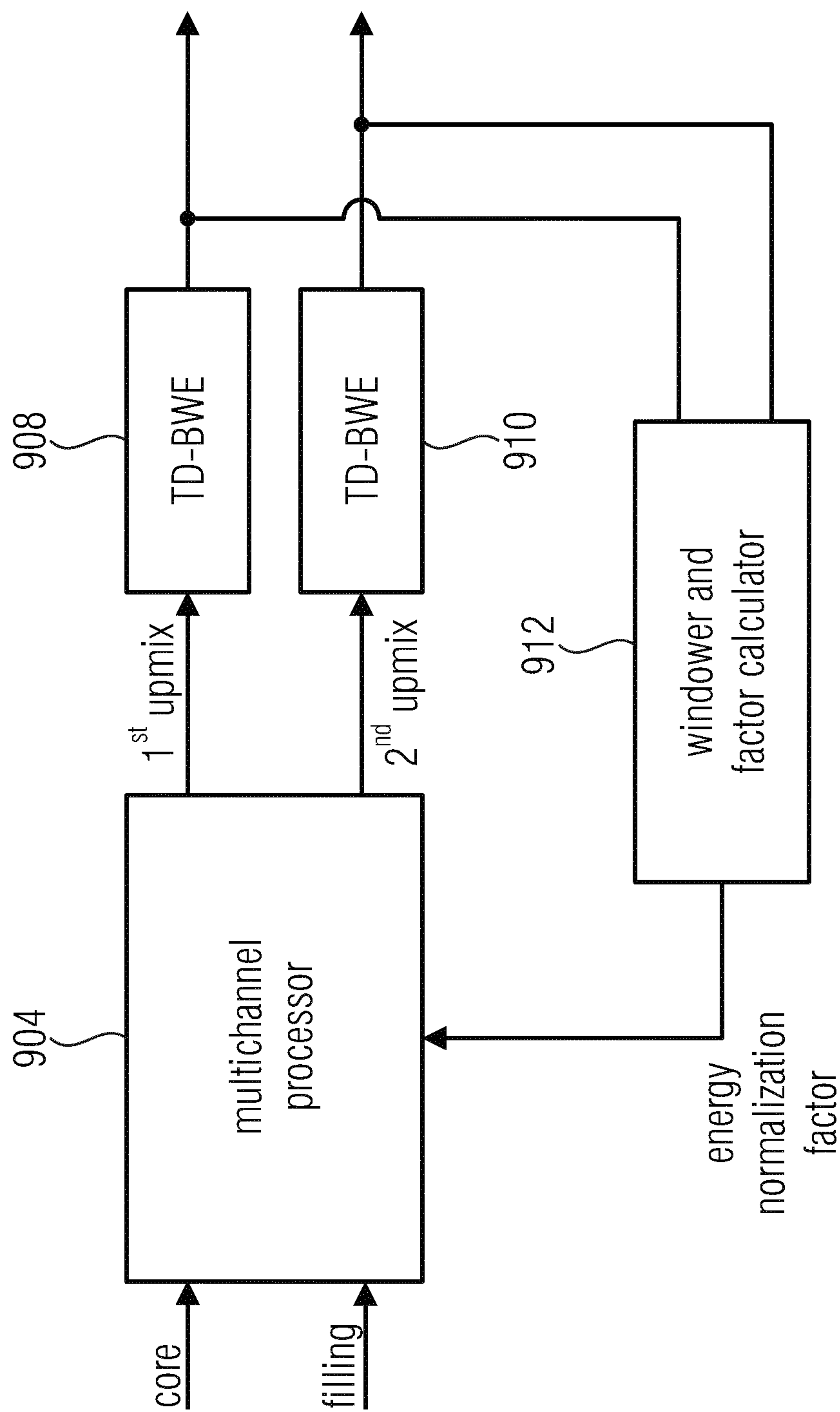


Fig. 9a

13/18

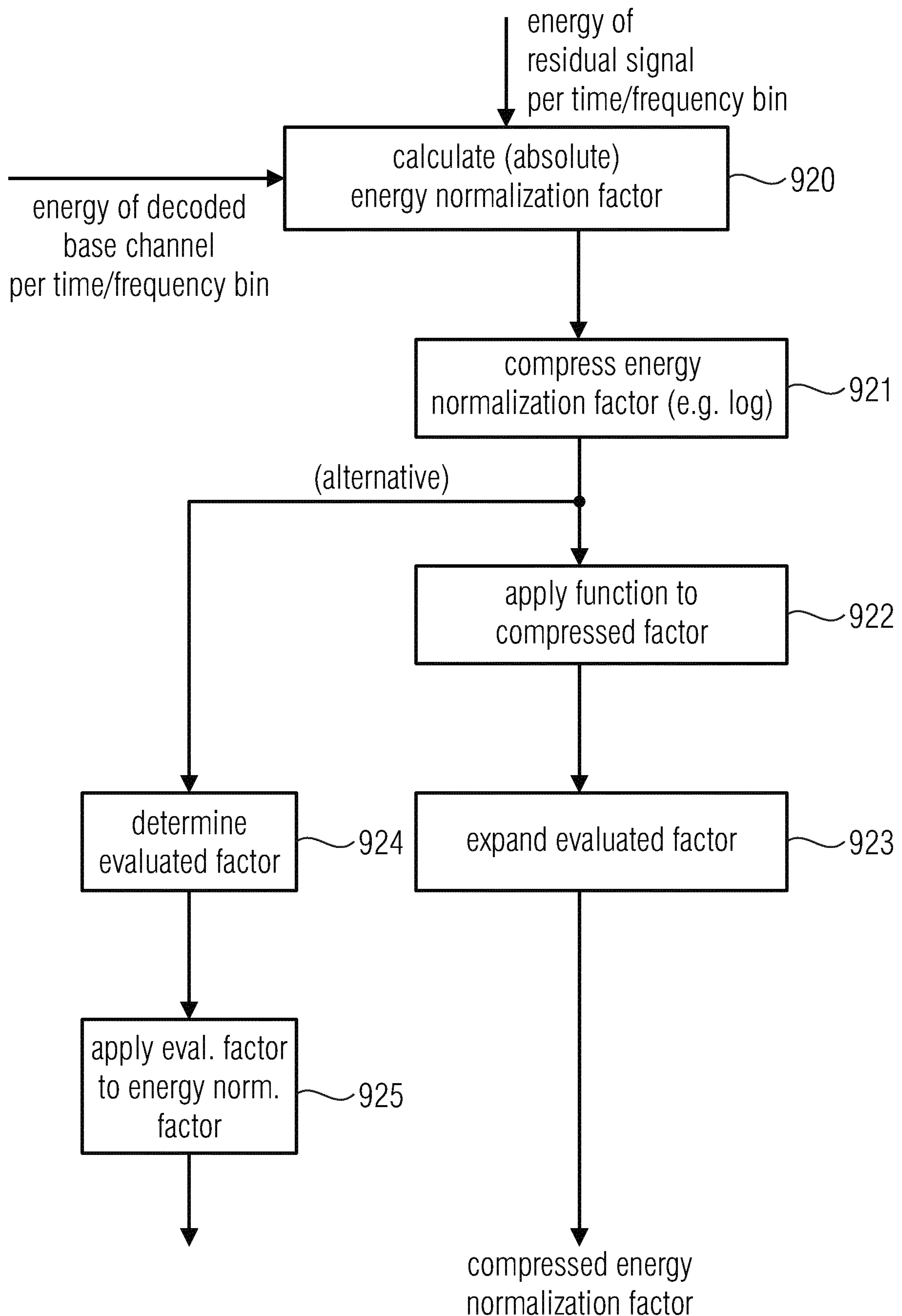


Fig. 9b

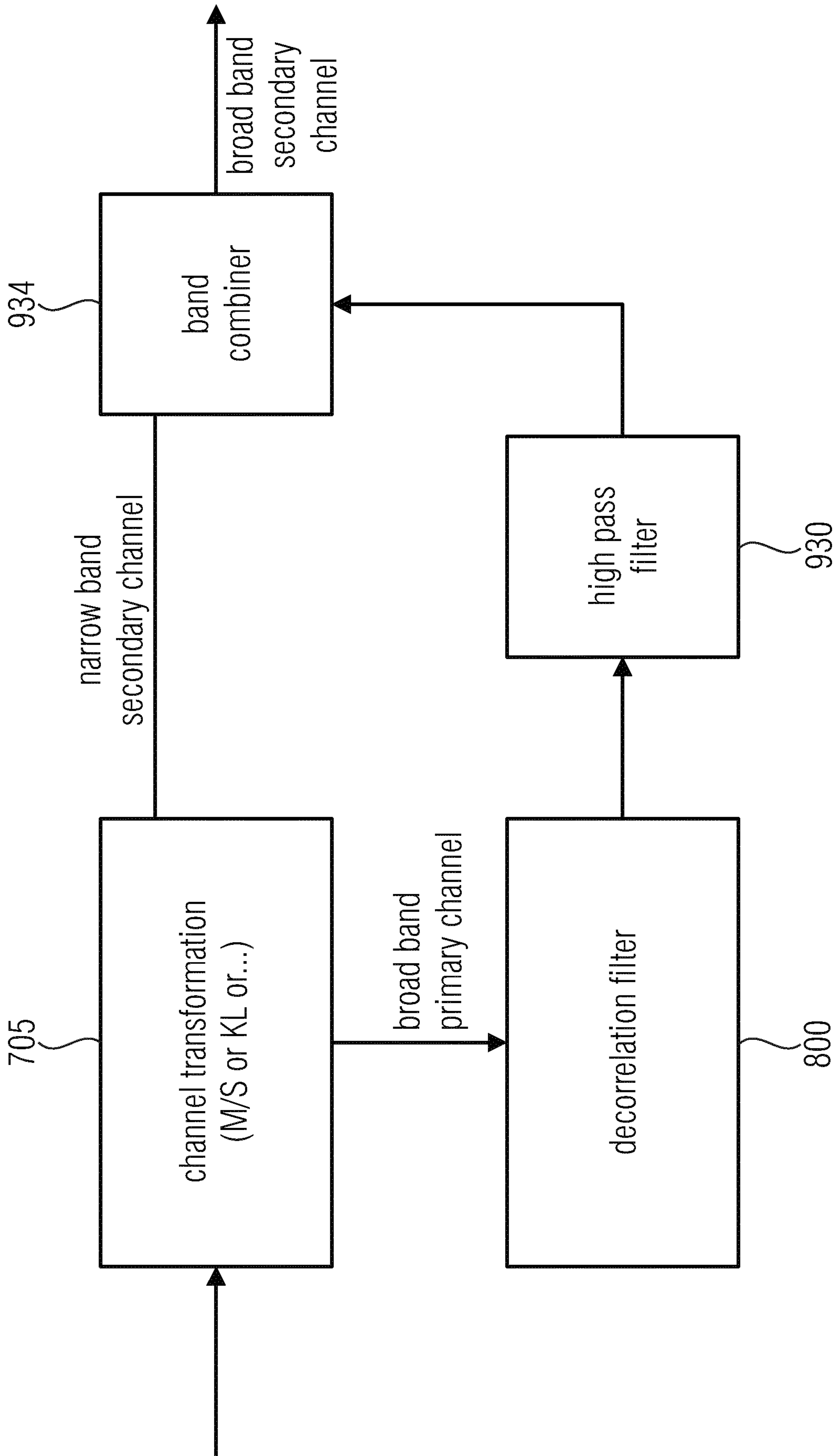


Fig. 10

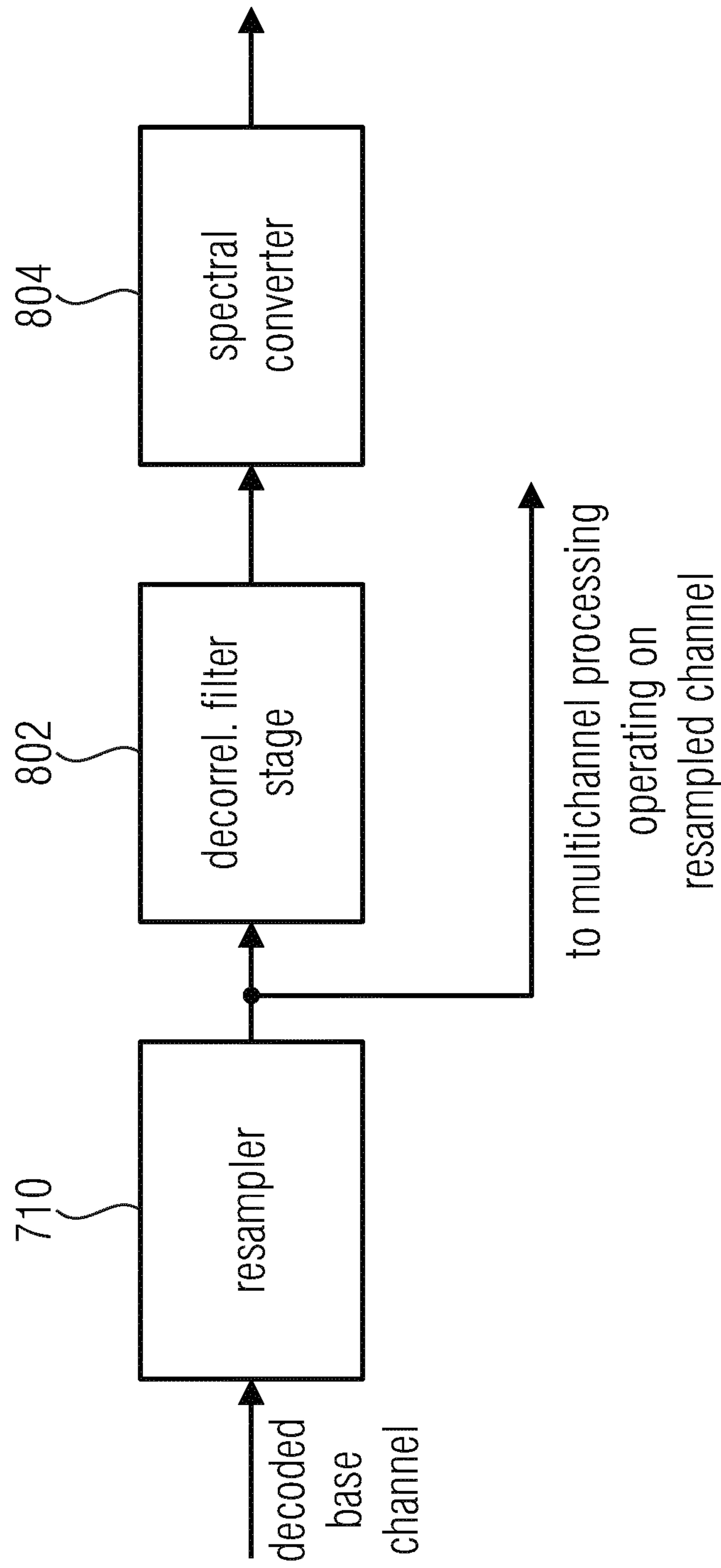


Fig. 11

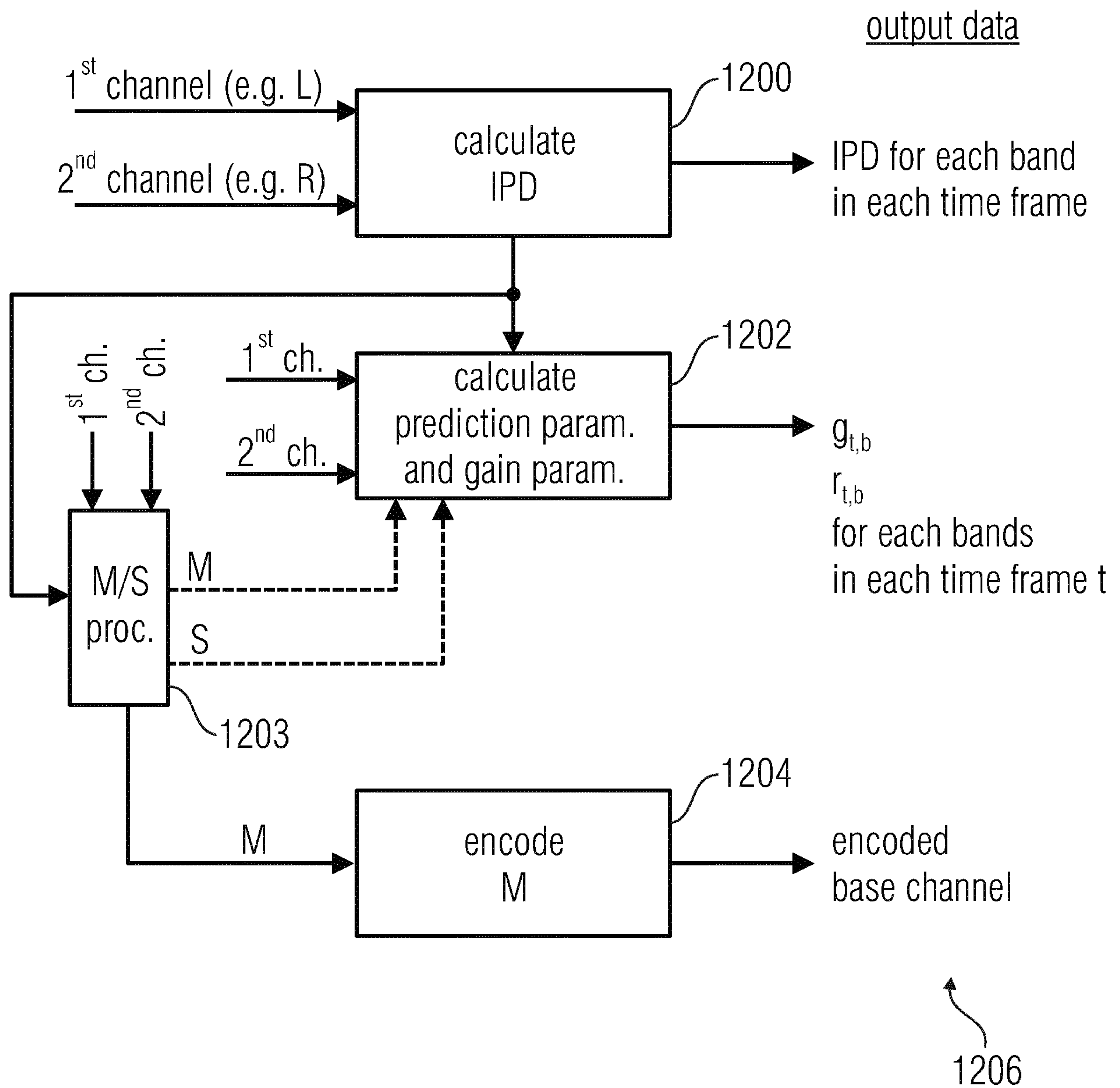


Fig. 12
(encoder side)

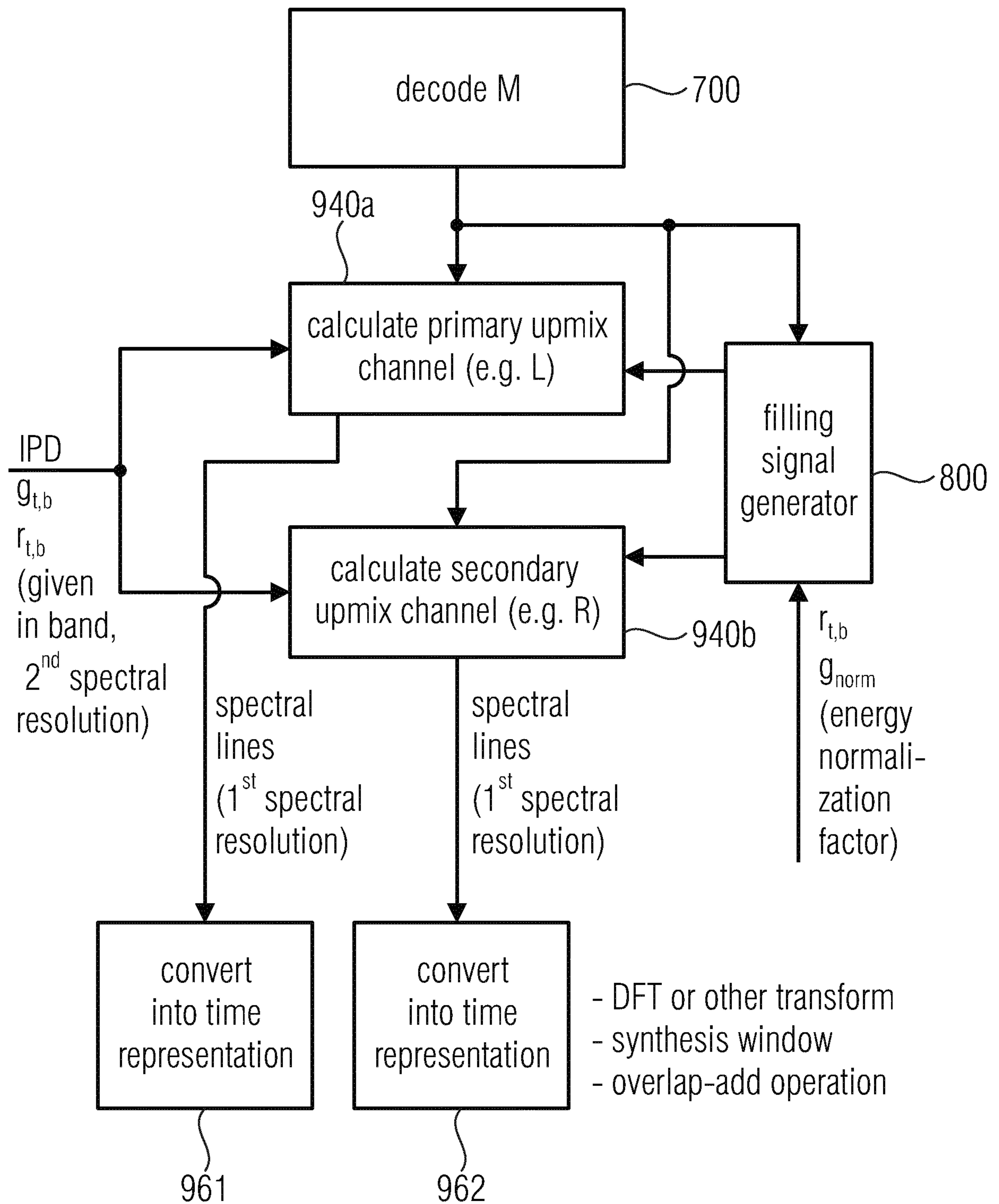


Fig. 13
(decoder side)

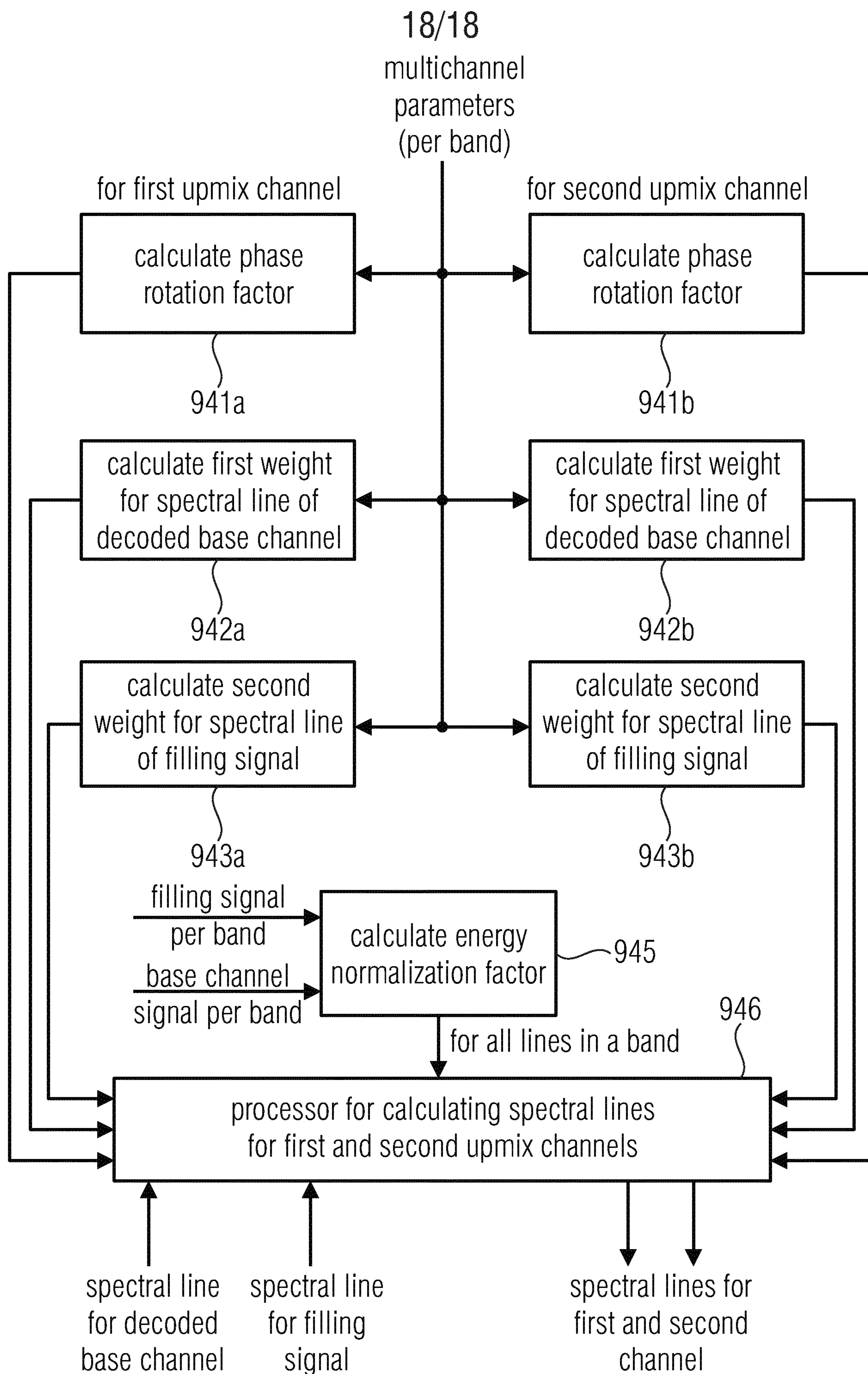


Fig. 14

SUBSTITUTE SHEET (RULE 26)