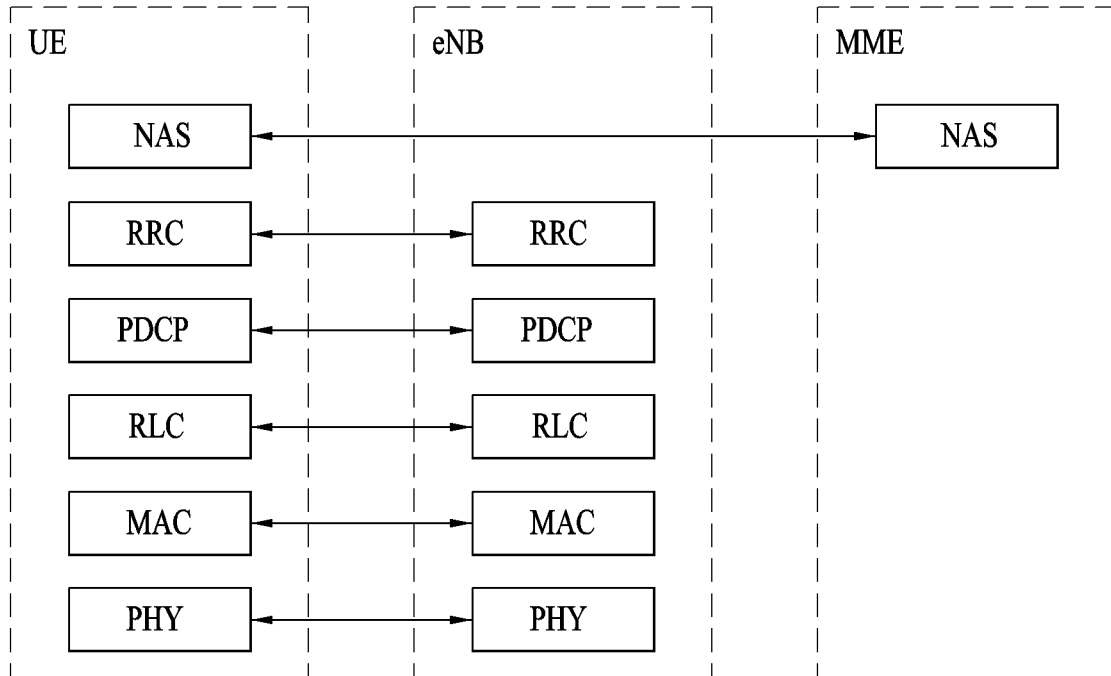
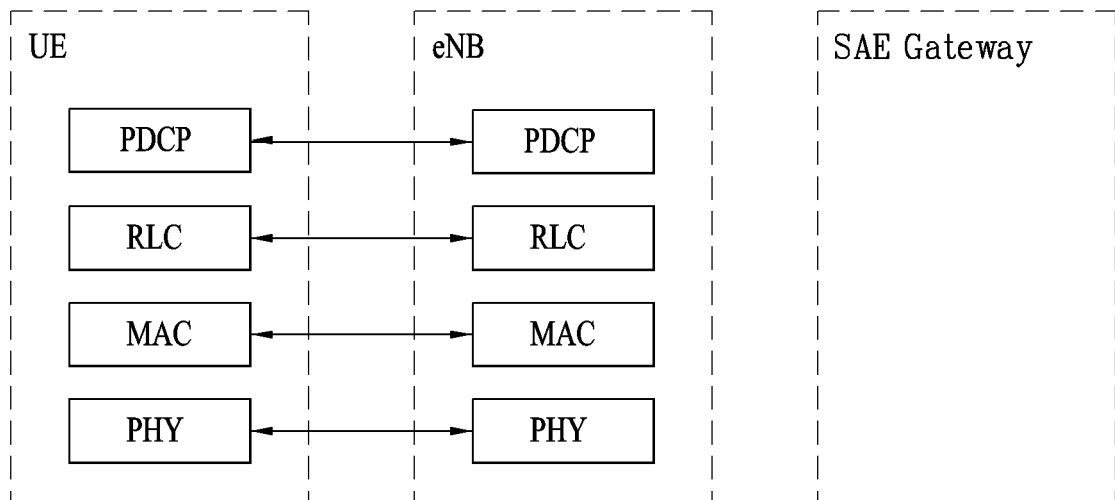


FIG. 1



(A) CONTROL-PLANE PROTOCOL STACK



(B) USER-PLANE PROTOCOL STACK

FIG. 2

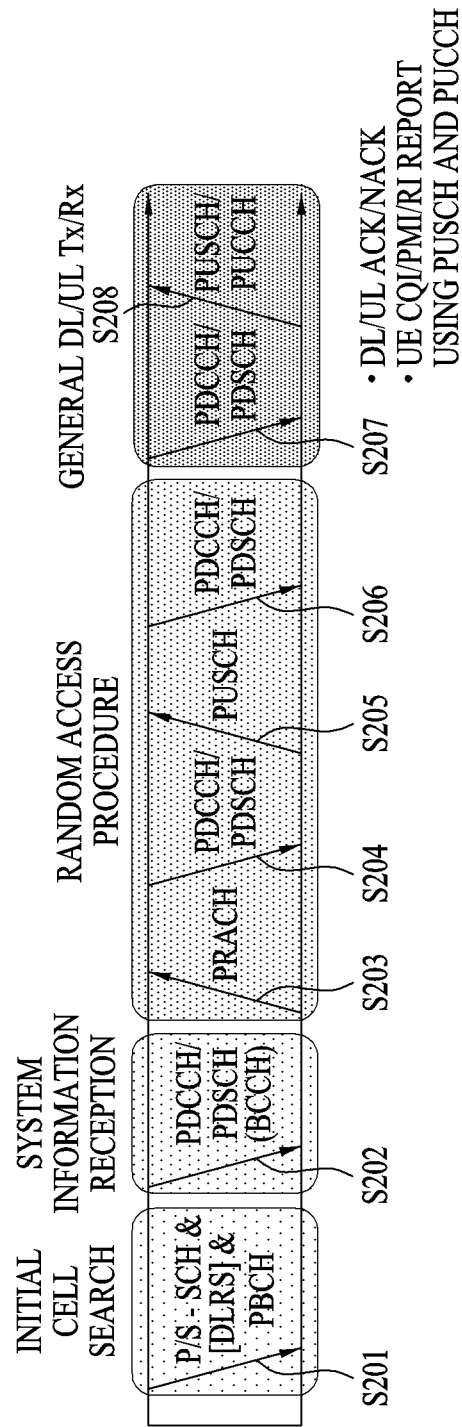


FIG. 2

FIG. 3

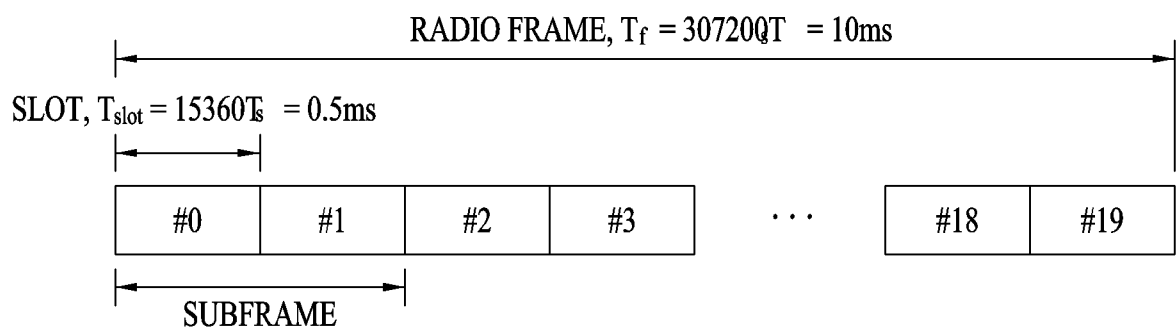


FIG. 4

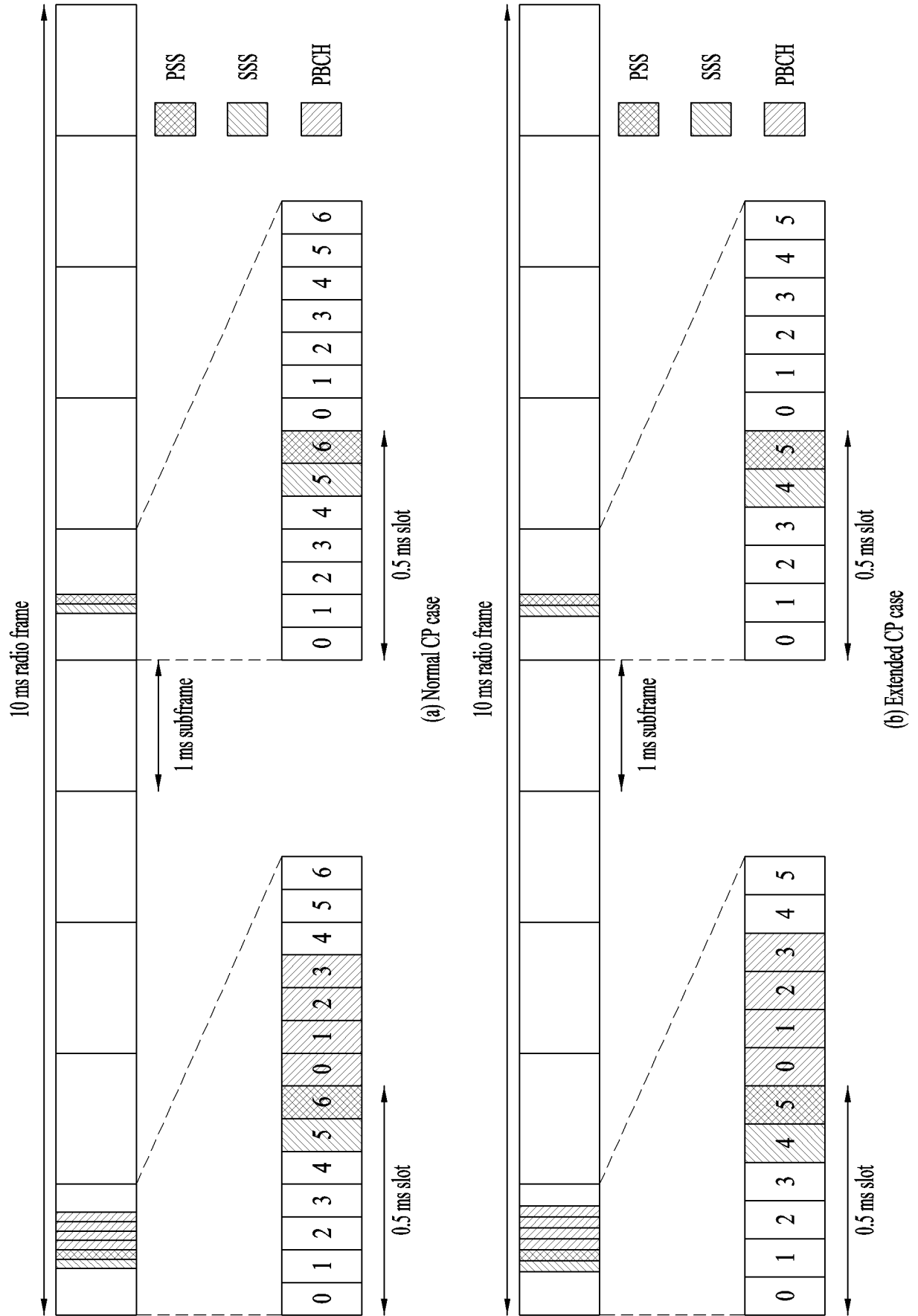


FIG. 5

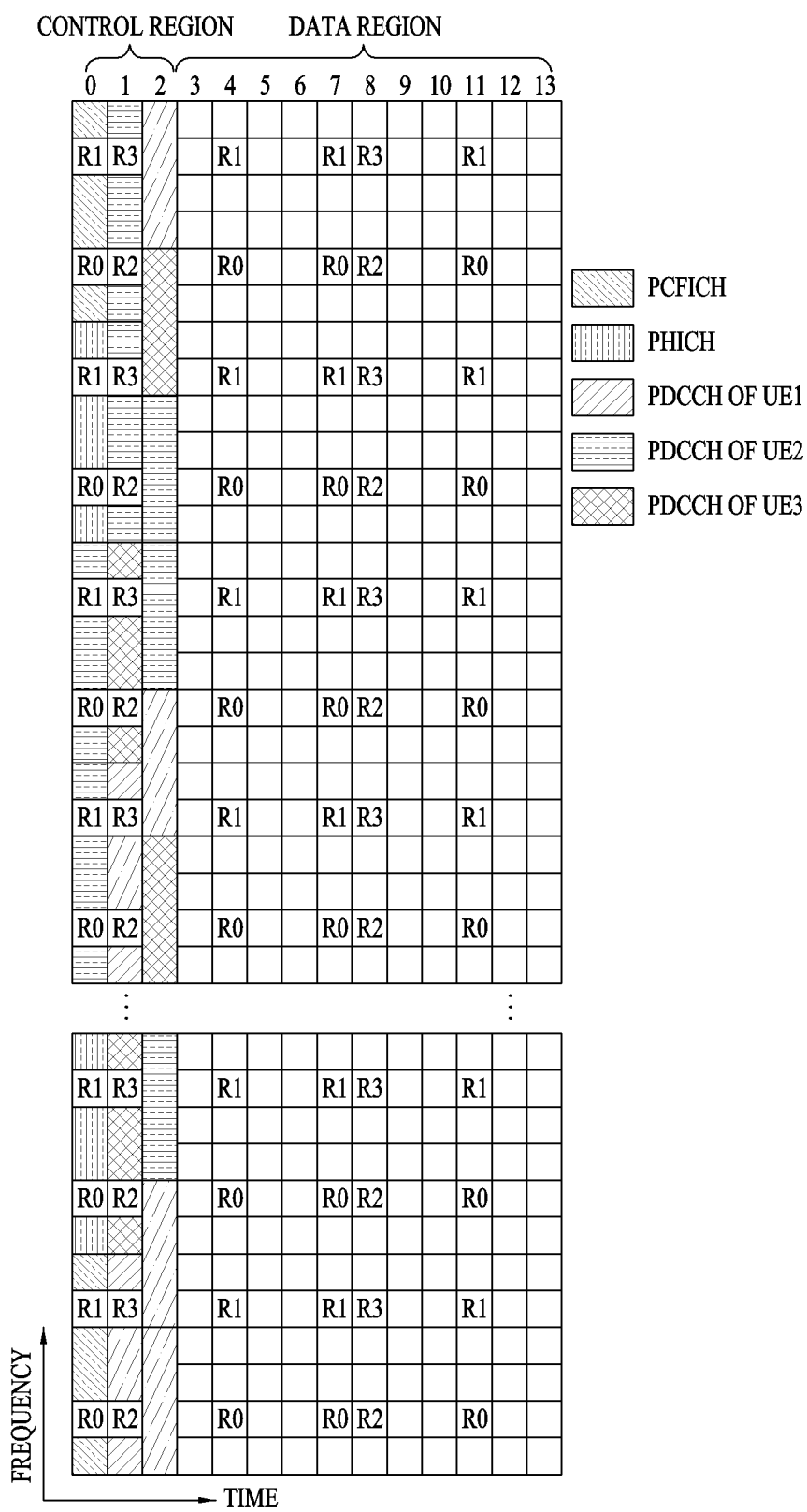


FIG. 6

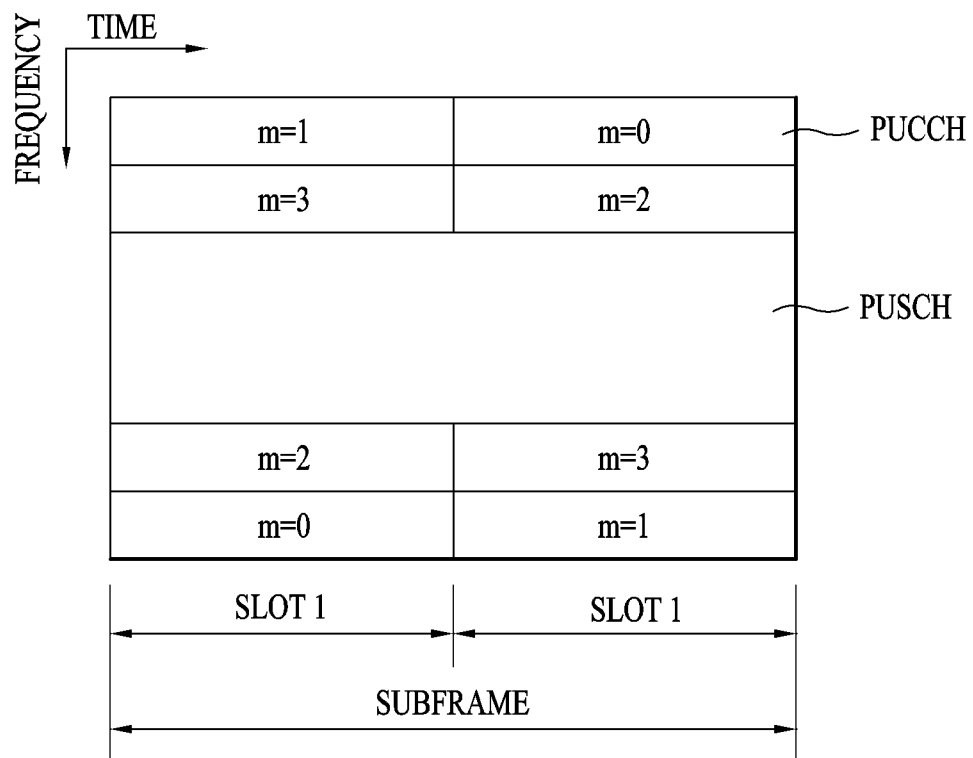


FIG. 7

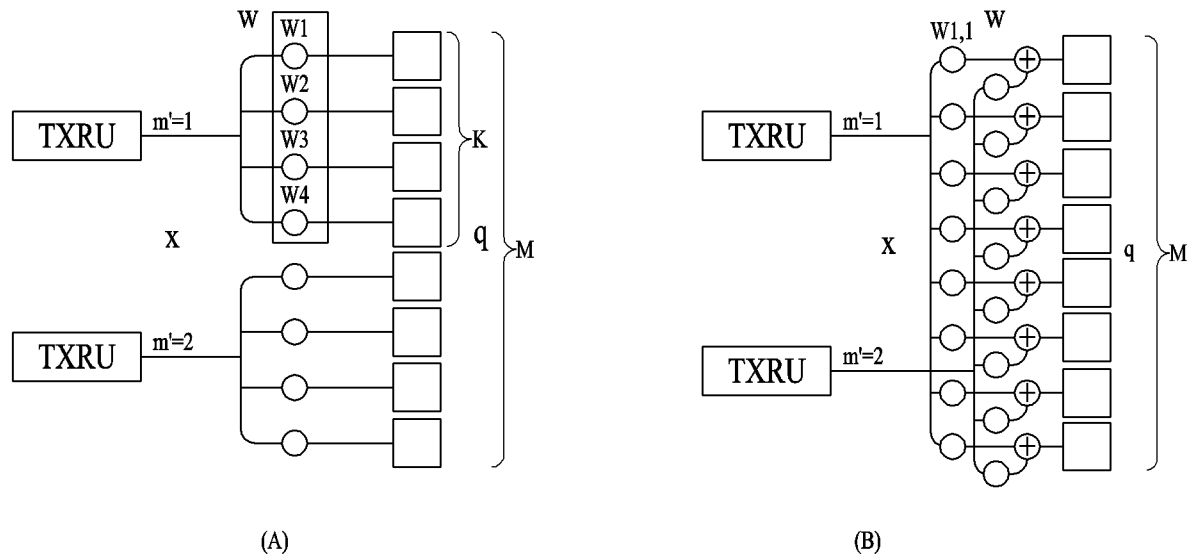


FIG. 8

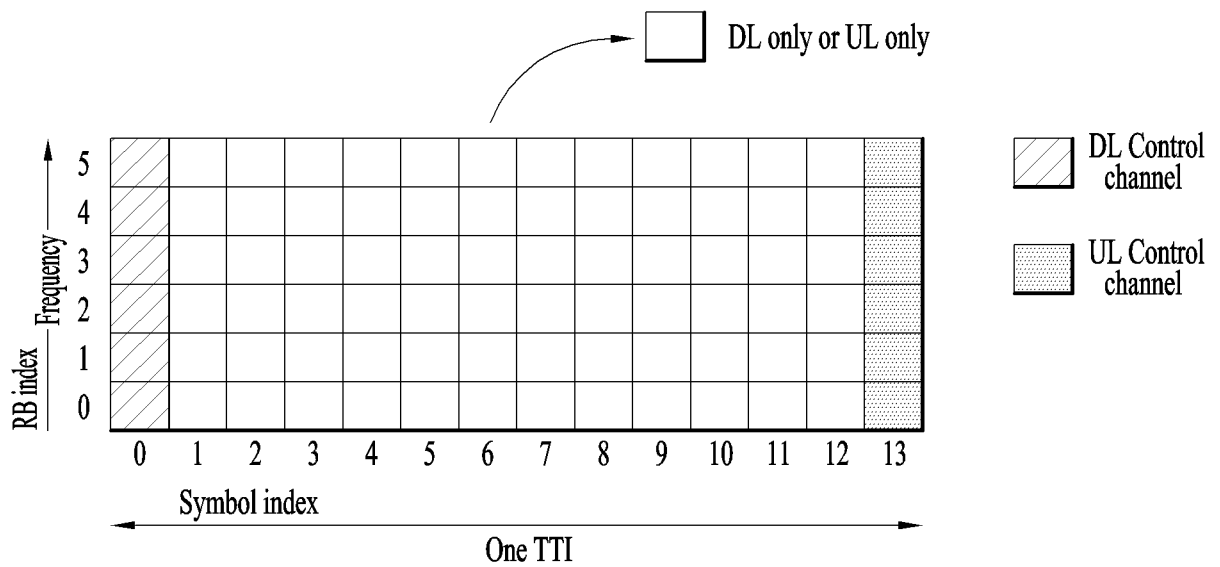


FIG. 9

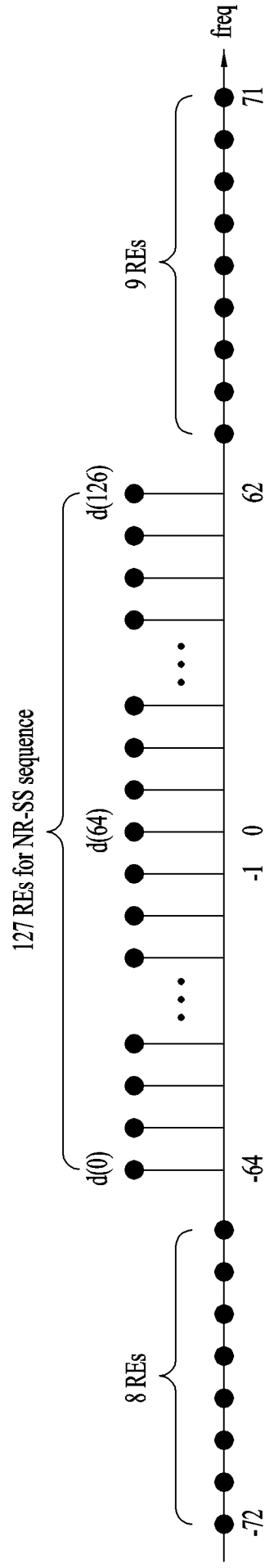


FIG. 10

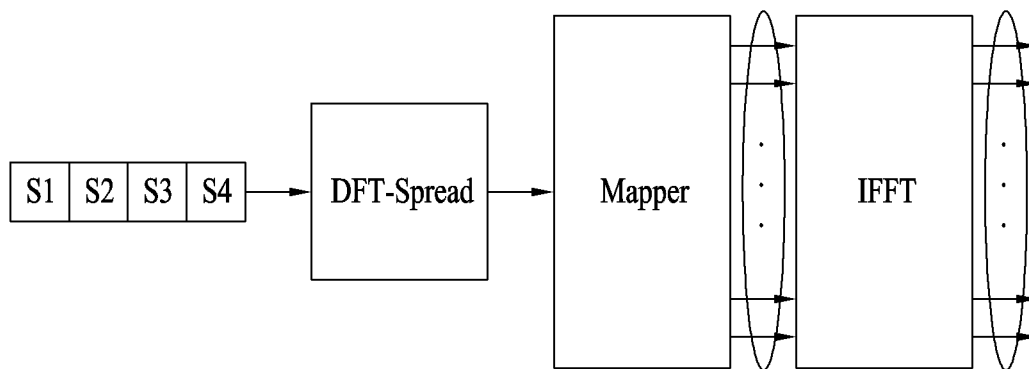


FIG. 11

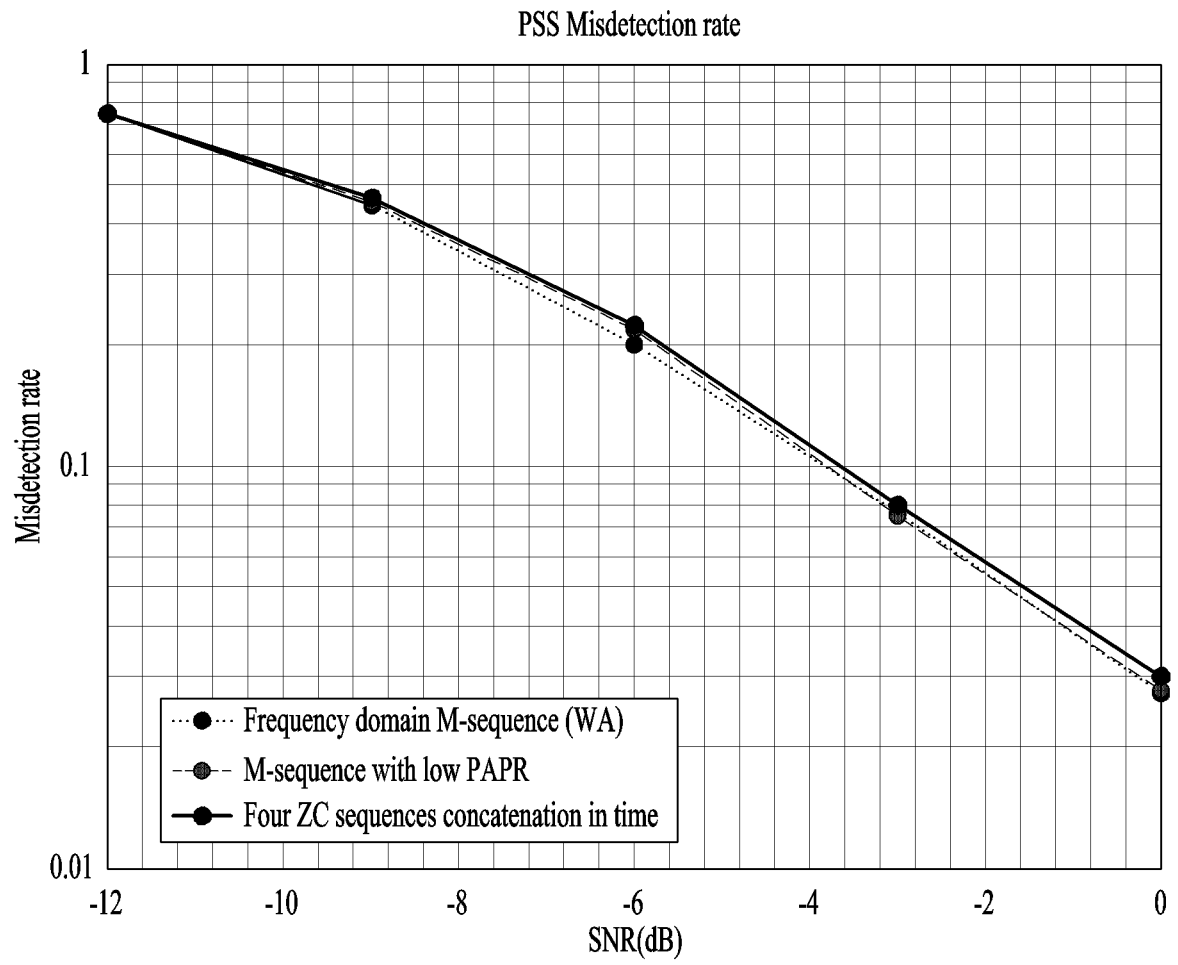


FIG. 12

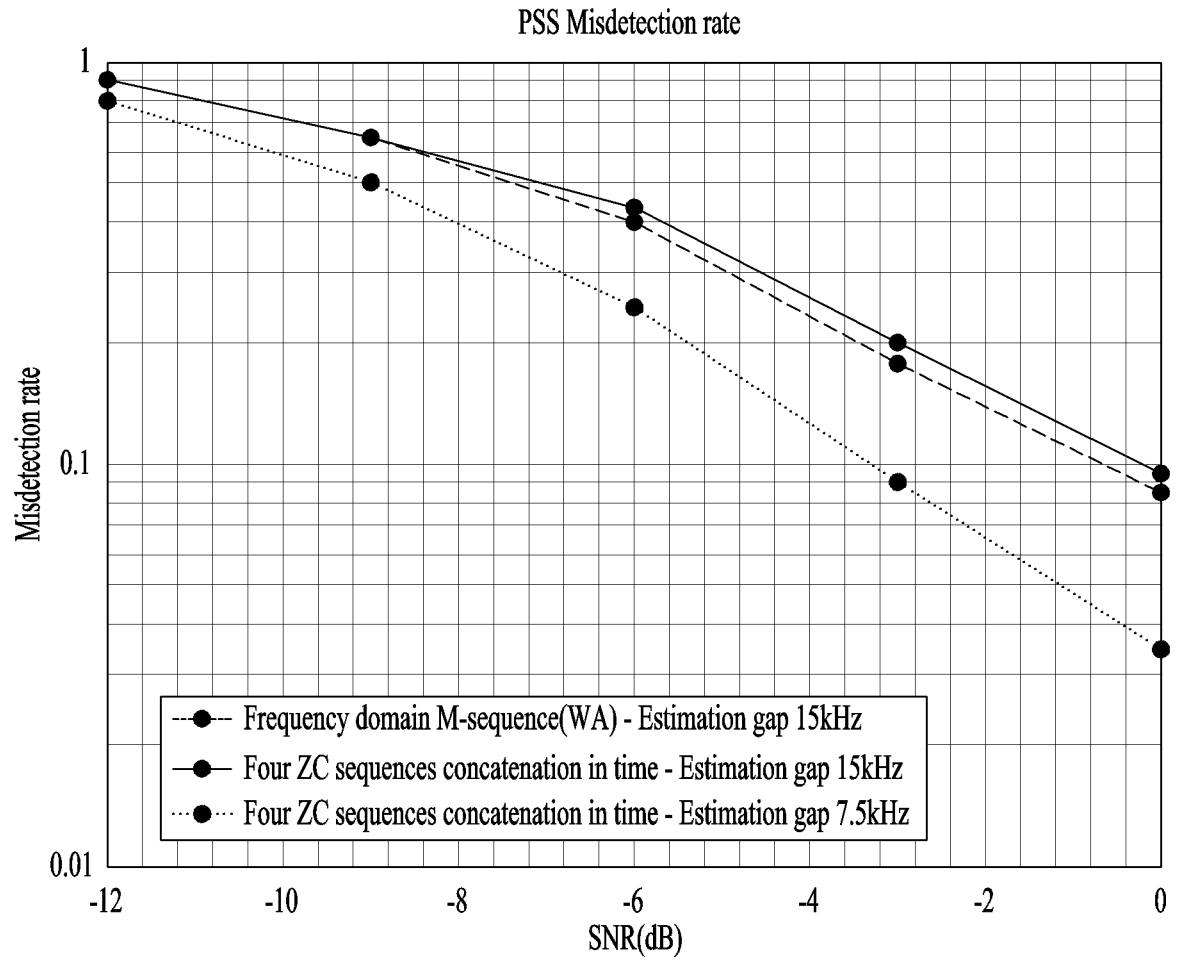


FIG. 13

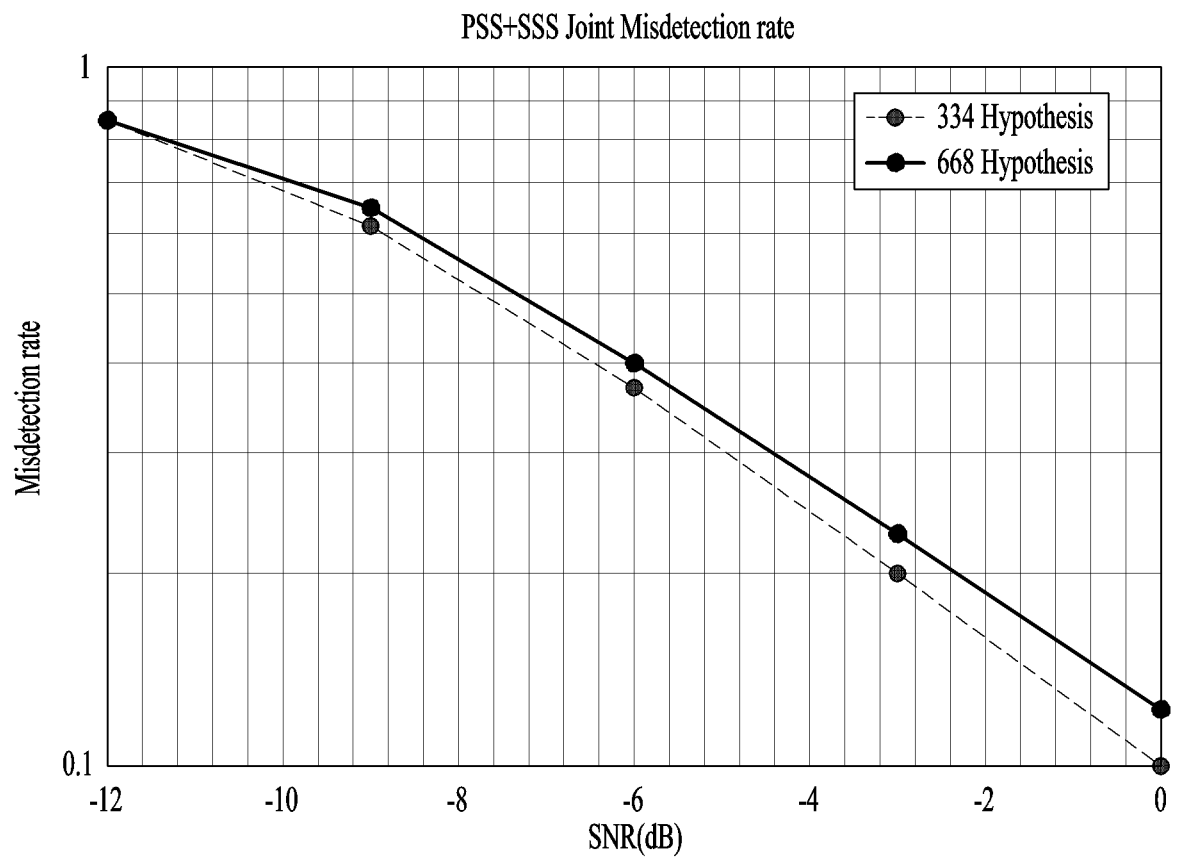


FIG. 14

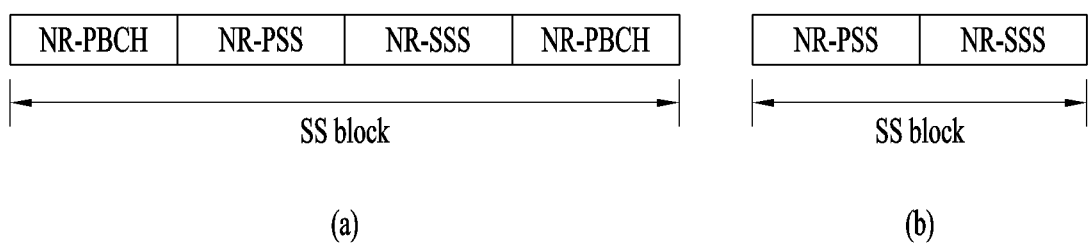
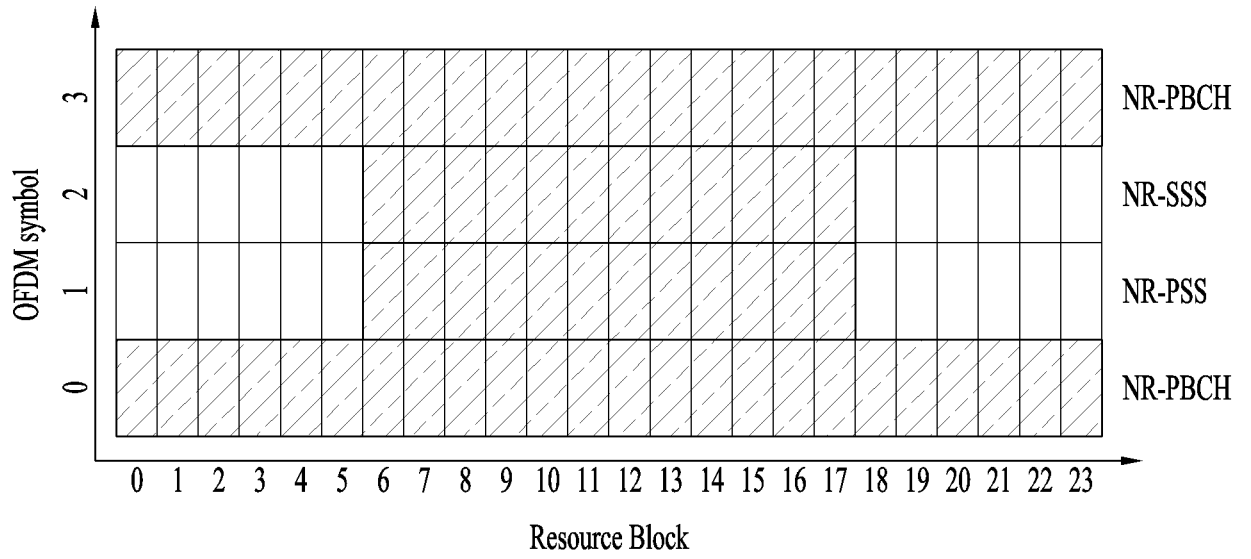
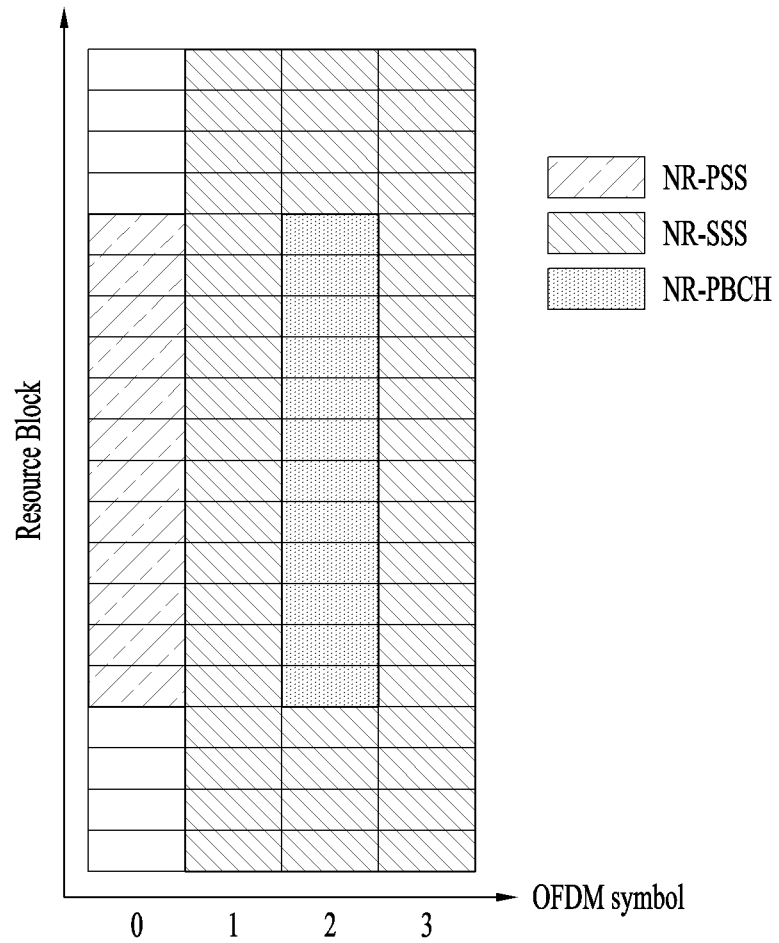


FIG. 15



(a)



(b)

FIG. 16

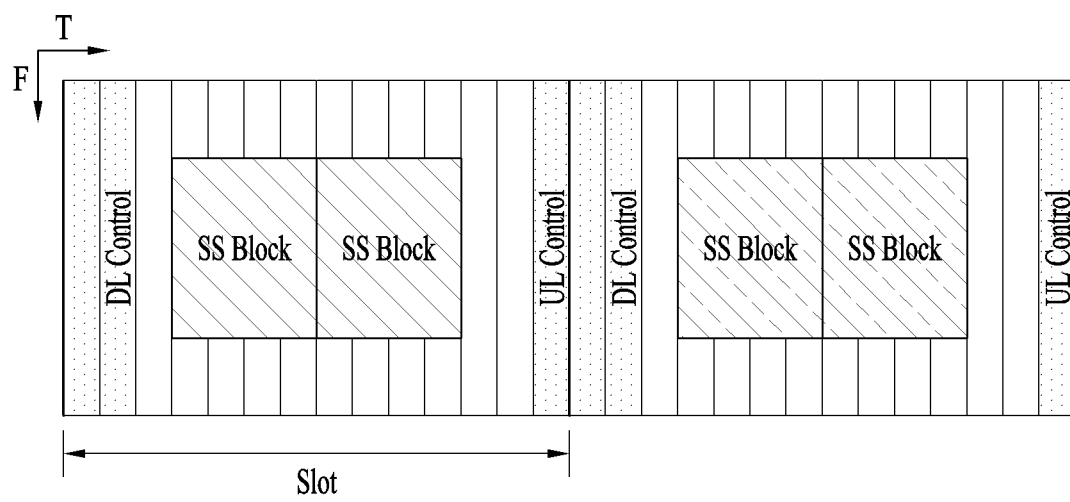


FIG. 17

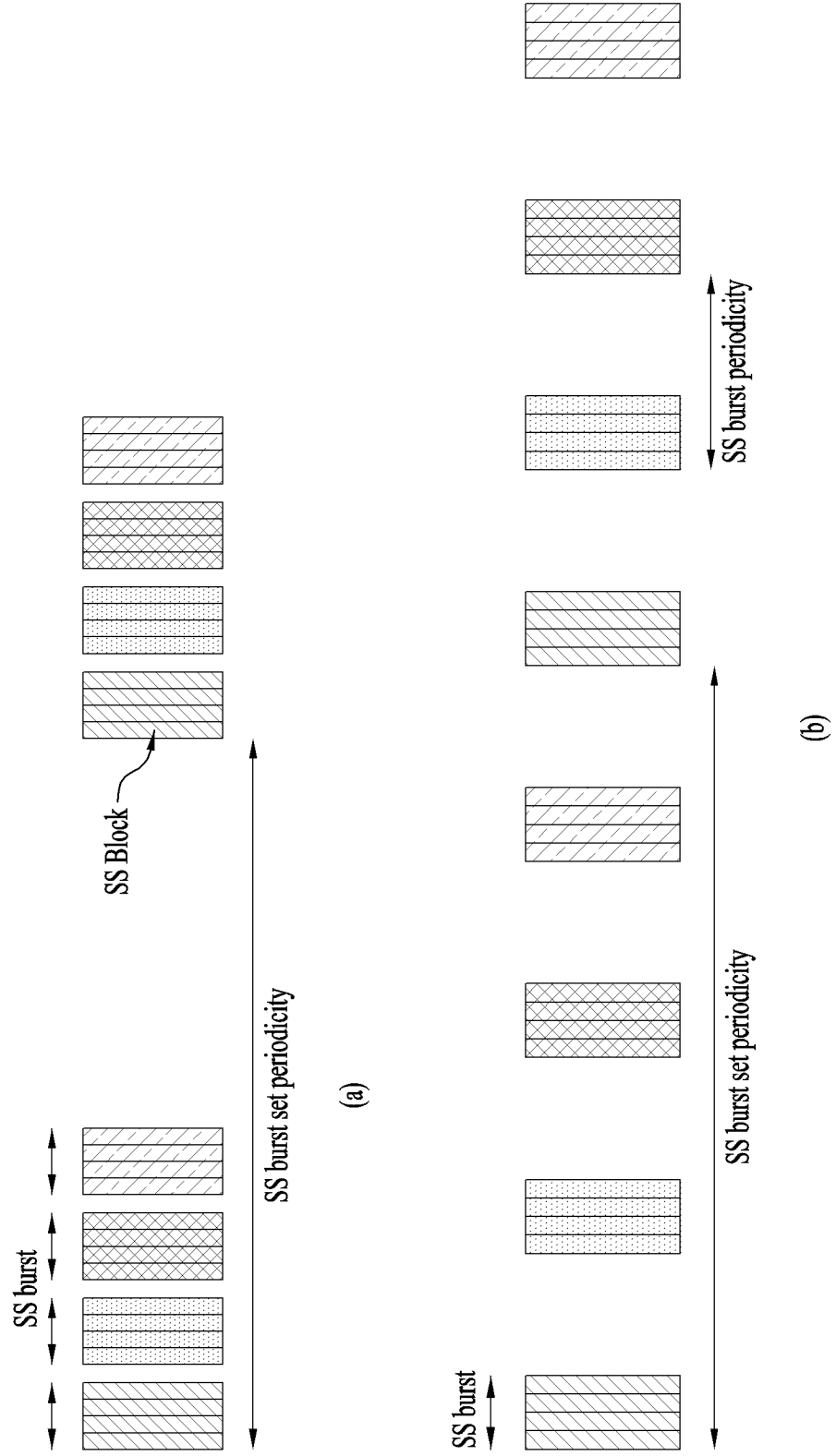
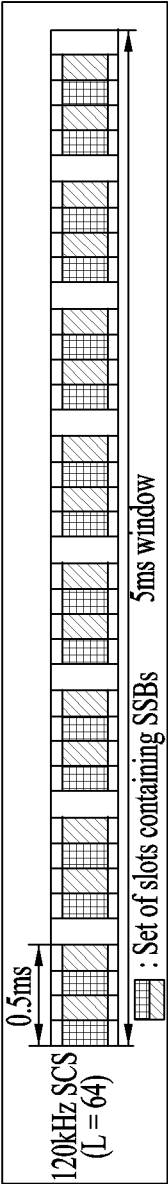
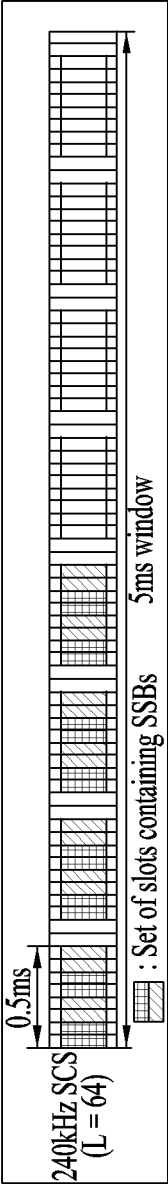


FIG. 18



(a)



(b)

FIG. 19

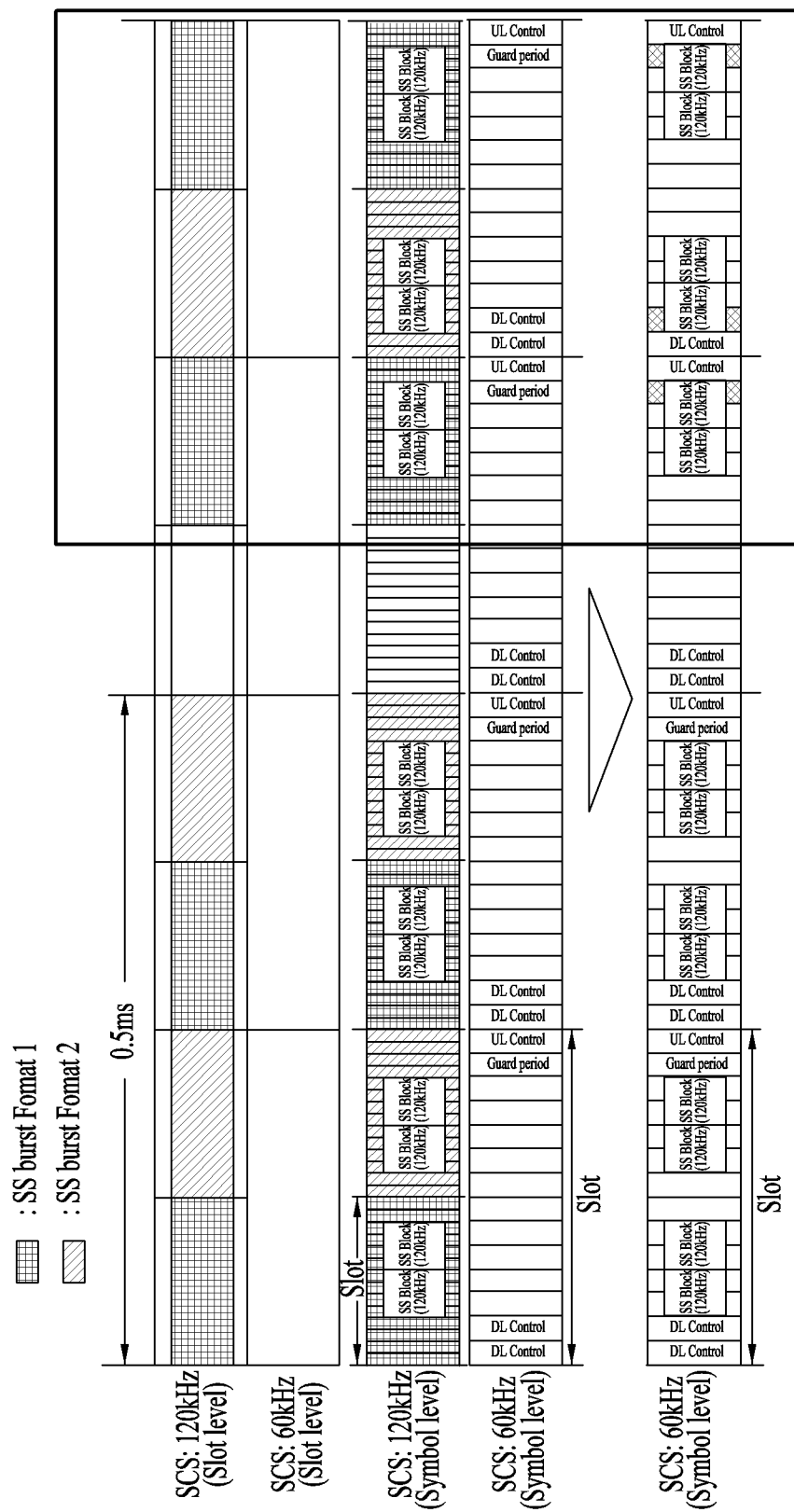


FIG. 20

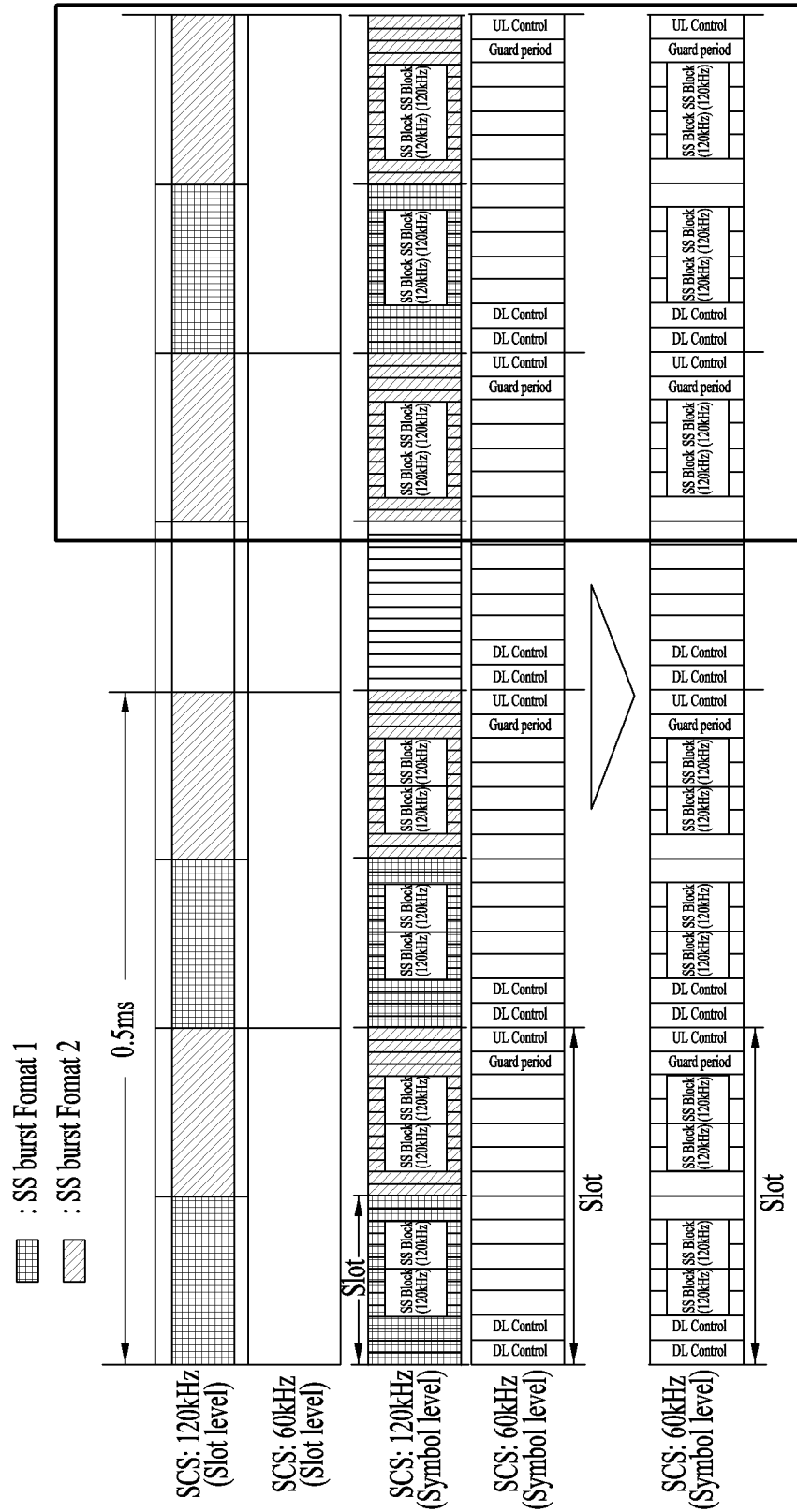
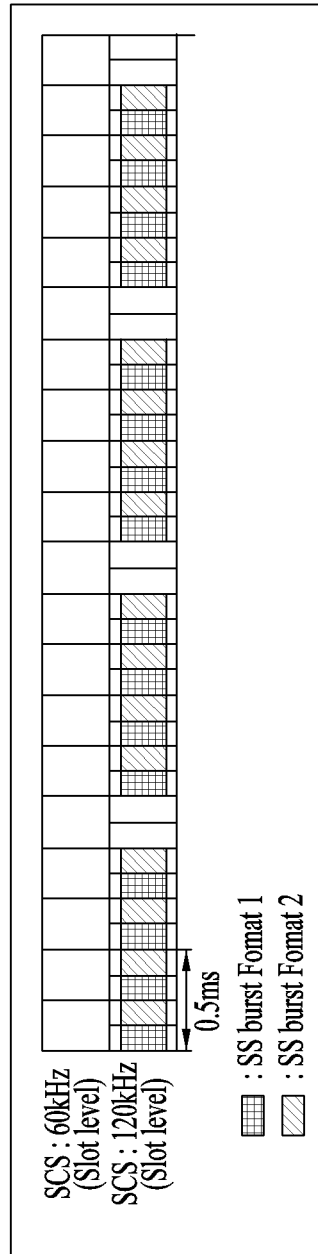
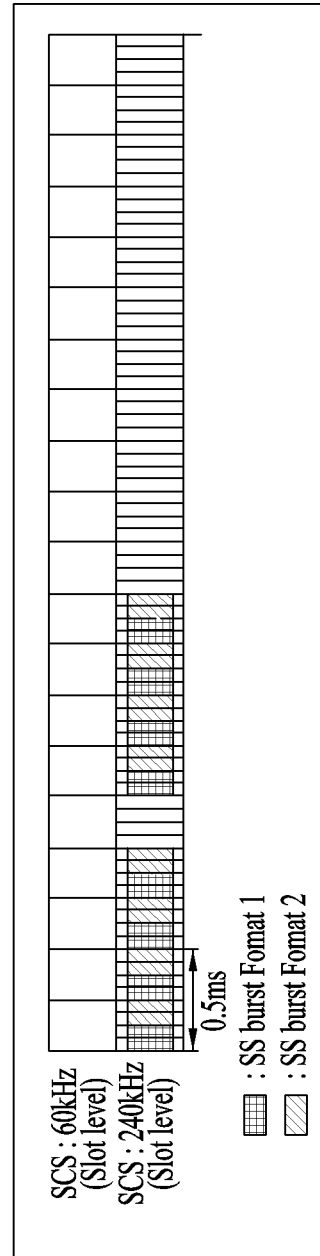


FIG. 21



(a)



(9)

FIG. 22

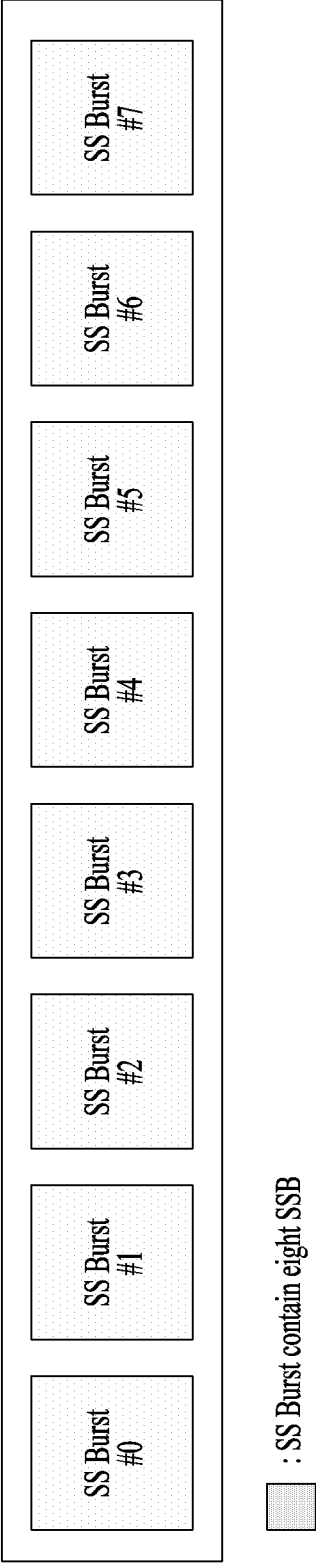


FIG. 23

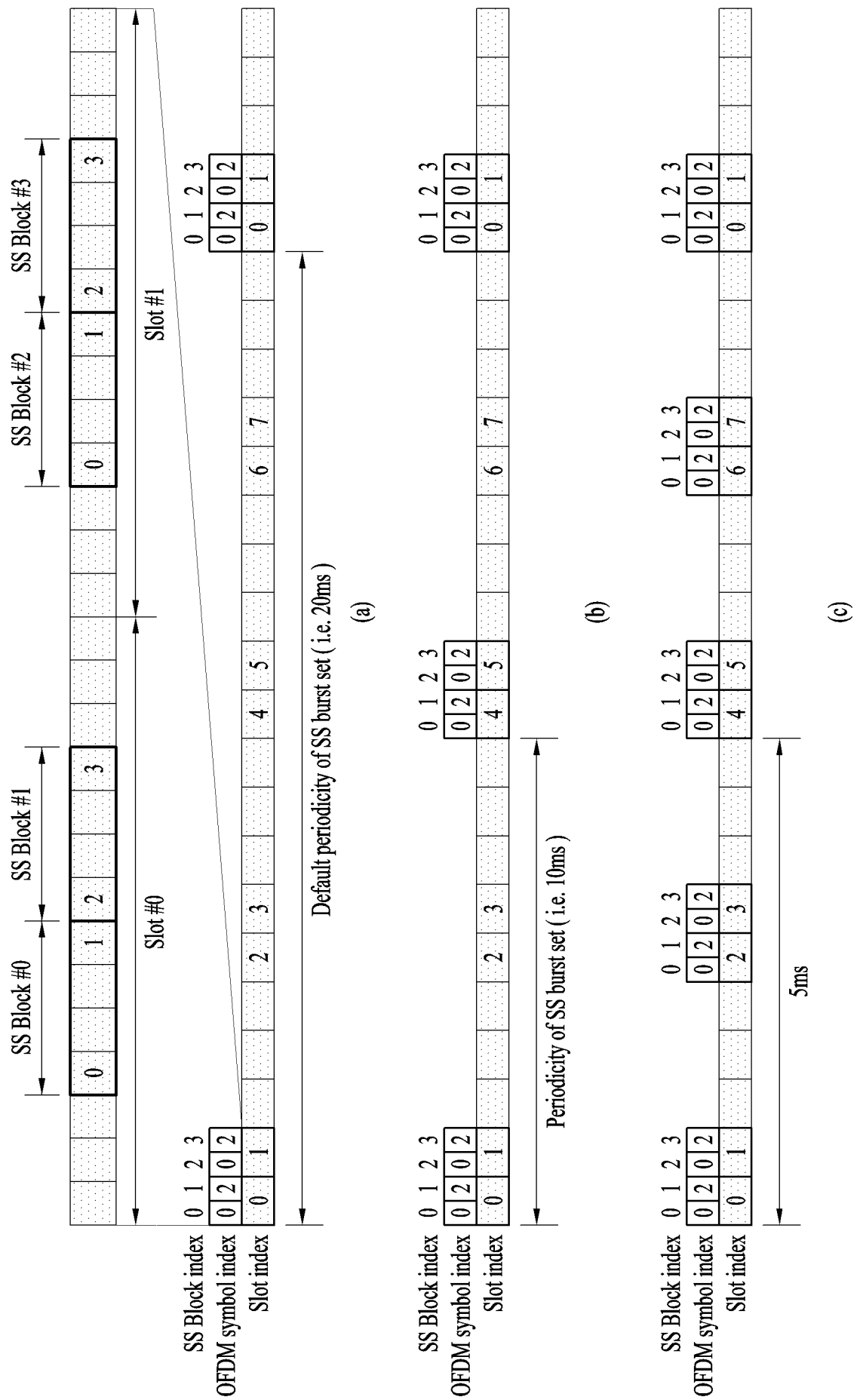


FIG. 24

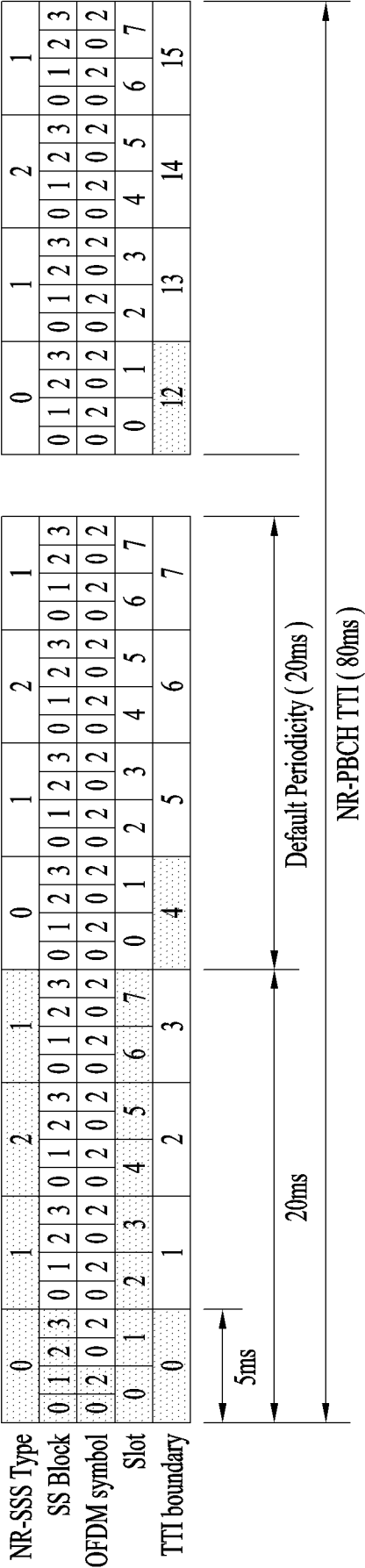


FIG. 25

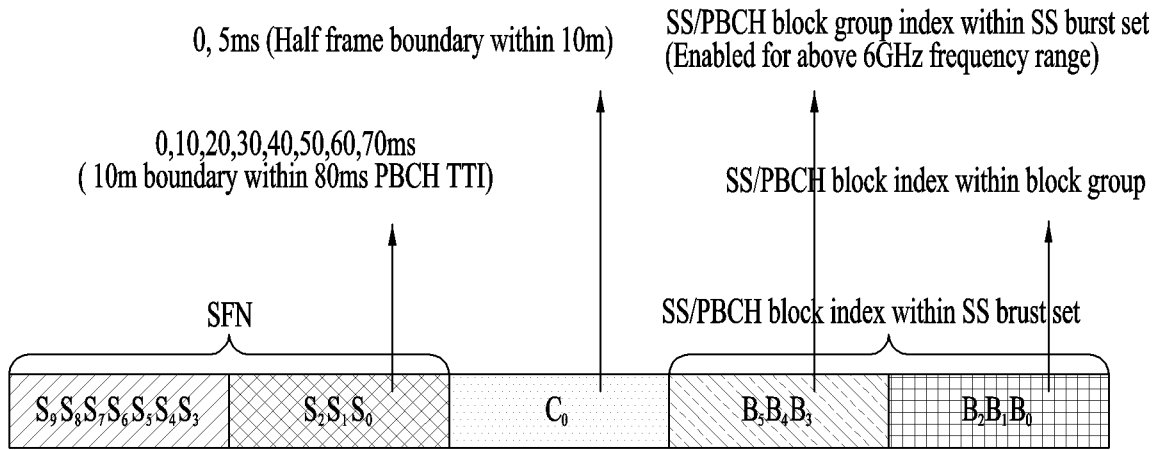


FIG. 26

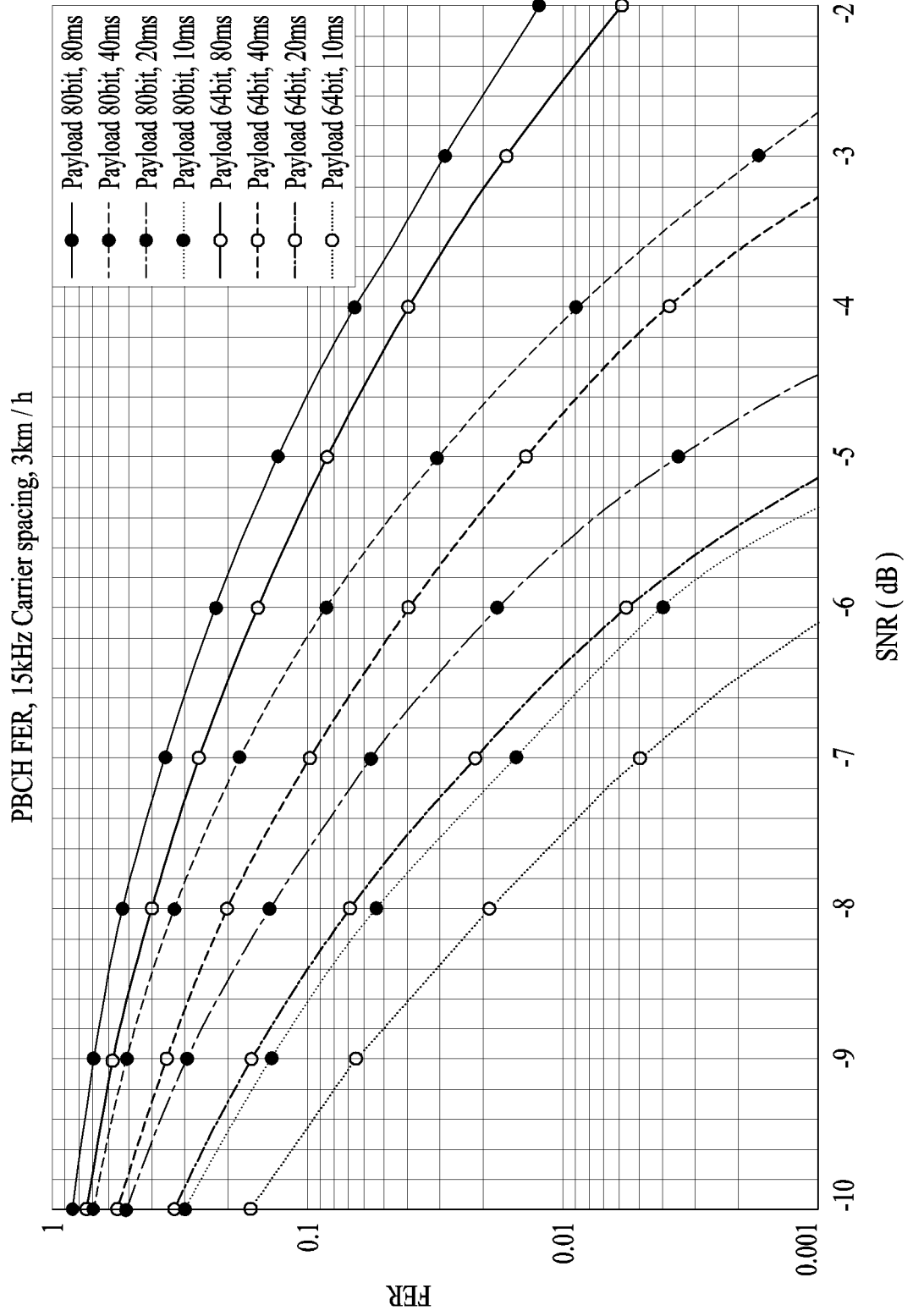


FIG. 27

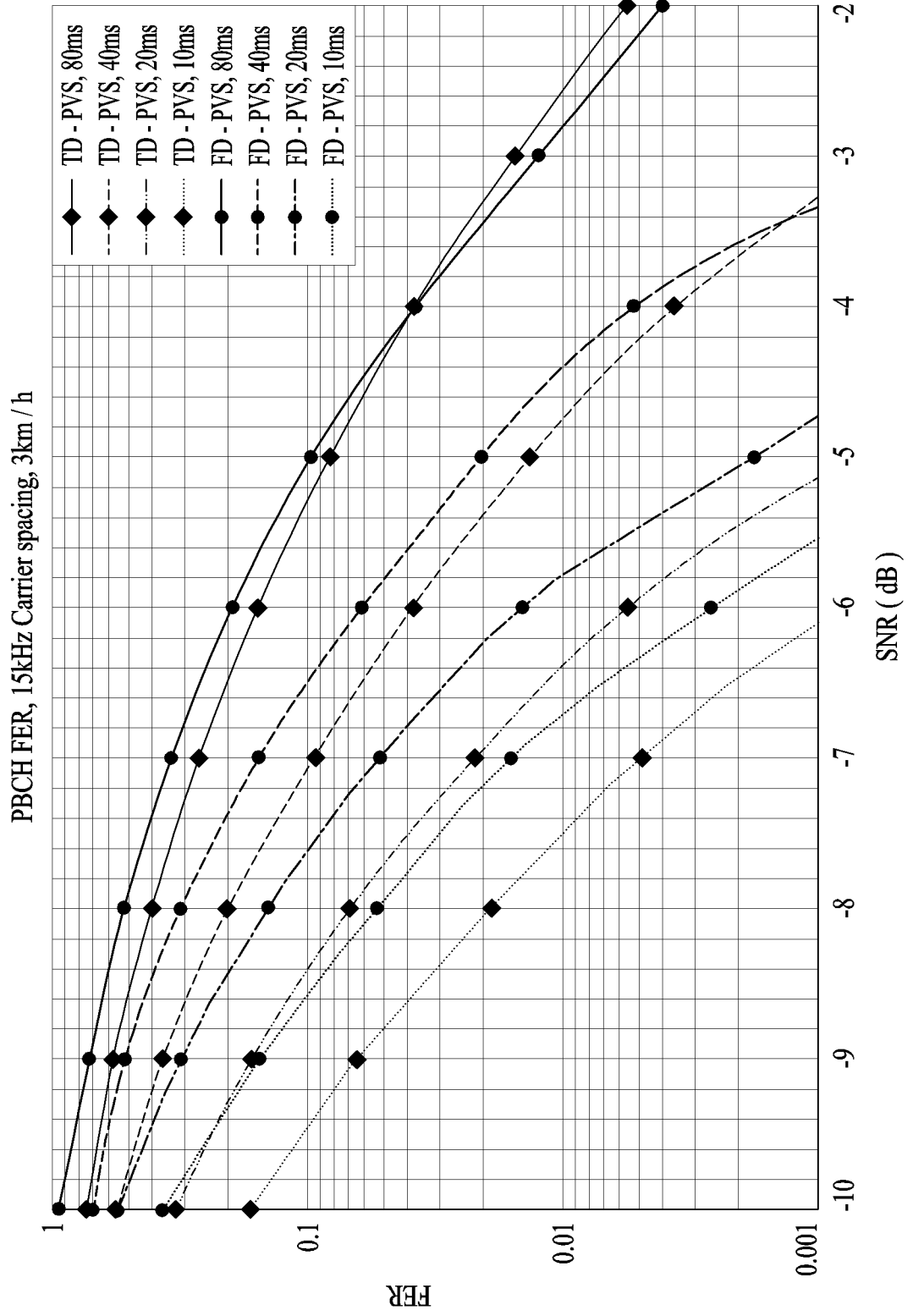
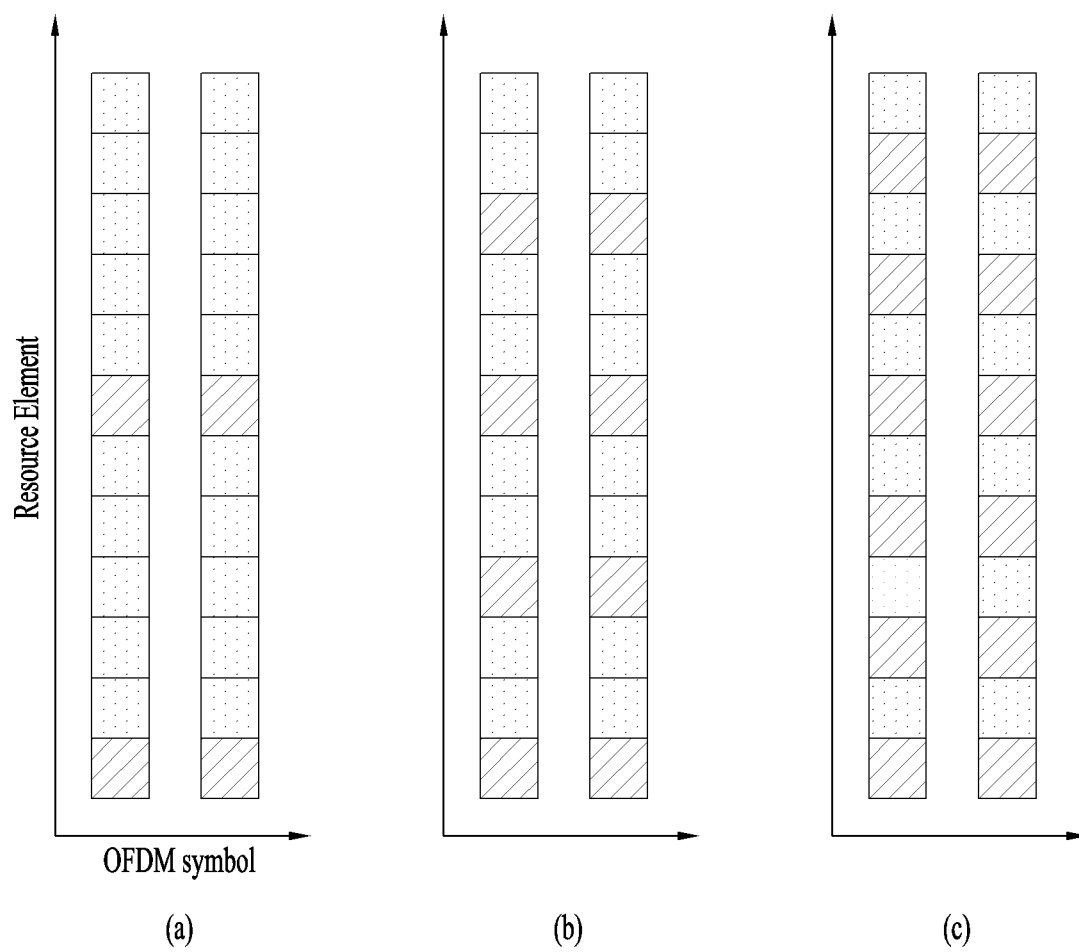


FIG. 28



□ Data ▨ DMRS

FIG. 29

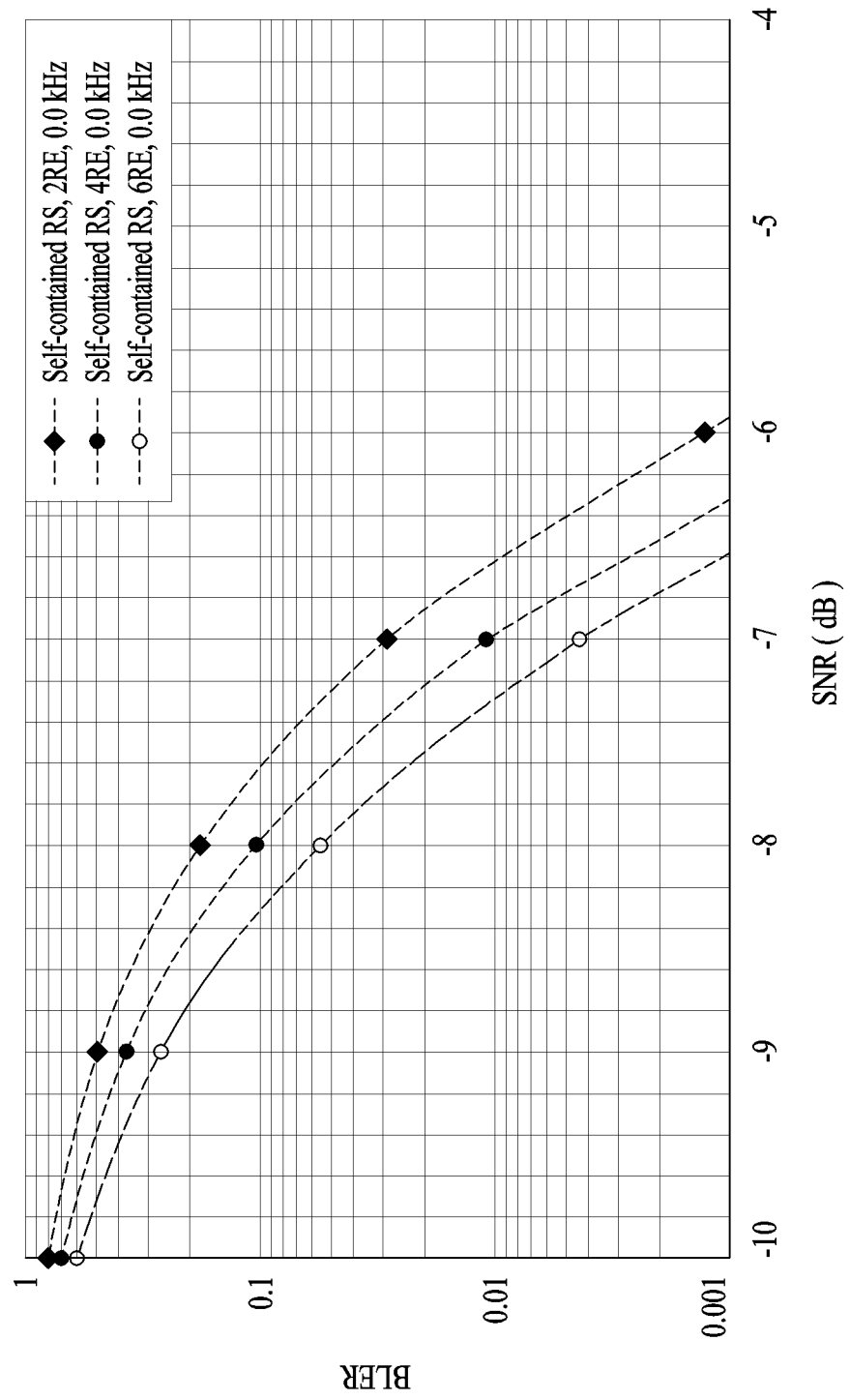


FIG. 30

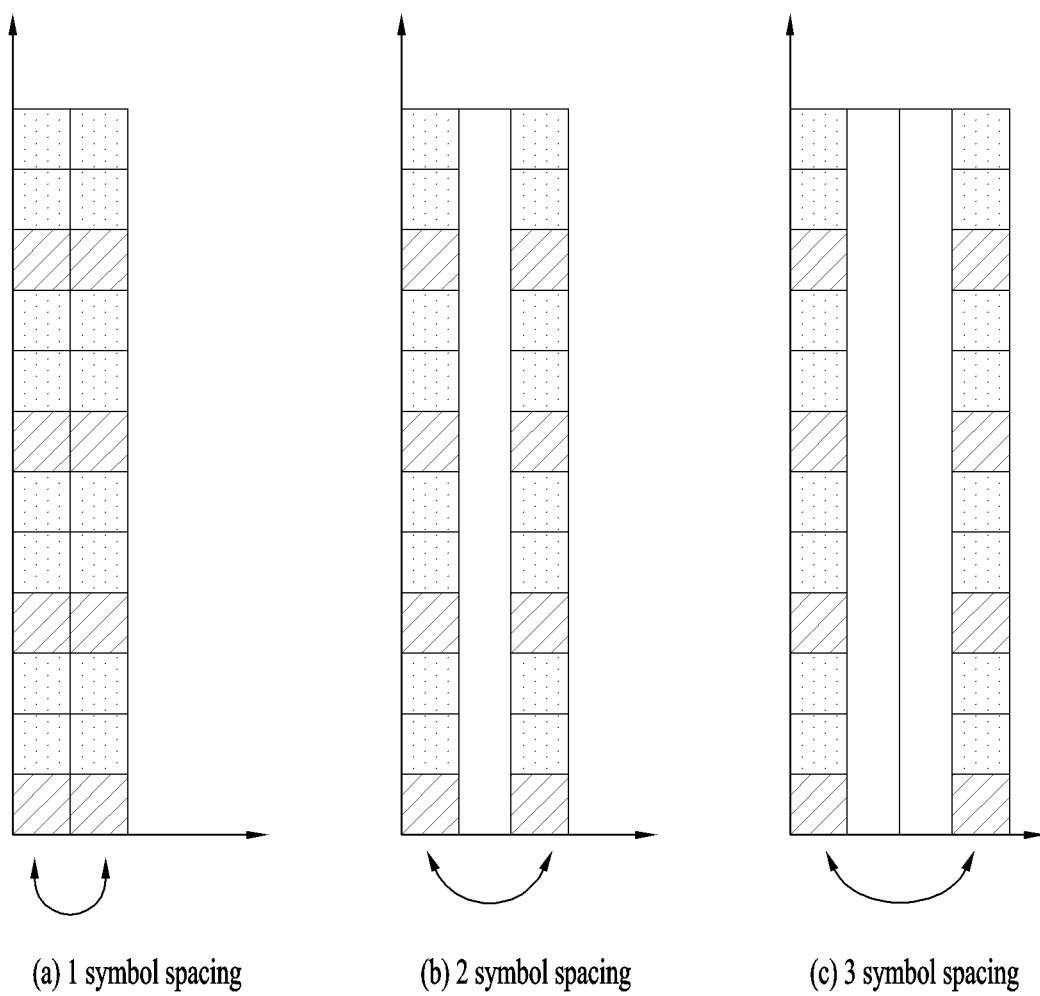


FIG. 31

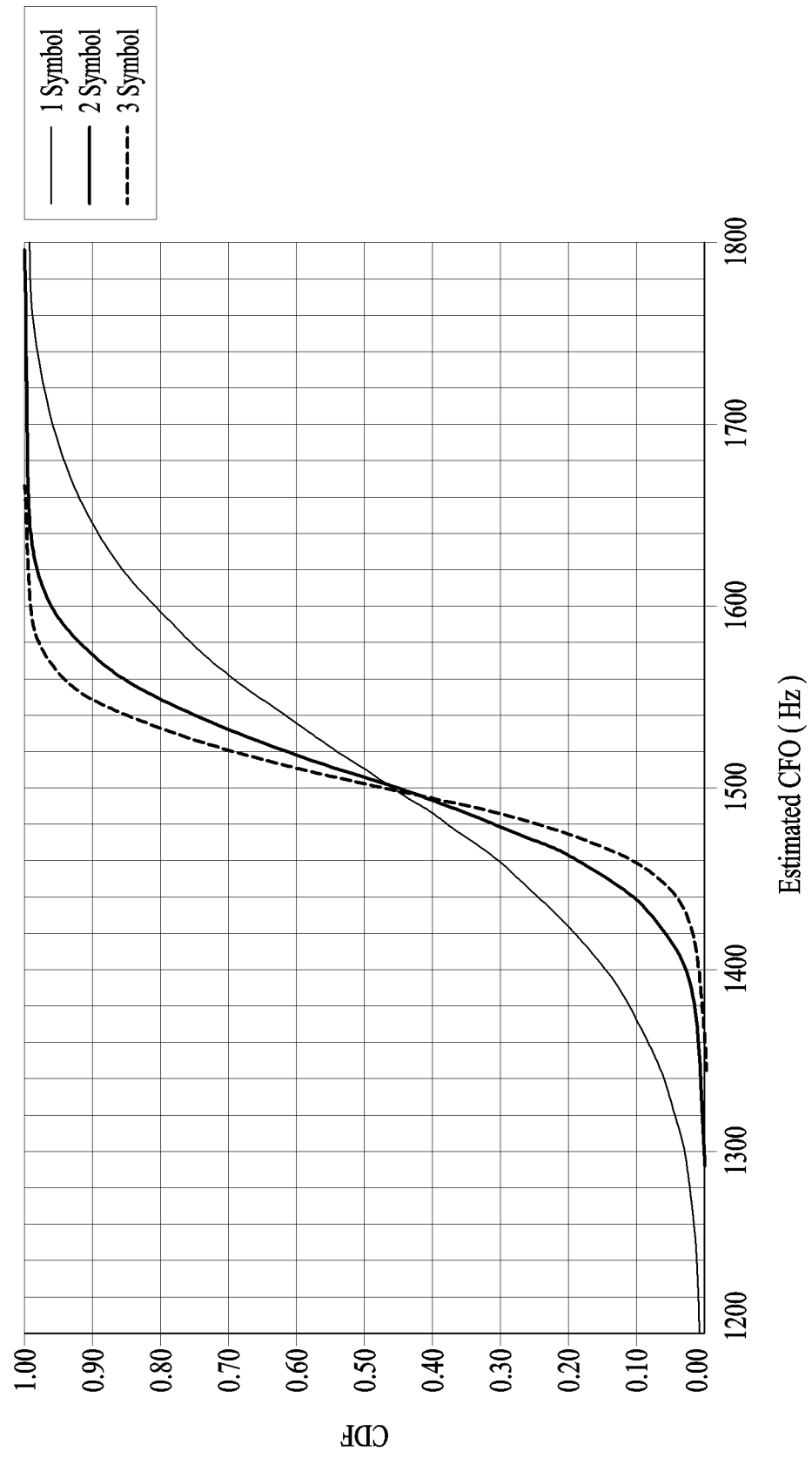


FIG. 32

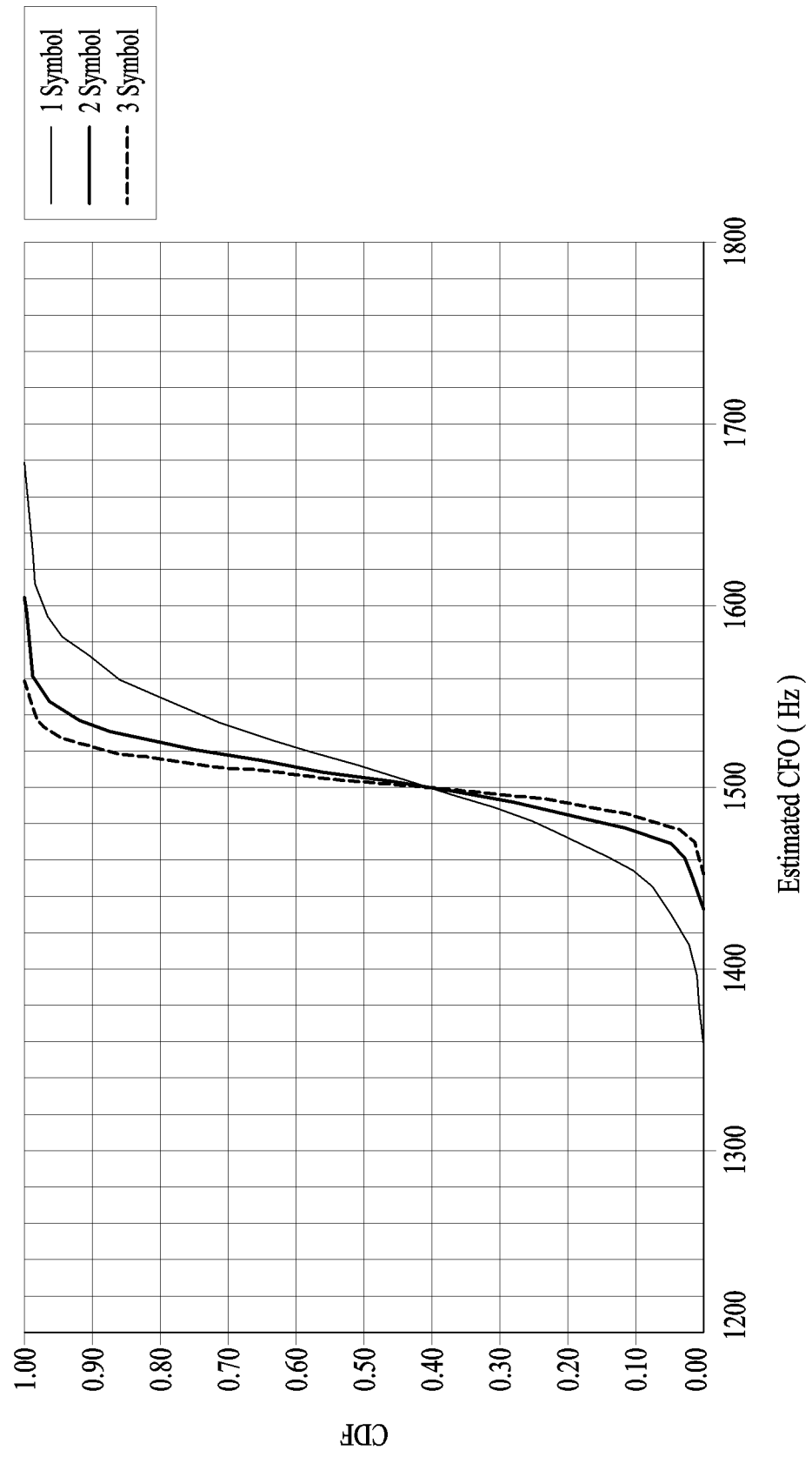


FIG. 33

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

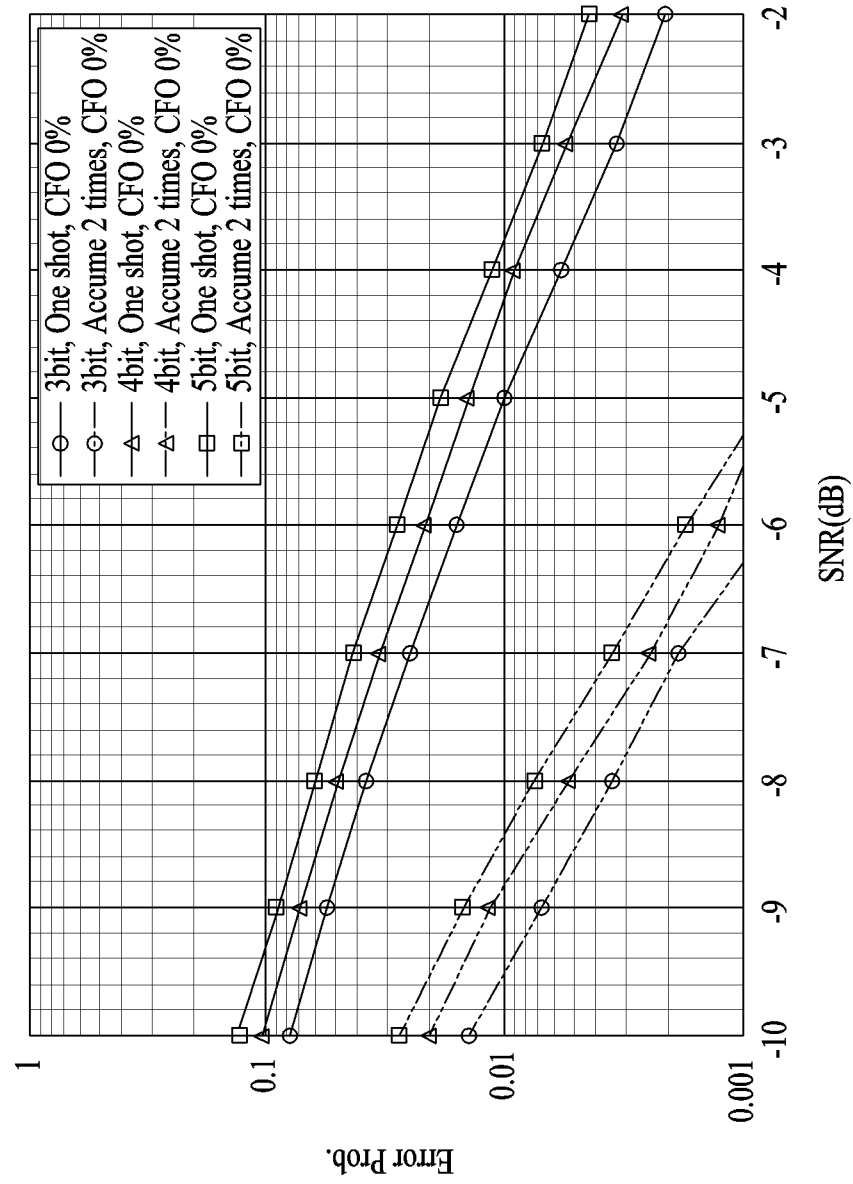


FIG. 34

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

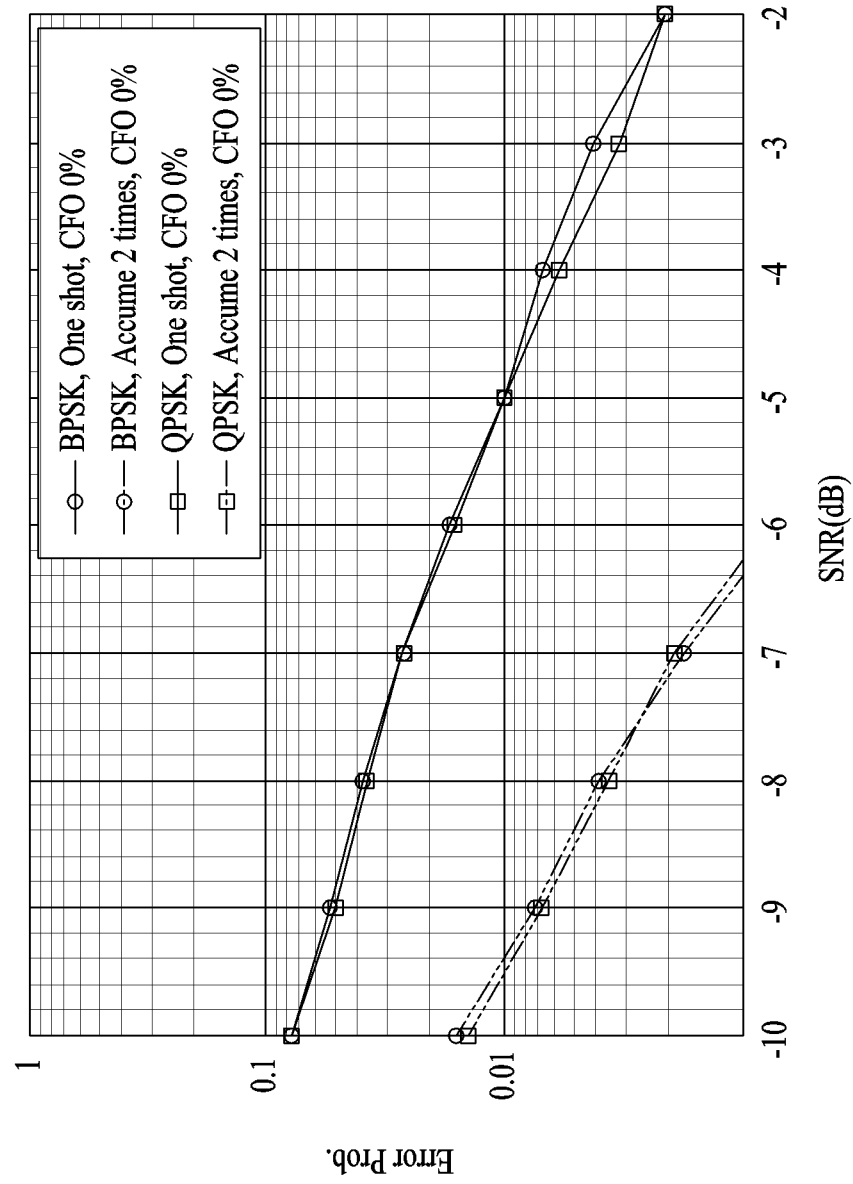


FIG. 35

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

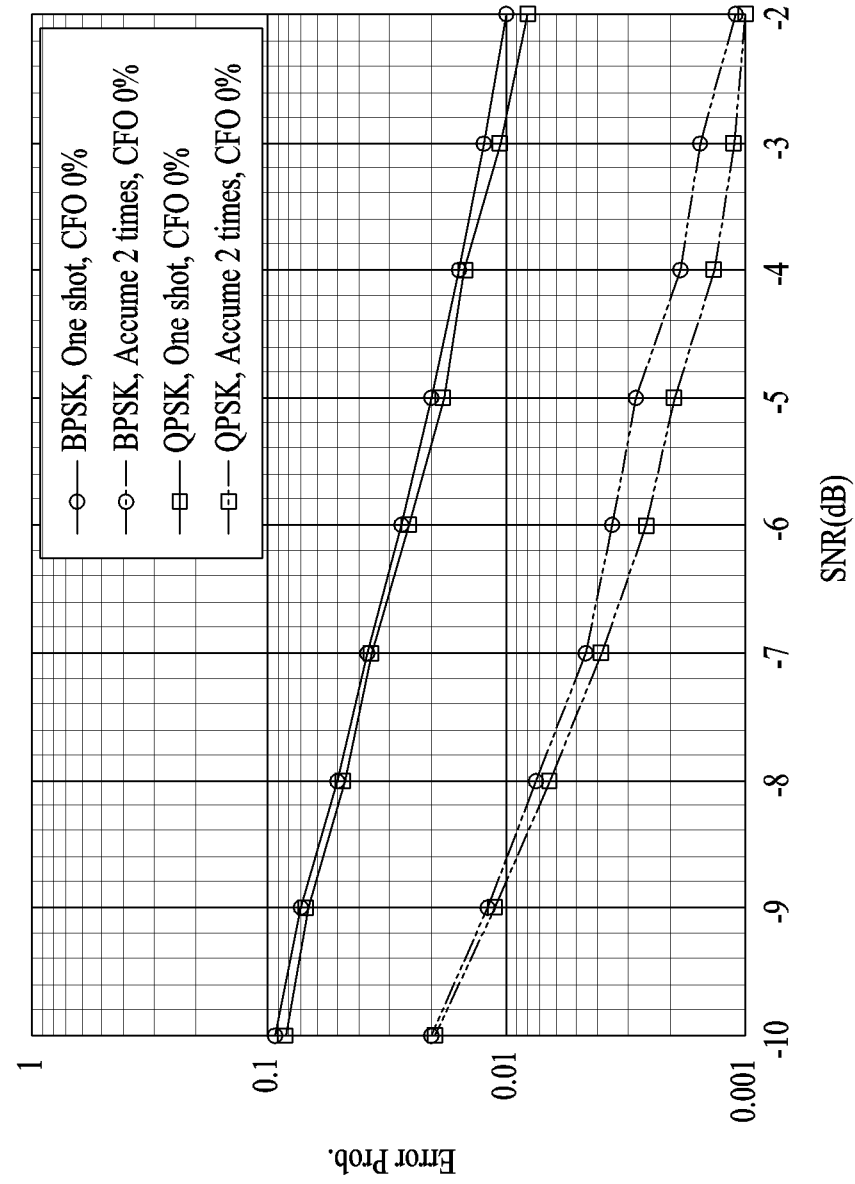


FIG. 36

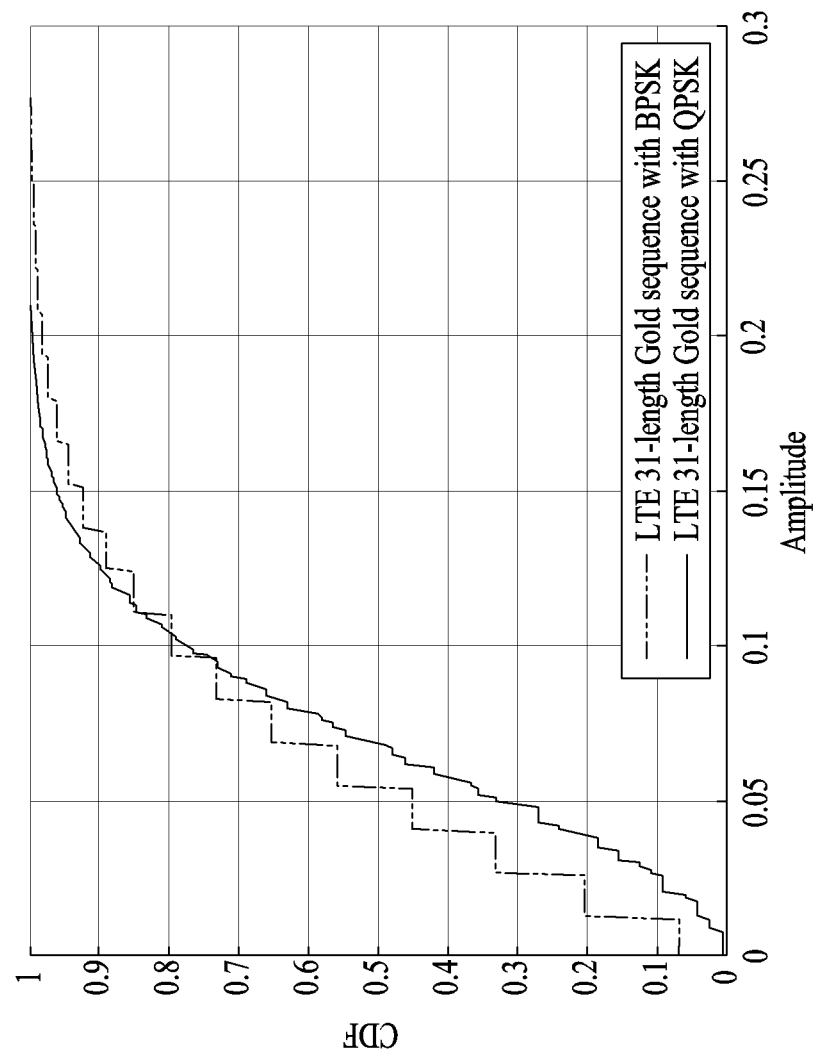


FIG. 37

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

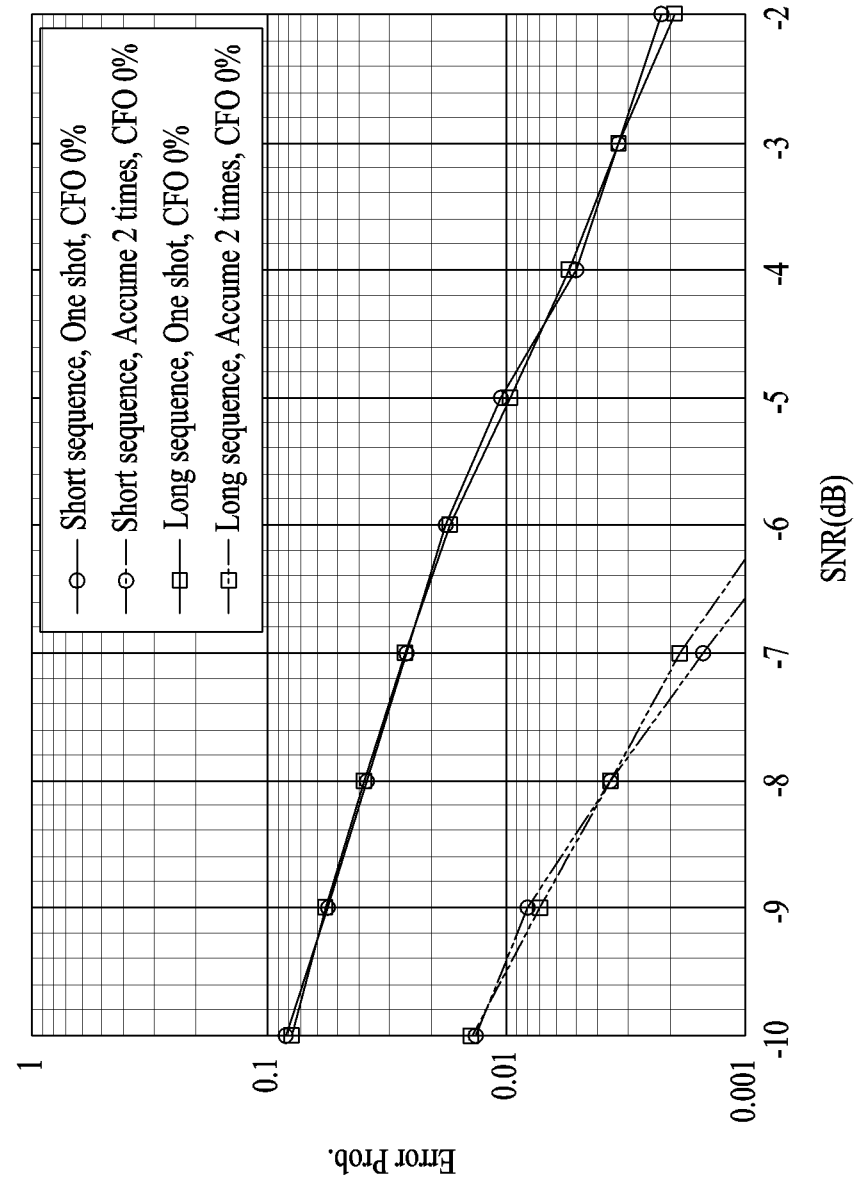


FIG. 38

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

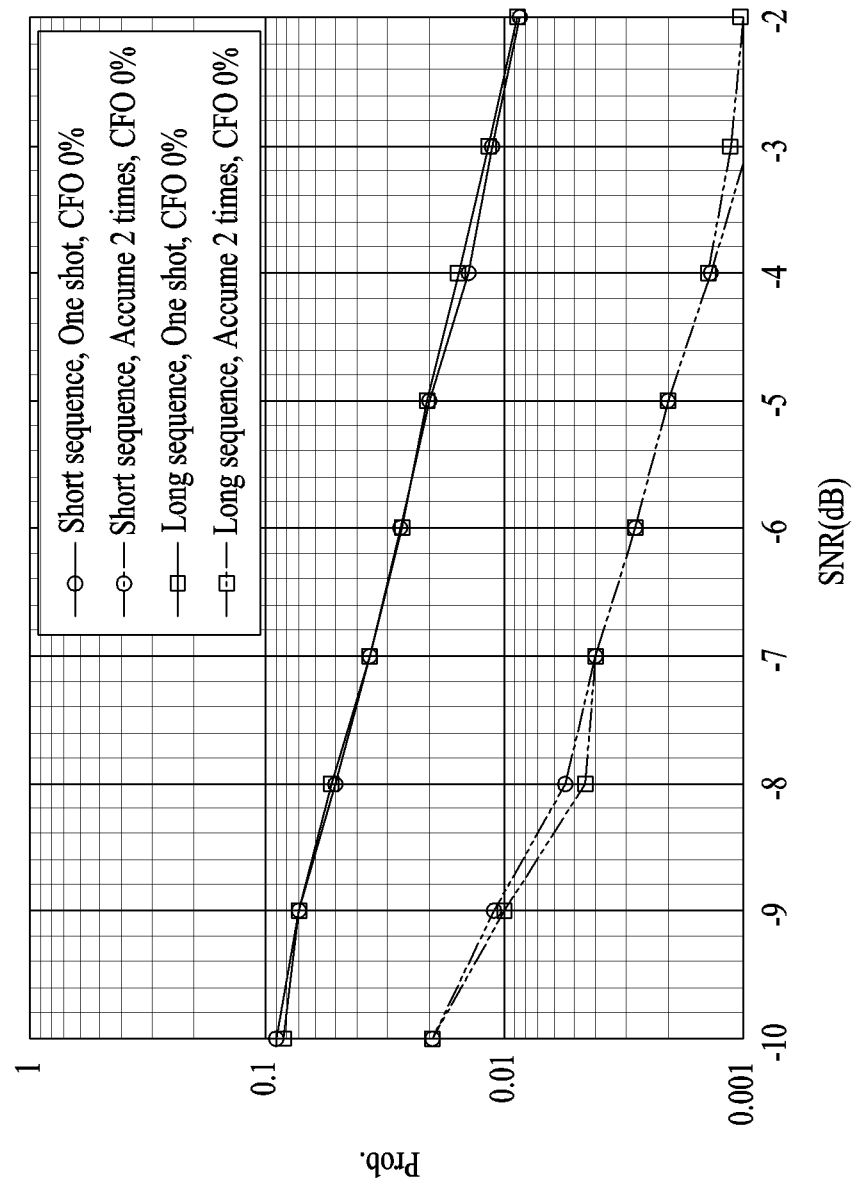


FIG. 39

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

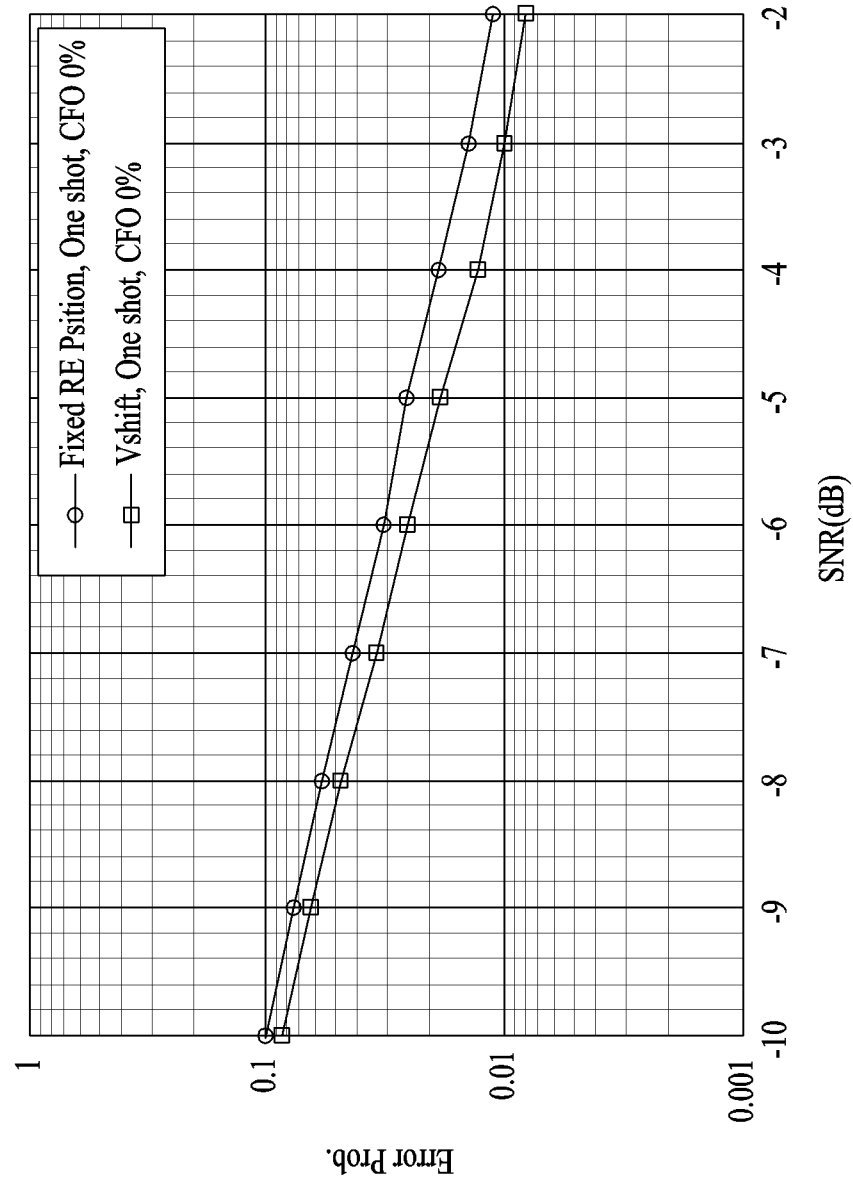


FIG. 40

CDL-C 100ms, 3km/h, 4GHz, SCS 15kHz

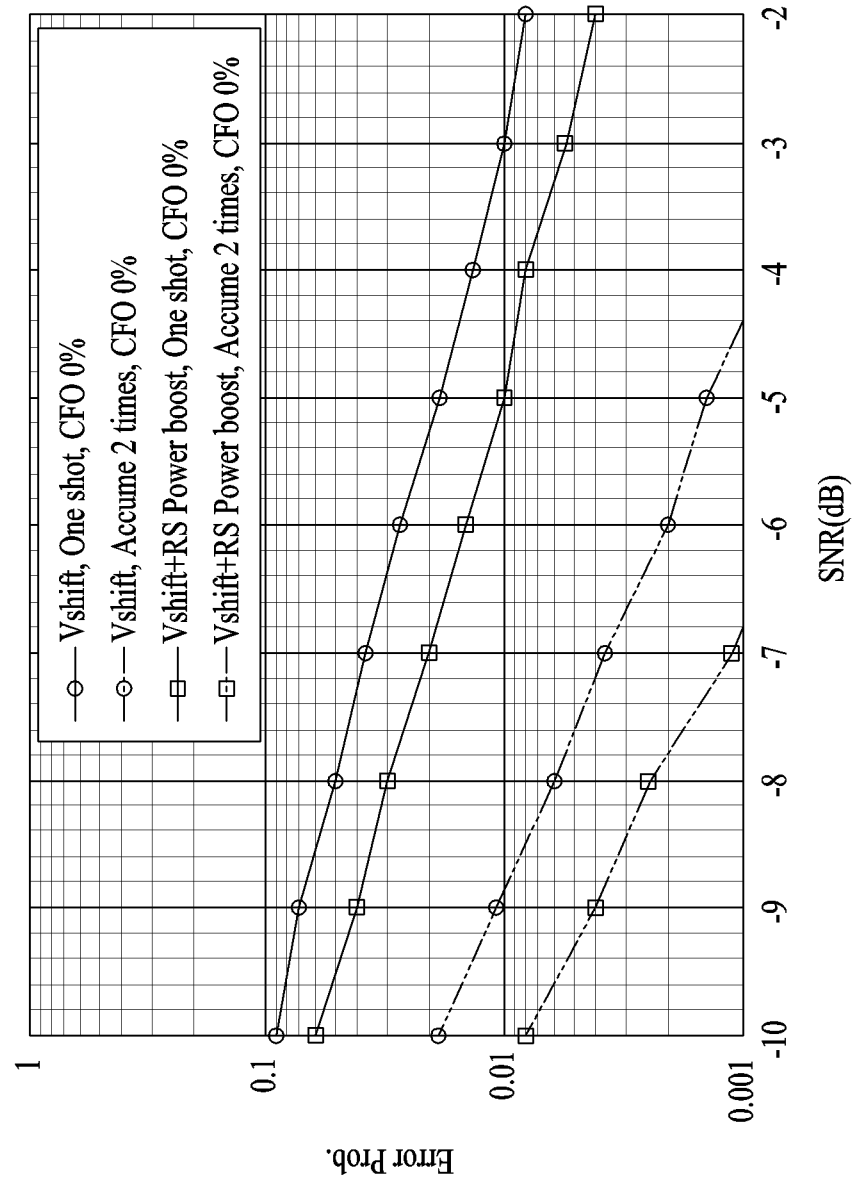


FIG. 41

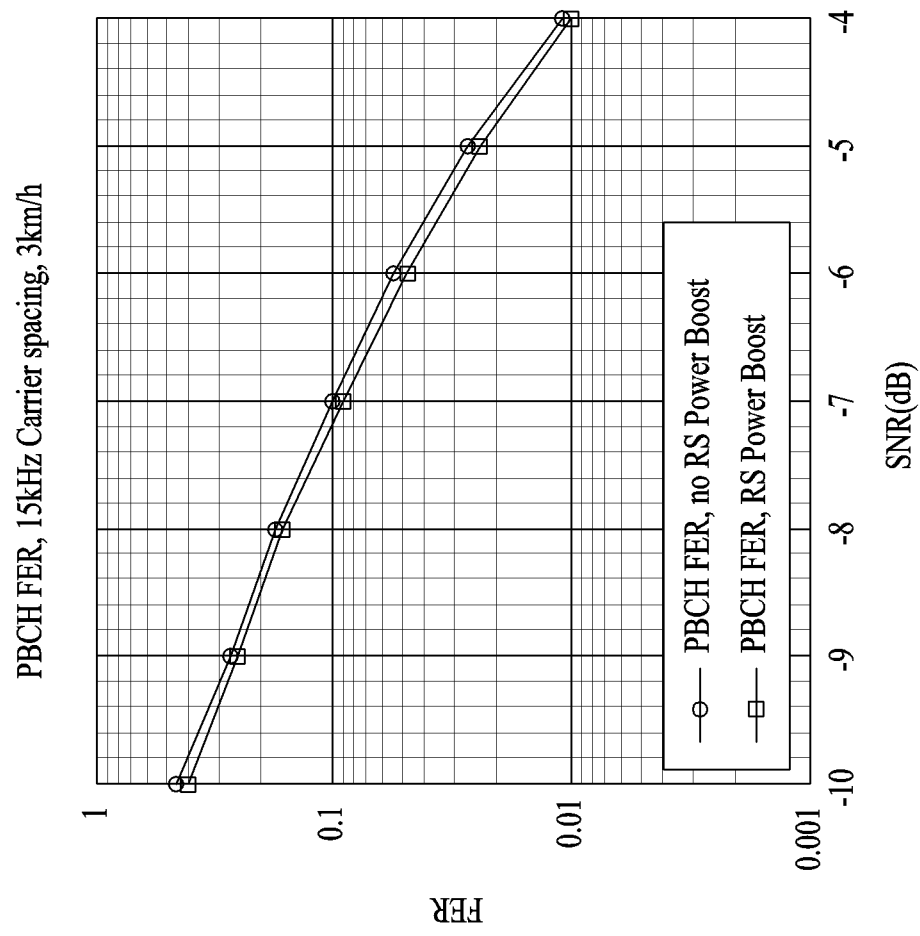


FIG. 42

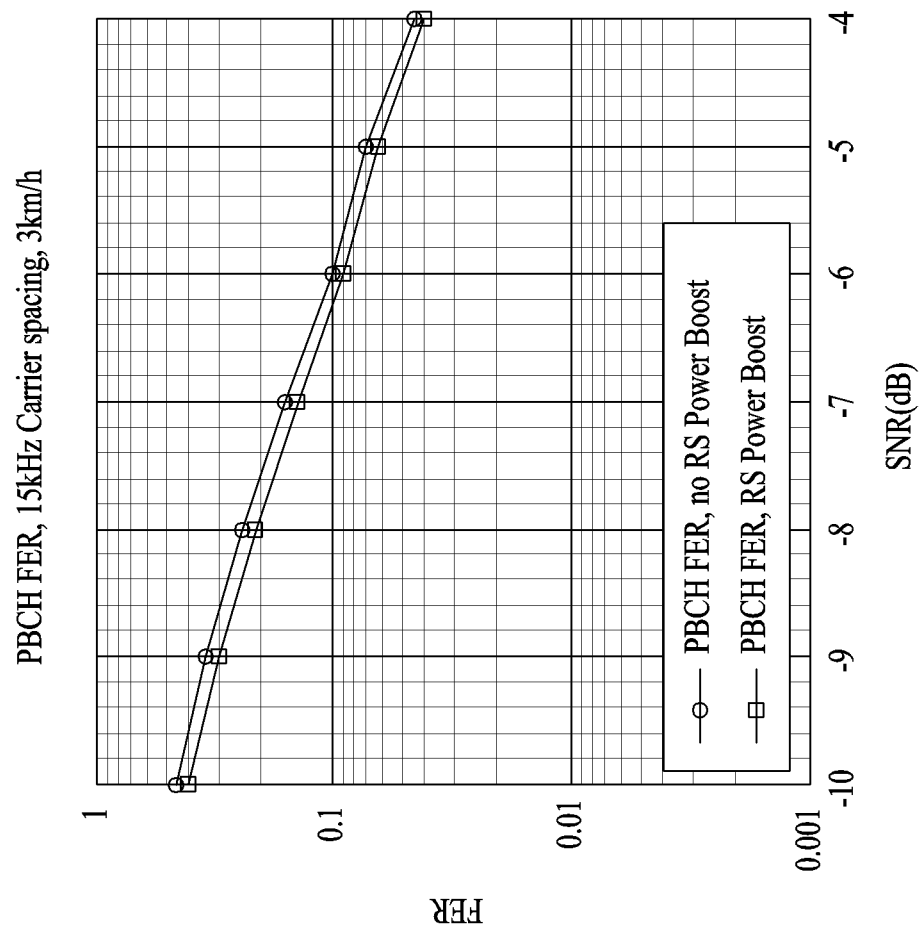


FIG. 43

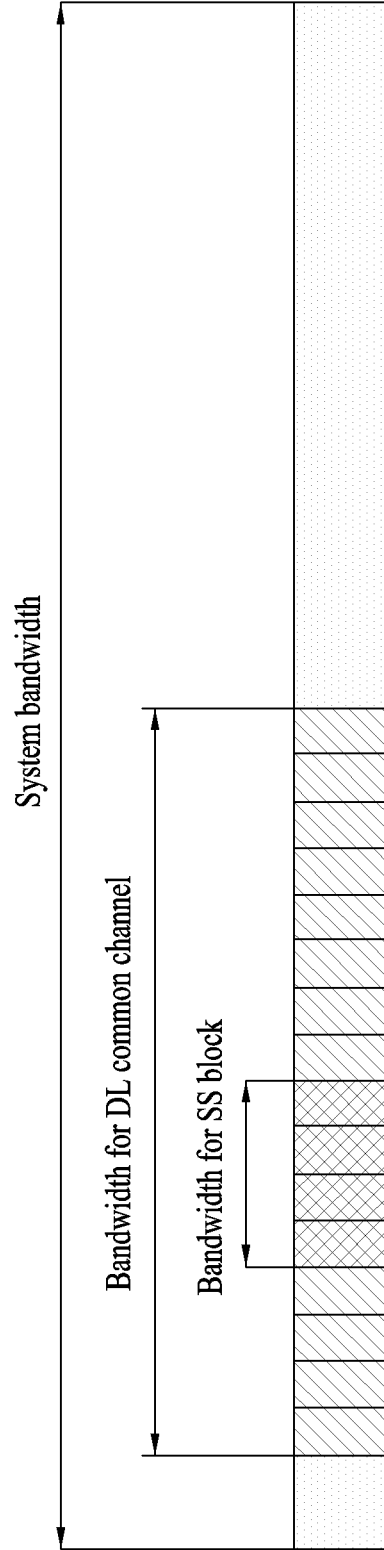


FIG. 44

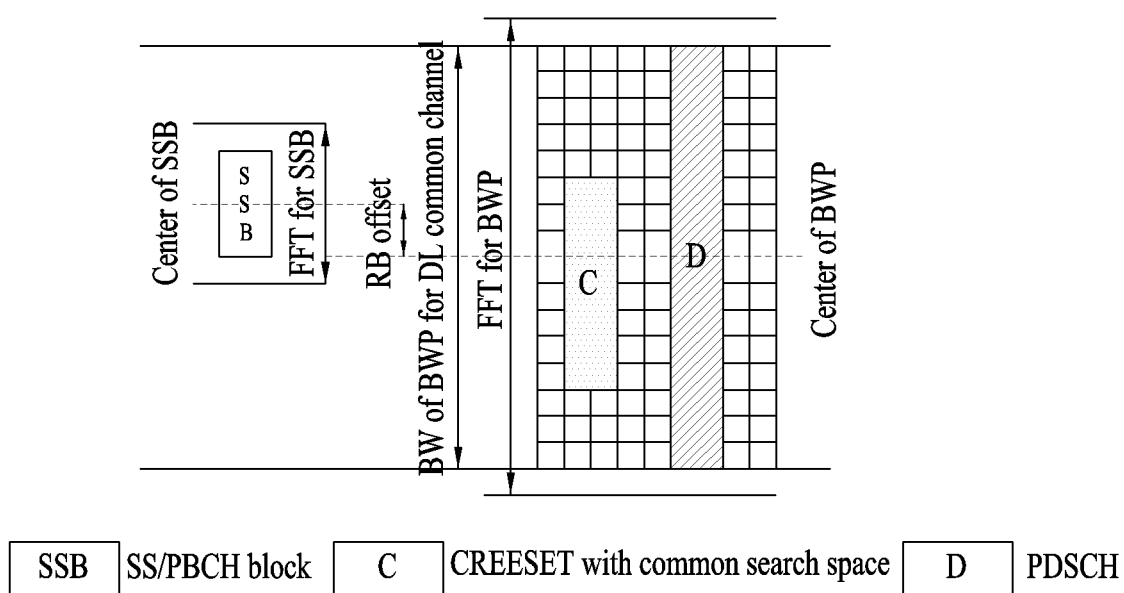


FIG. 45

