

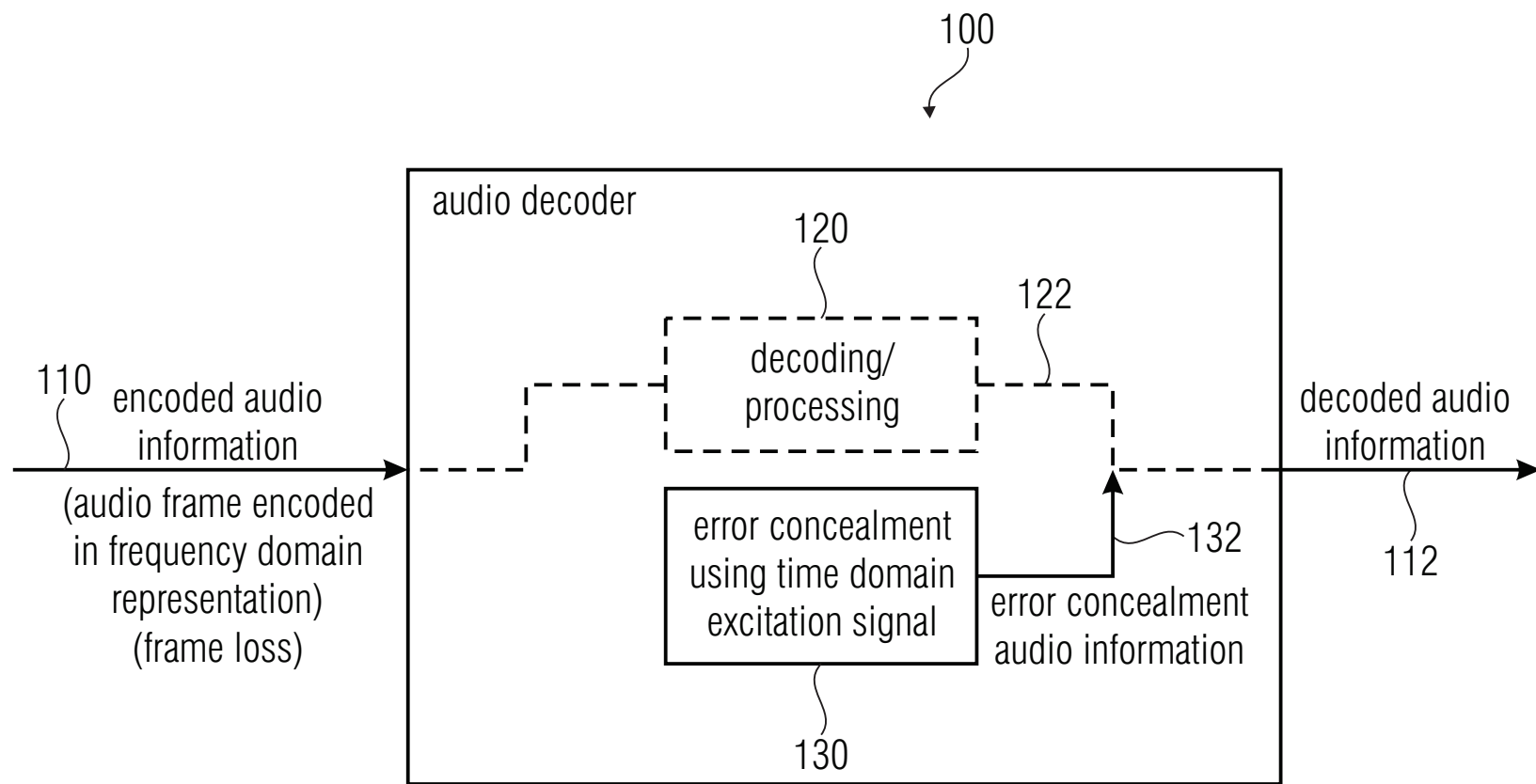


FIG 1

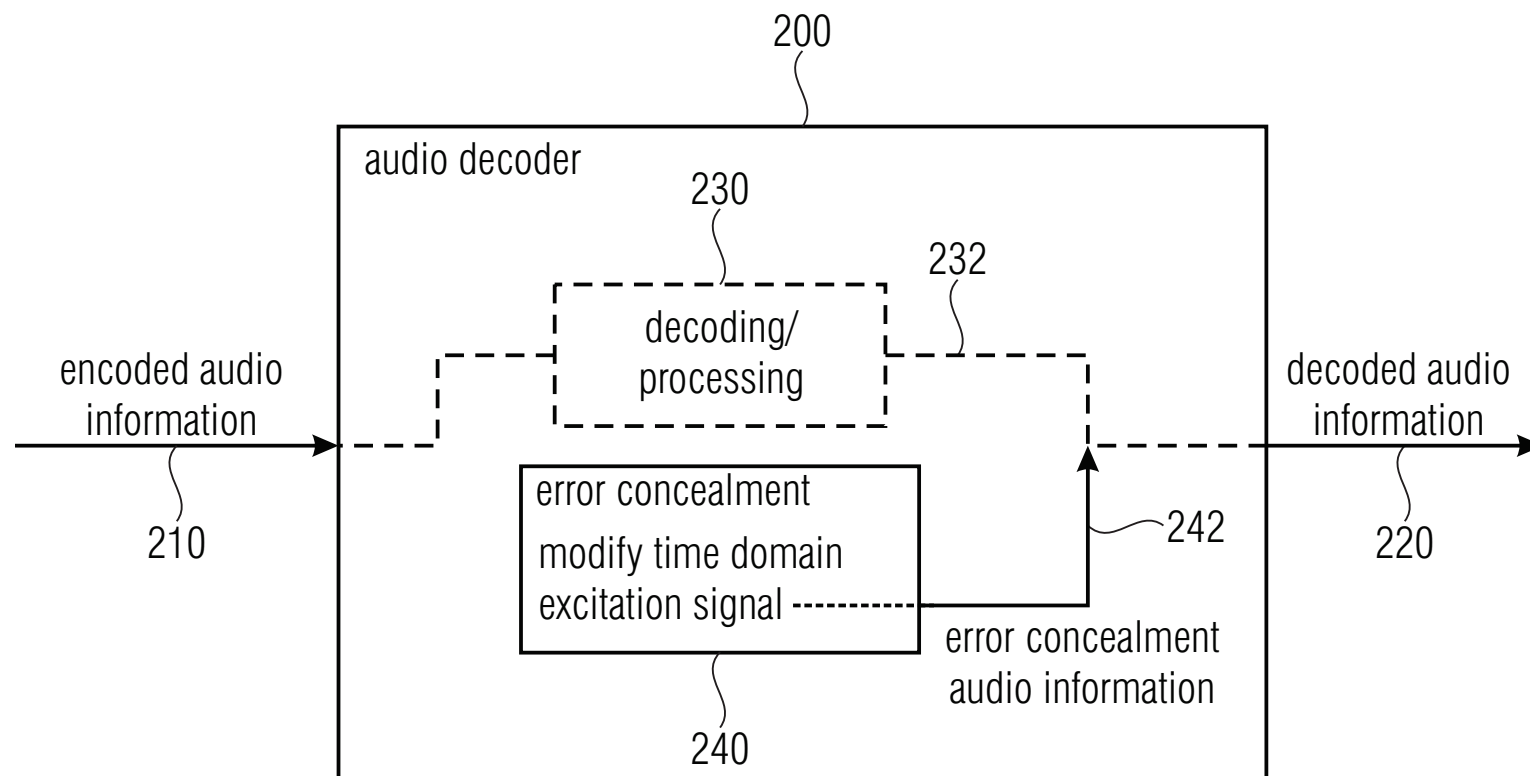


FIG 2

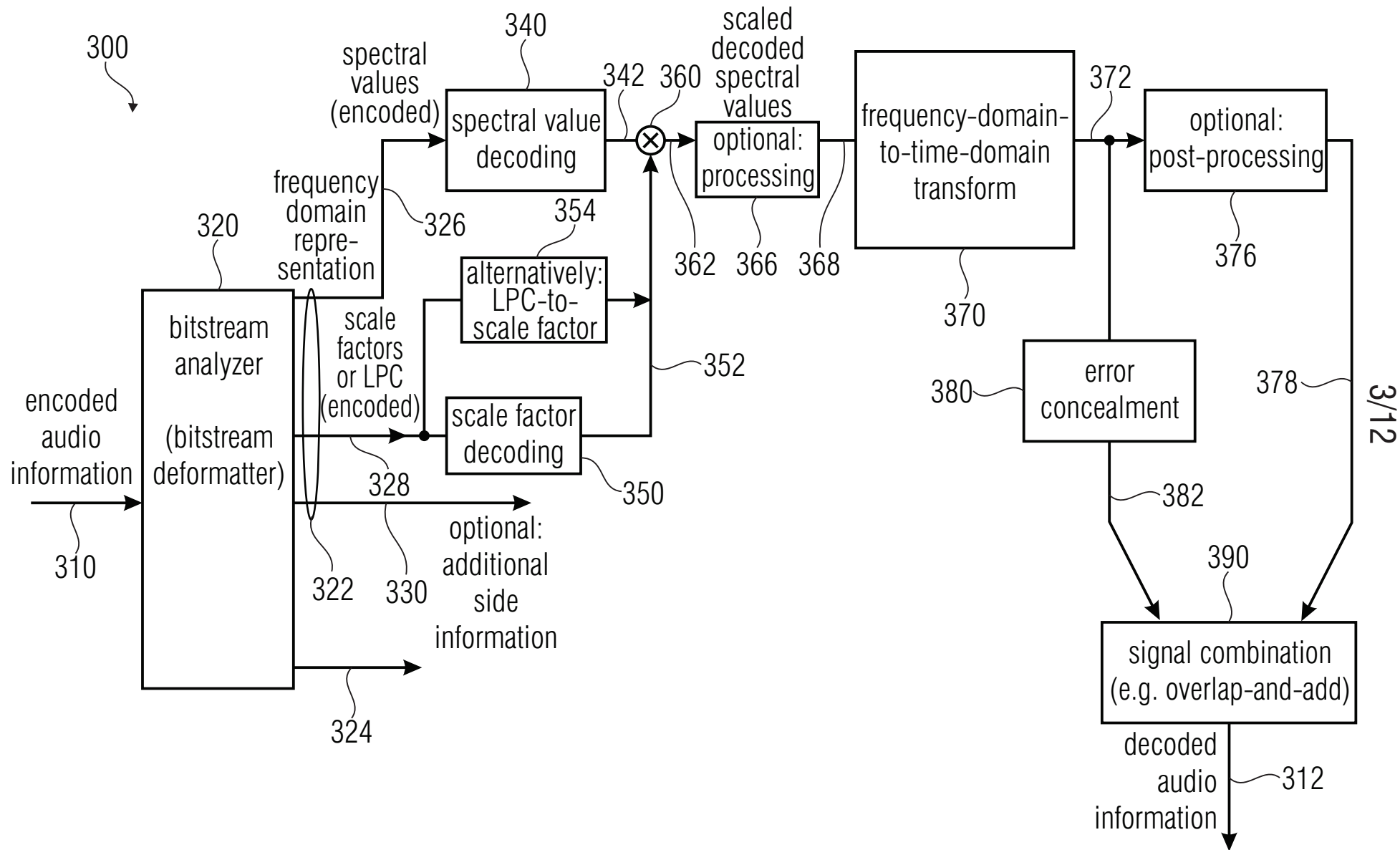


FIG 3

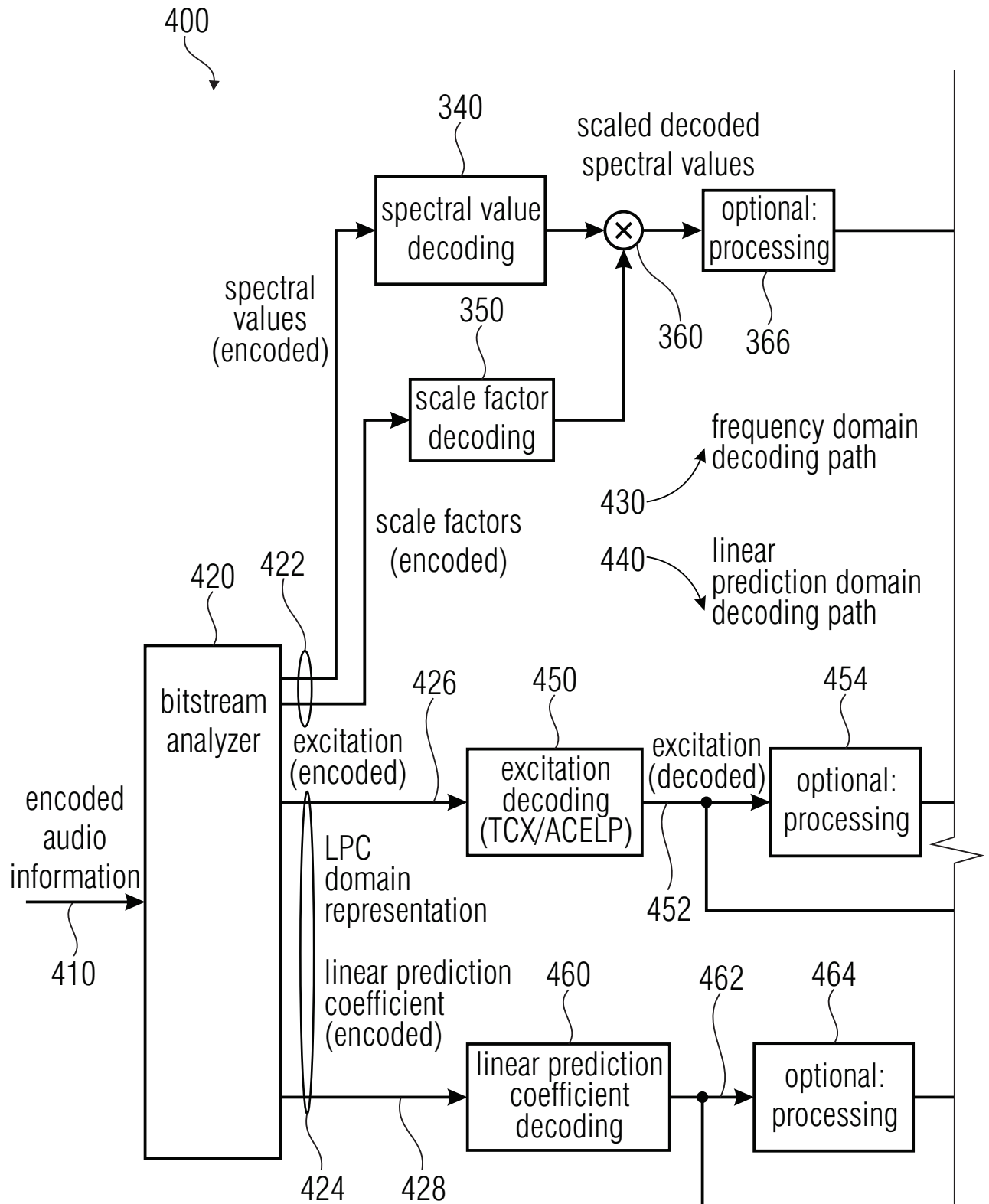


FIG 4	
FIG 4A	FIG 4B

FIG 4A

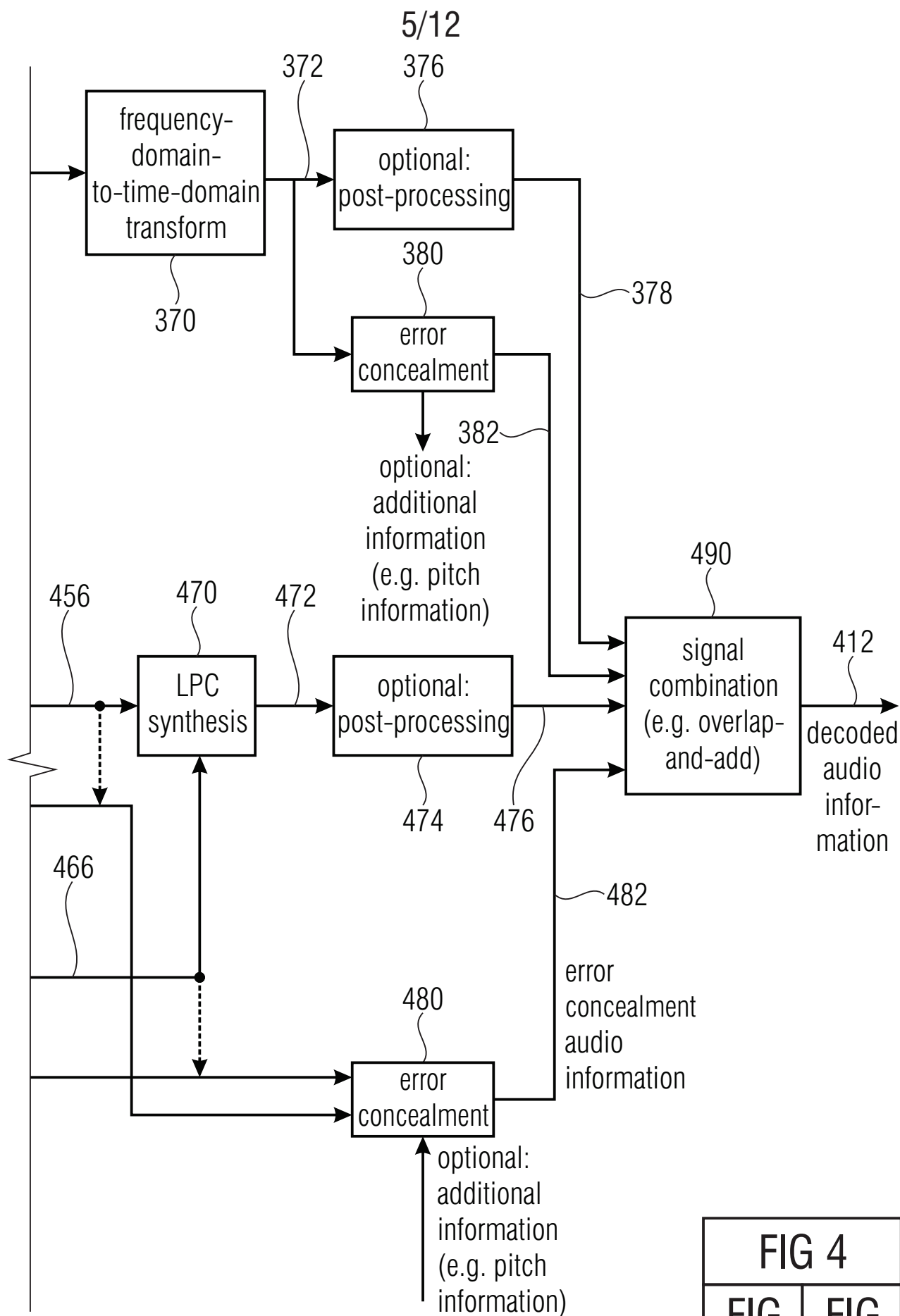
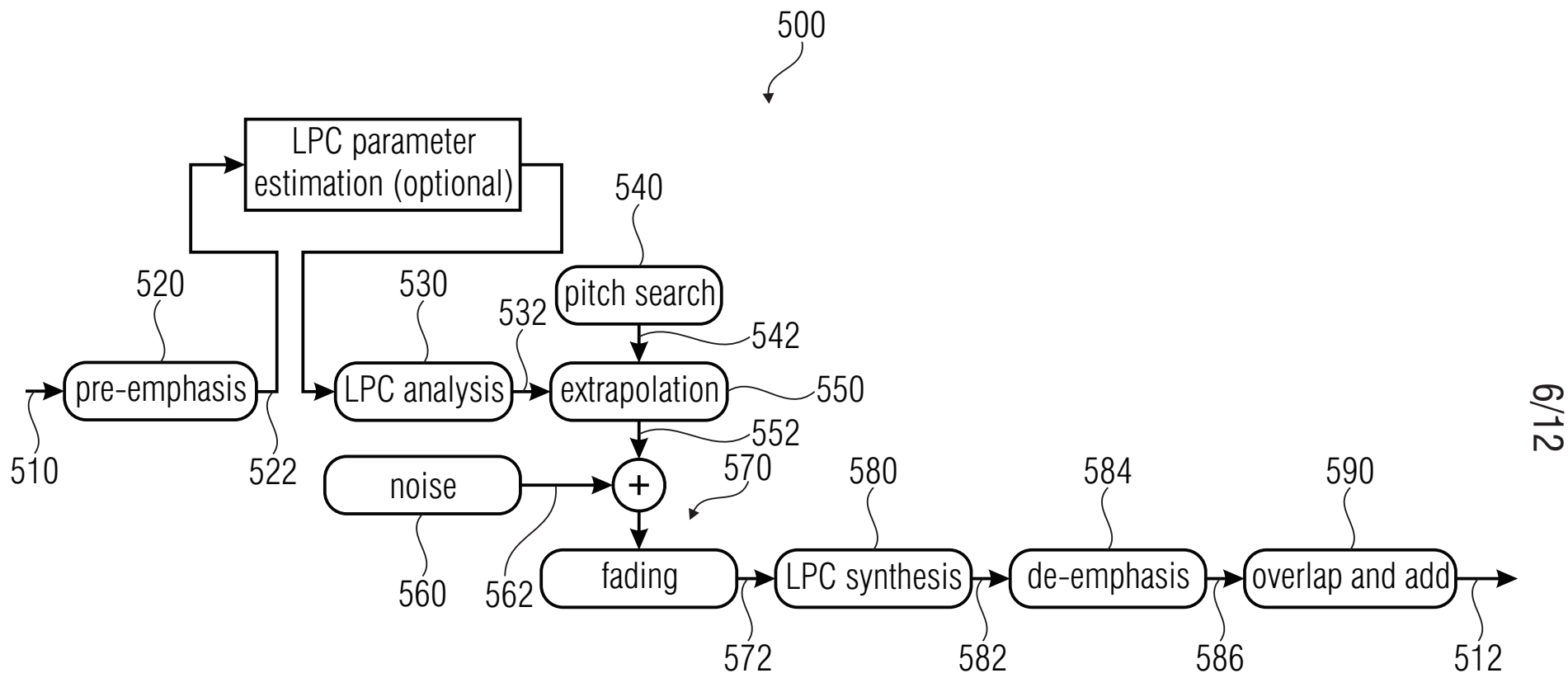
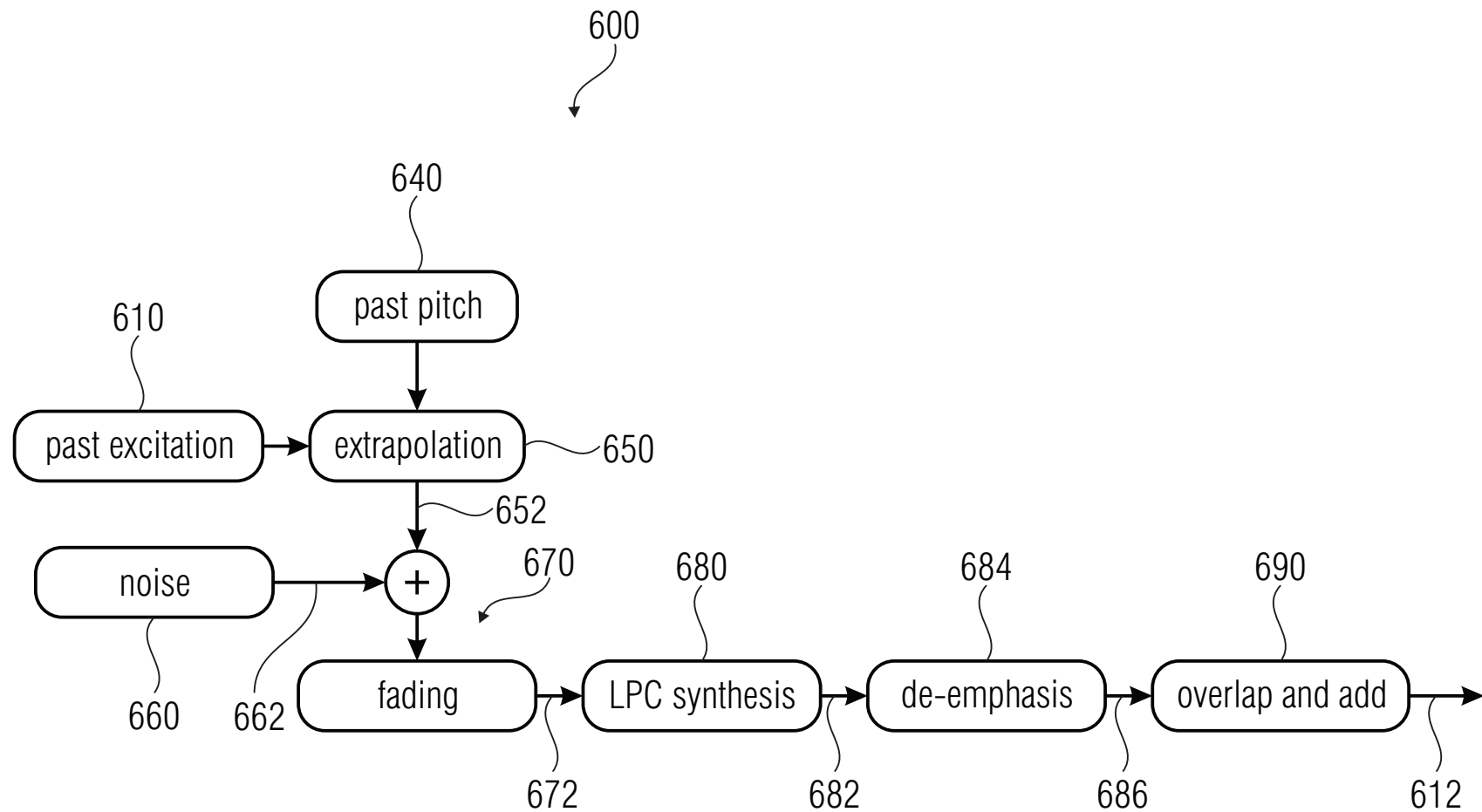


FIG 4B

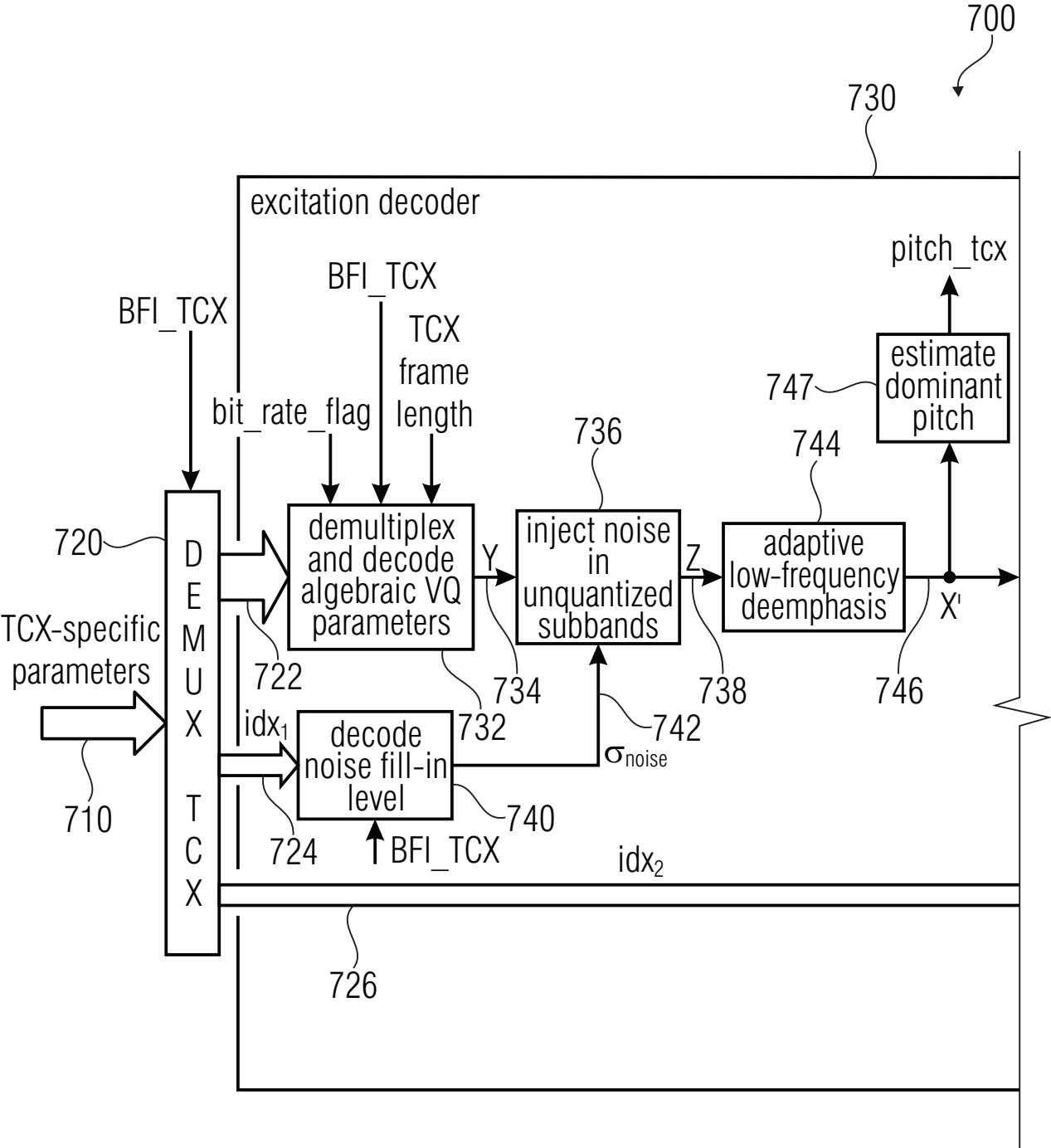
FIG 4	
FIG 4A	FIG 4B



Time domain concealment overview for transform decoder
FIG 5

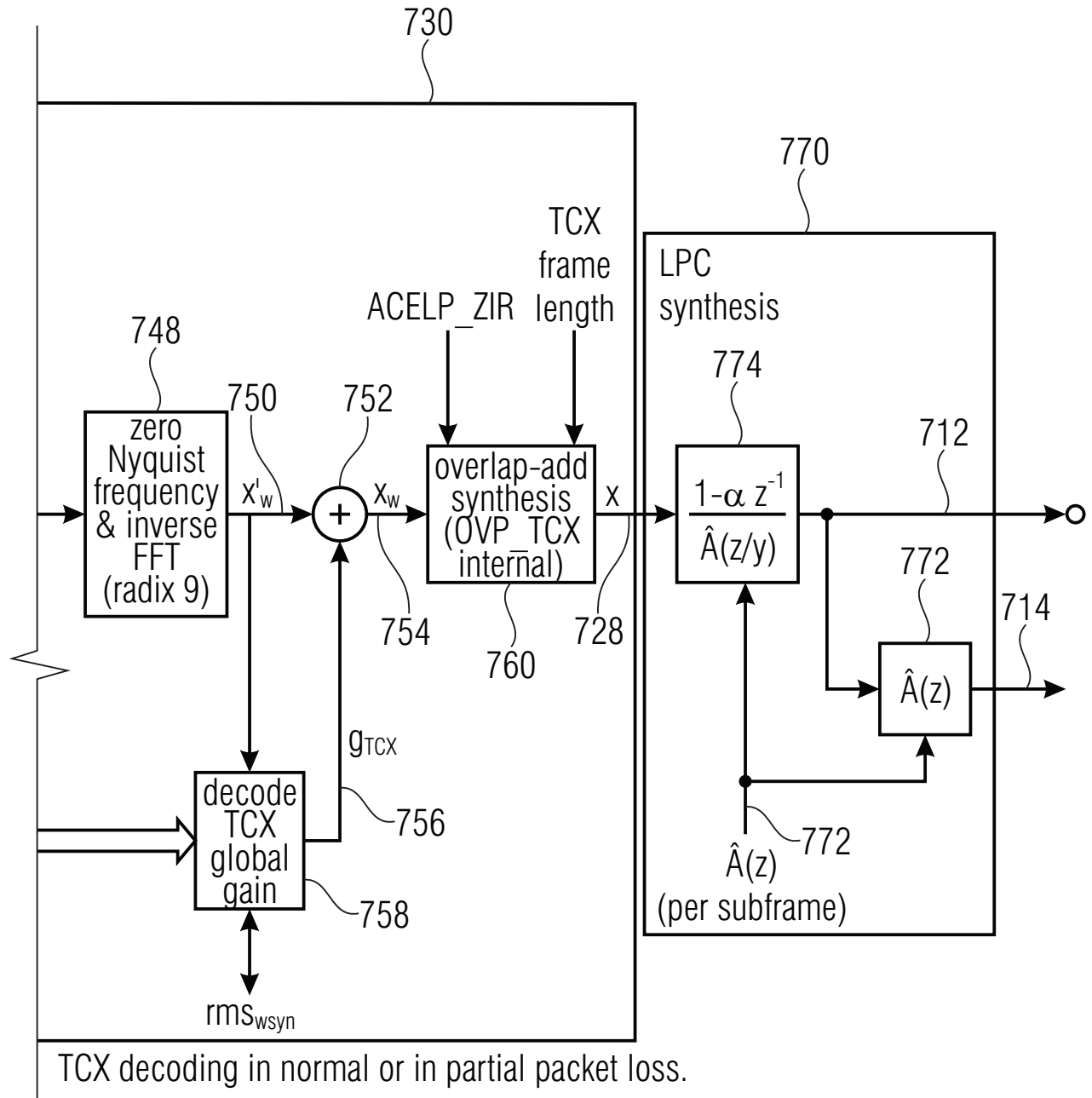


Time domain concealment overview for switch codec
FIG6



Block diagram of the TCX decoder
FIG 7A

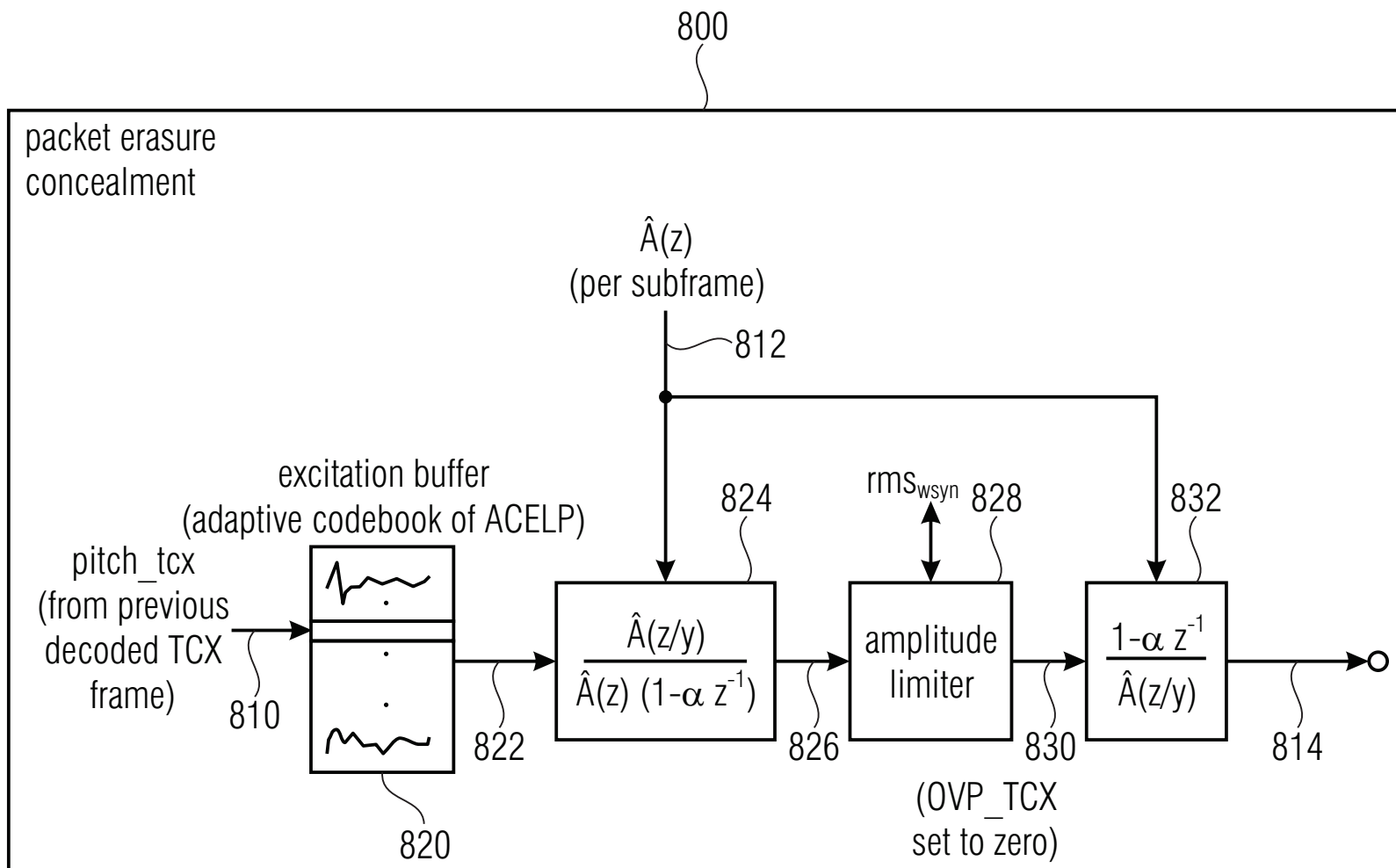
FIG 7	
FIG 7A	FIG 7B



Block diagram of the TCX decoder

FIG 7B

FIG 7	
FIG 7A	FIG 7B



TCX decoding in case of TCX-256 packet erasure concealment

Block diagram of the TCX decoder
FIG 8

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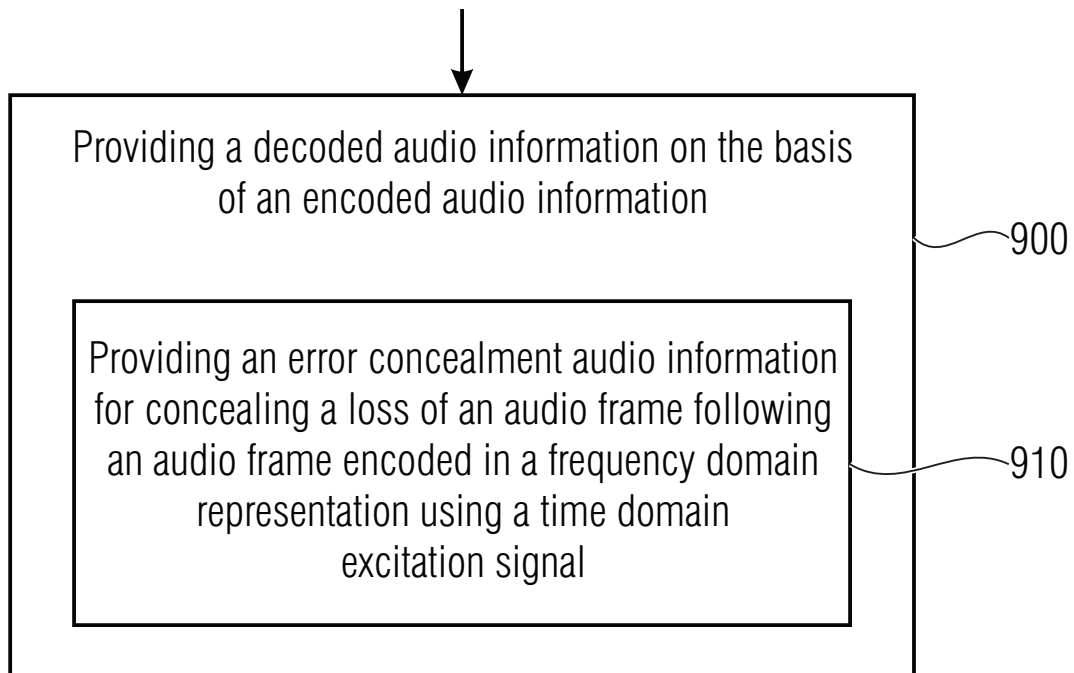


FIG 9

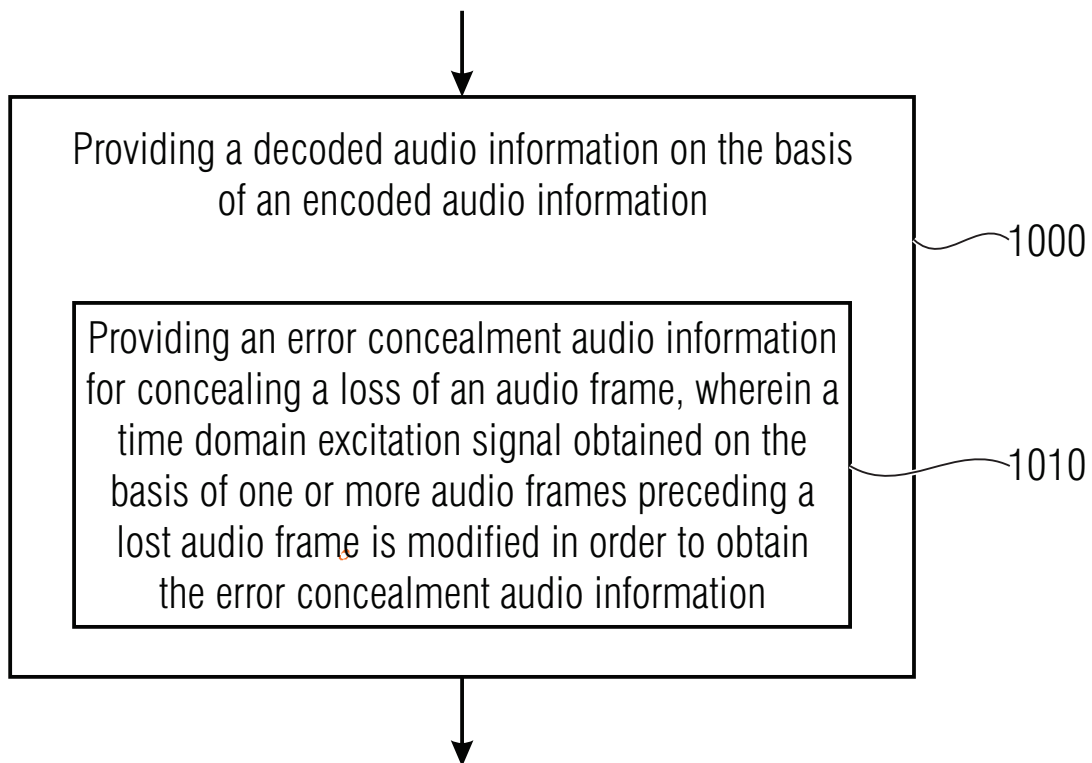


FIG 10

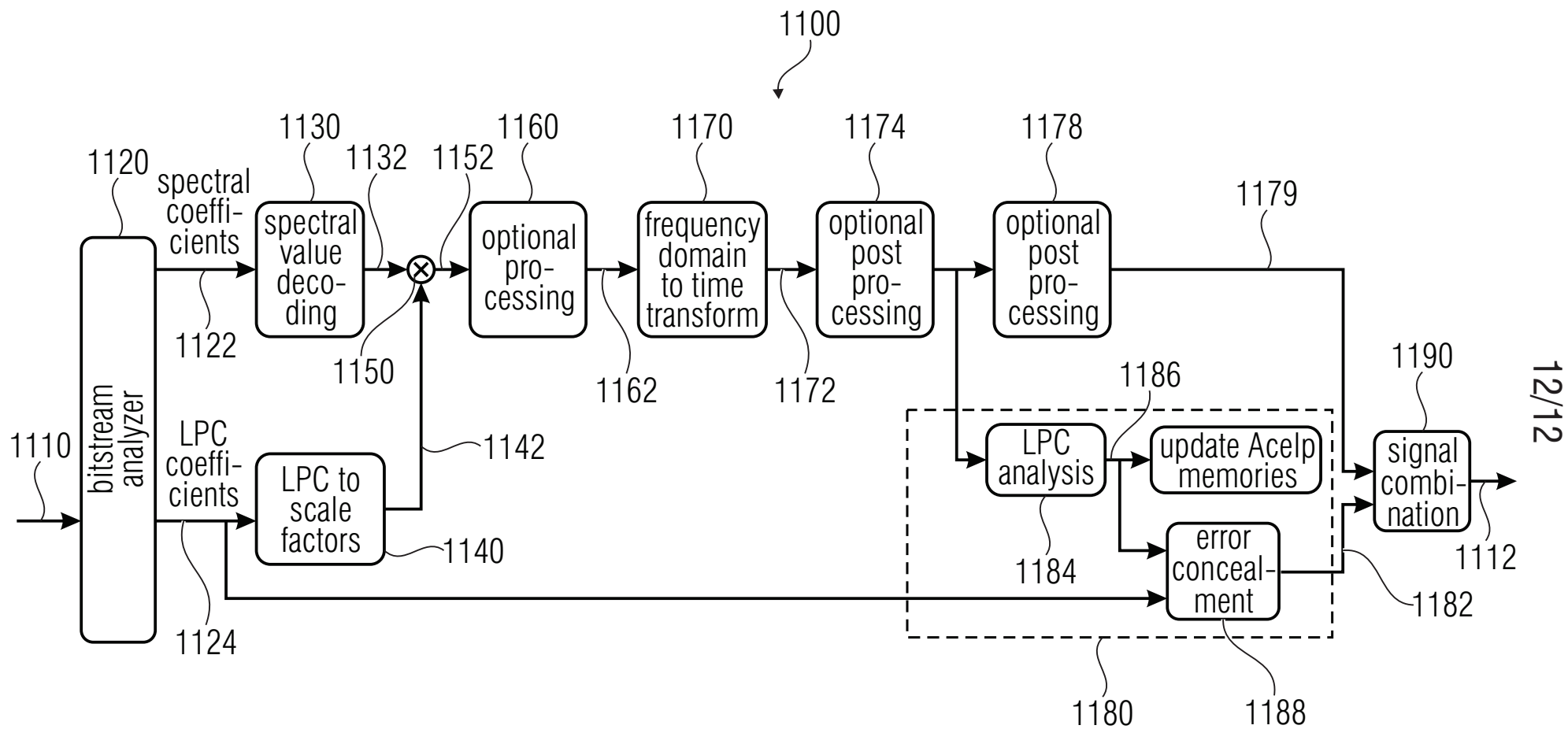


FIG 11