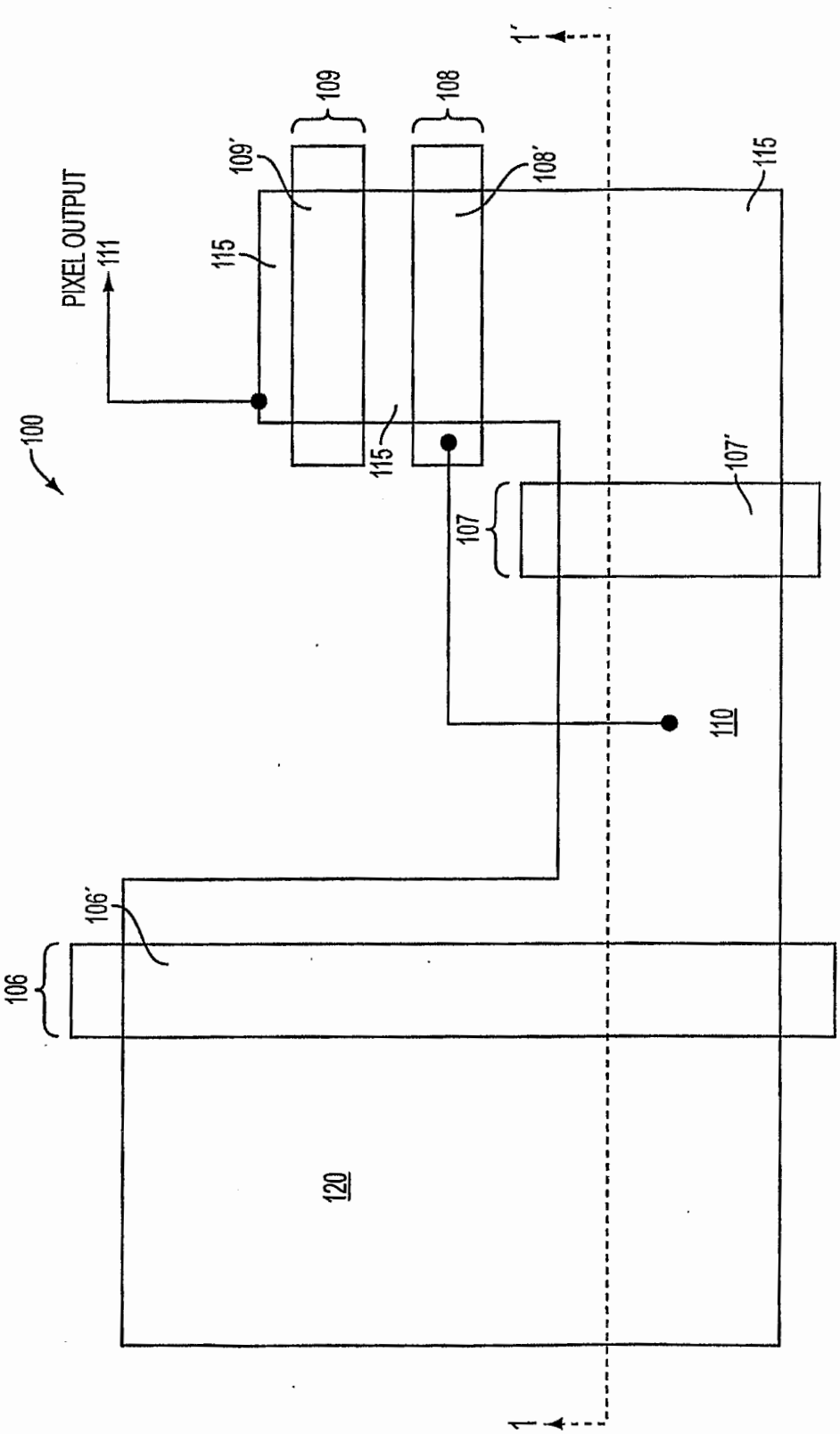


FIG. 1
PRIOR ART

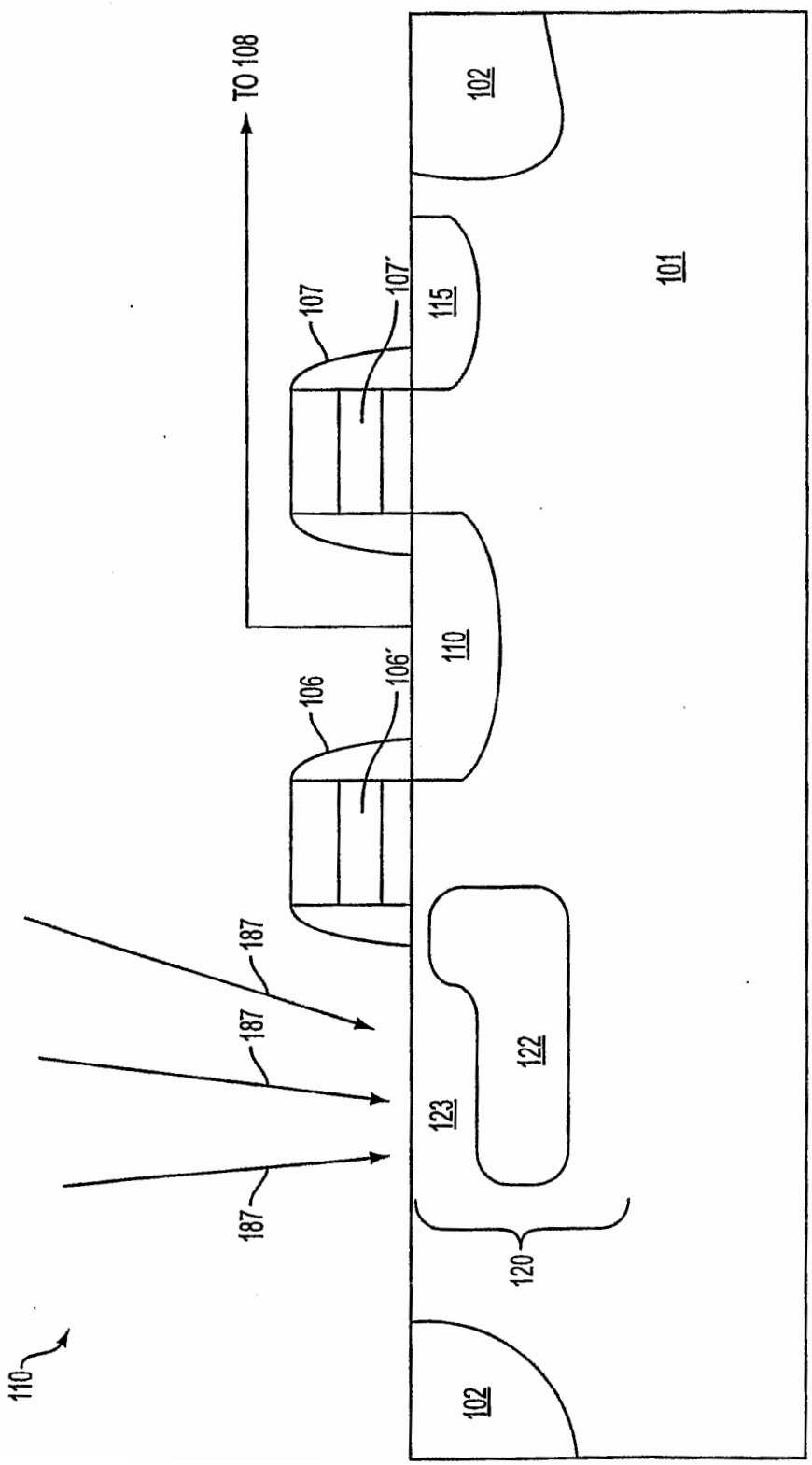


FIG. 2
PRIOR ART

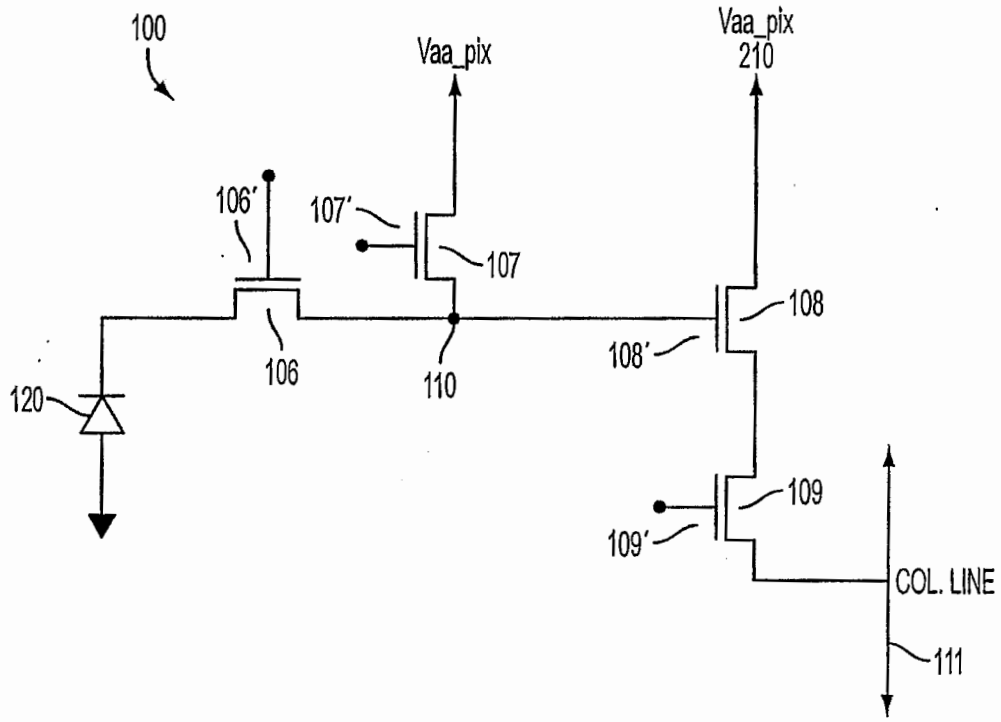


FIG. 3
PRIOR ART

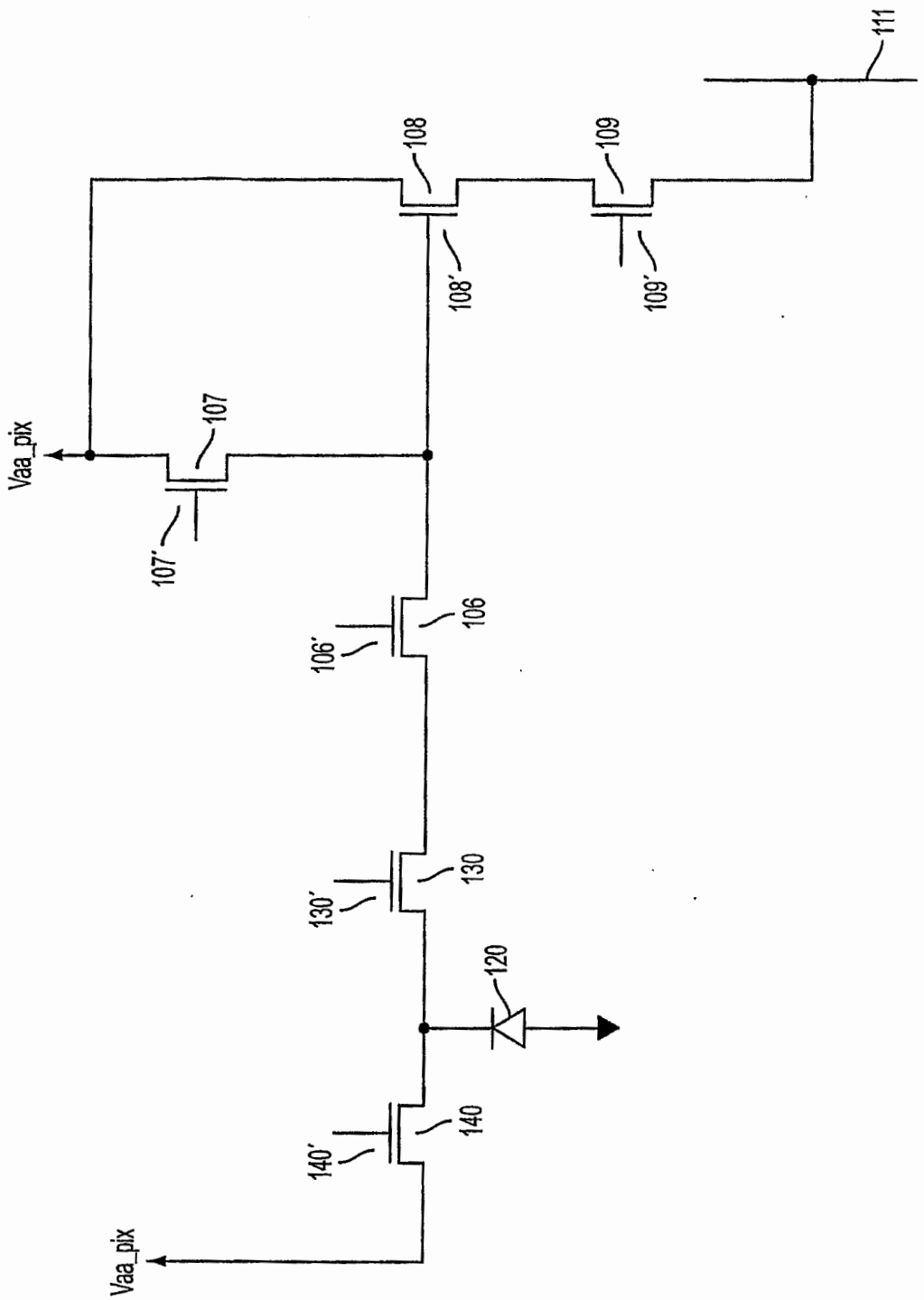


FIG. 4
PRIOR ART

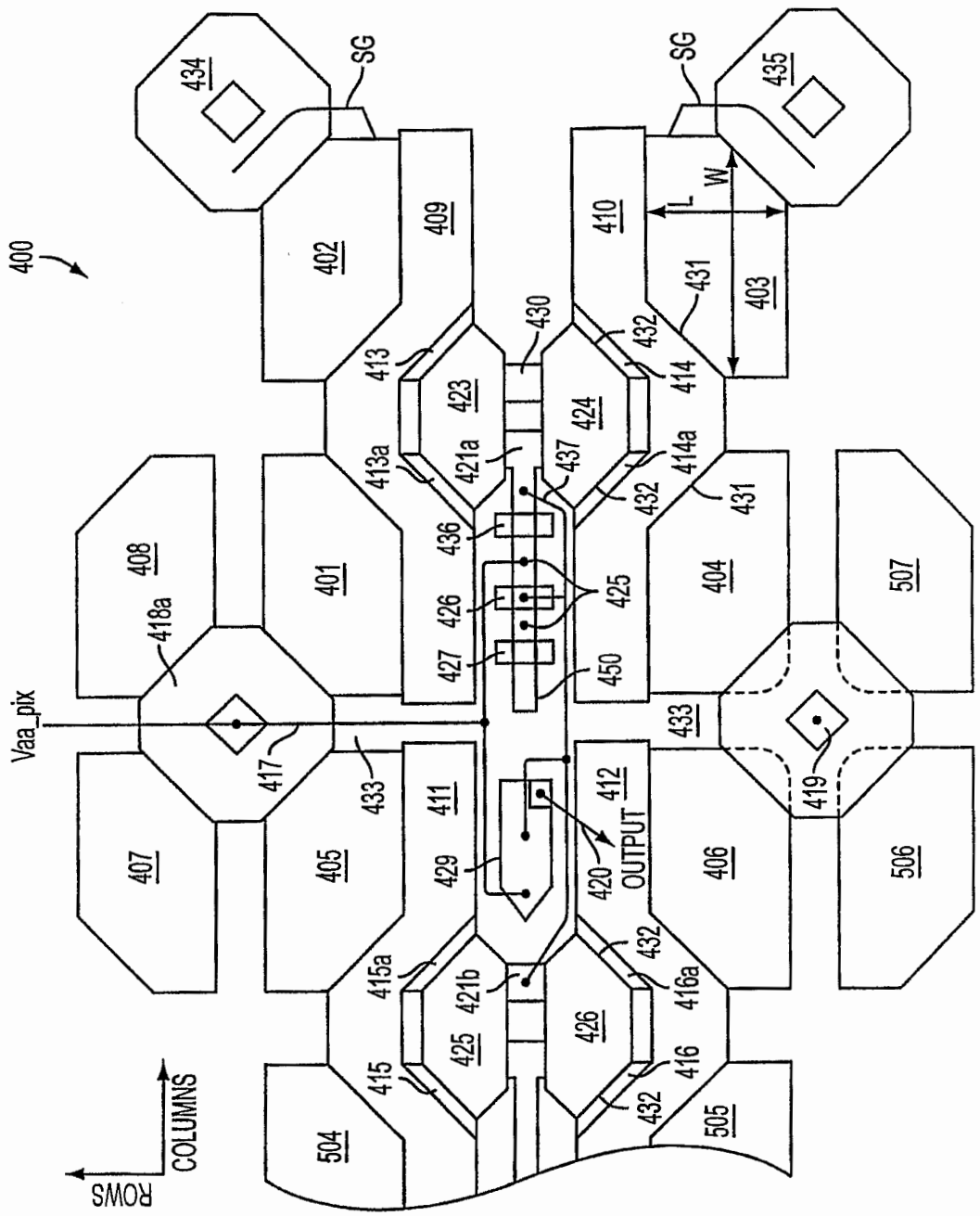

400
434
SG
435
402
409
410
L
W
403
431
430
432
413
423
424
414
413a
421a
437
414a
408
401
404
507
418a
436
426
425
427
450
Vaa_pix
417
433
411
412
419
407
405
429
420
OUTPUT
406
506
415a
421b
426
416a
425
415
416
504
505
COLUMNS
ROWS

FIG. 5

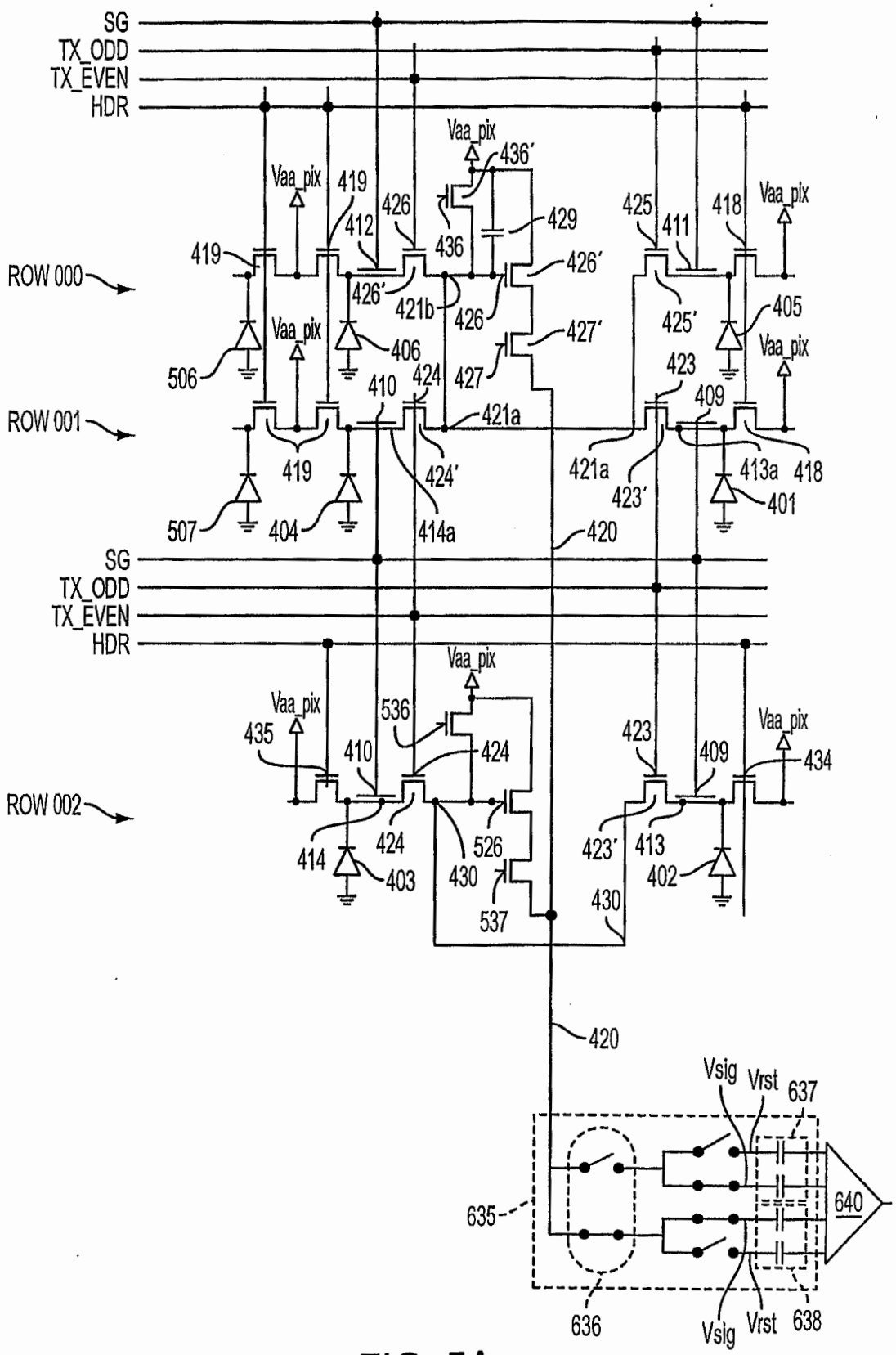

SG
TX_ODD
TX_EVEN
HDR
ROW 000
ROW 001
ROW 002
Vaa_pix
419
412
426
436'
436
429
426'
421b
427'
427
425
411
418
405
425'
423
409
421a
413a
401
423'
506
507
404
406
410
424
424'
414a
420
435
536
526
430
537
414
403
413
402
434
Vsig
Vrst
637
635
640
636
638

FIG. 5A

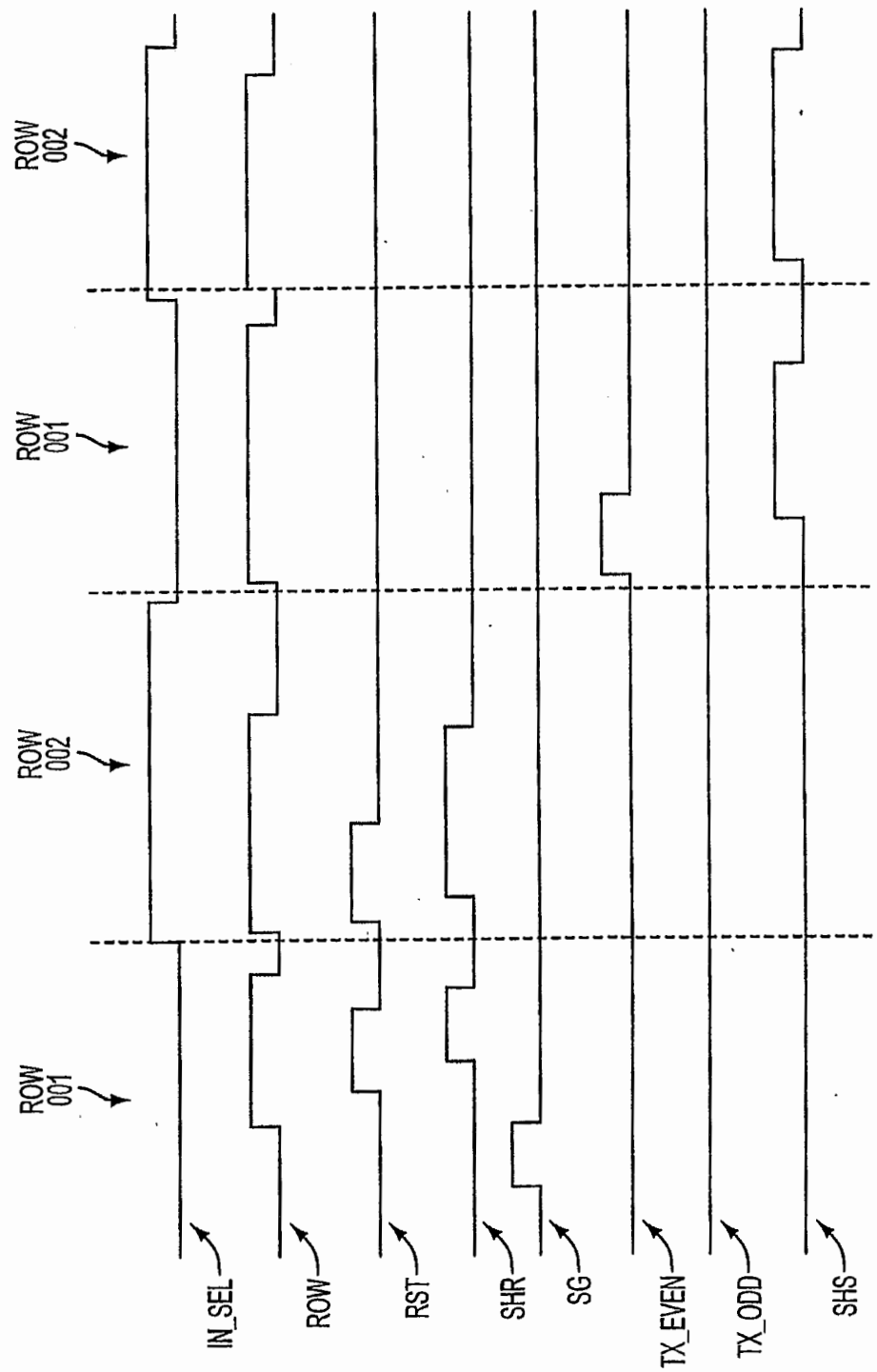

ROW 001
ROW 002
ROW 001
ROW 002
IN_SEL
ROW
RST
SHR
SG
TX_EVEN
TX_ODD
SHS

FIG. 6

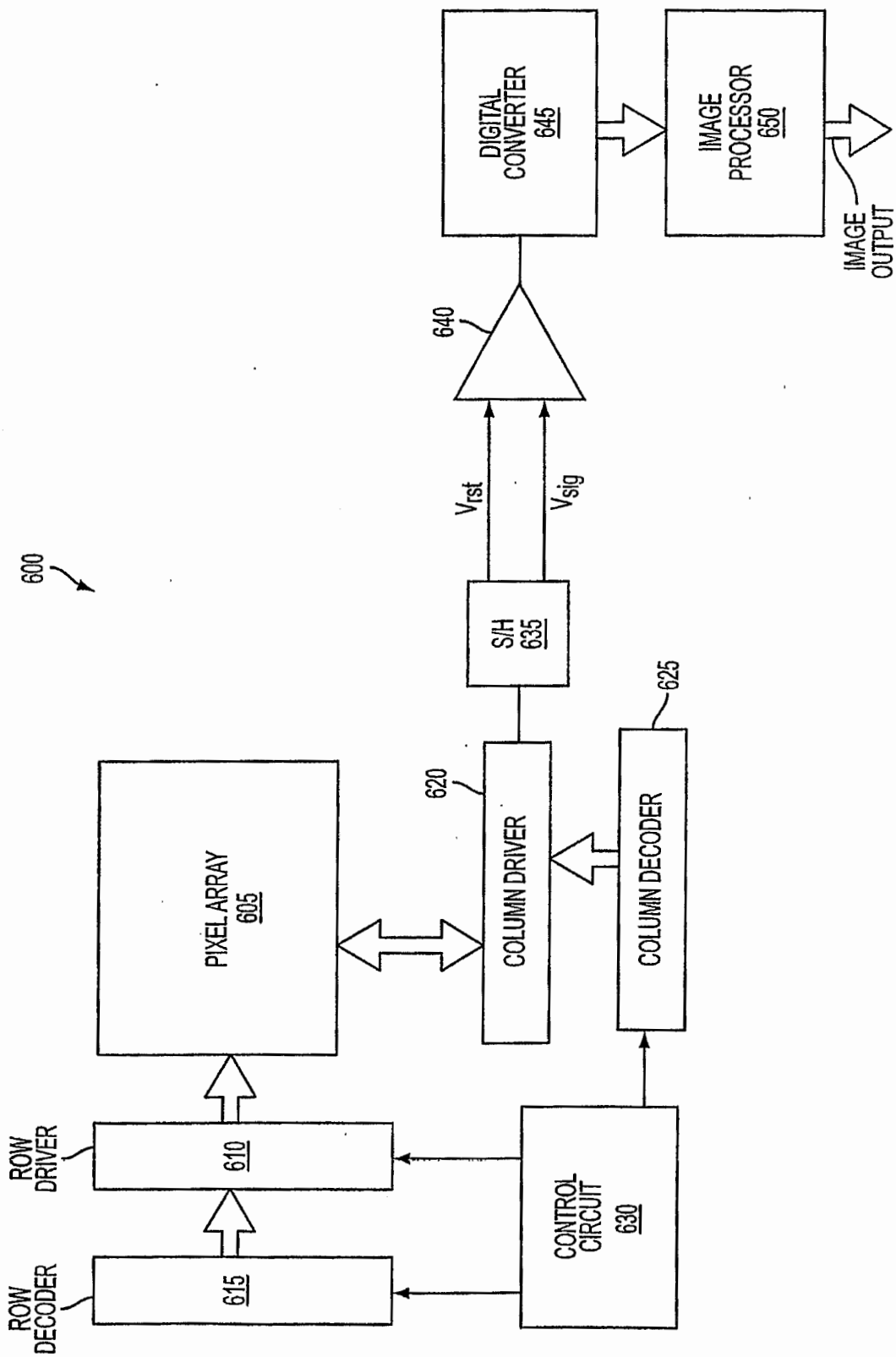

600
ROW DECODER
615
ROW DRIVER
610
PIXEL ARRAY
605
620
COLUMN DRIVER
625
COLUMN DECODER
CONTROL CIRCUIT
630
S/H
635
V_{rst}
V_{sig}
640
DIGITAL CONVERTER
645
IMAGE PROCESSOR
650
IMAGE OUTPUT

FIG. 7

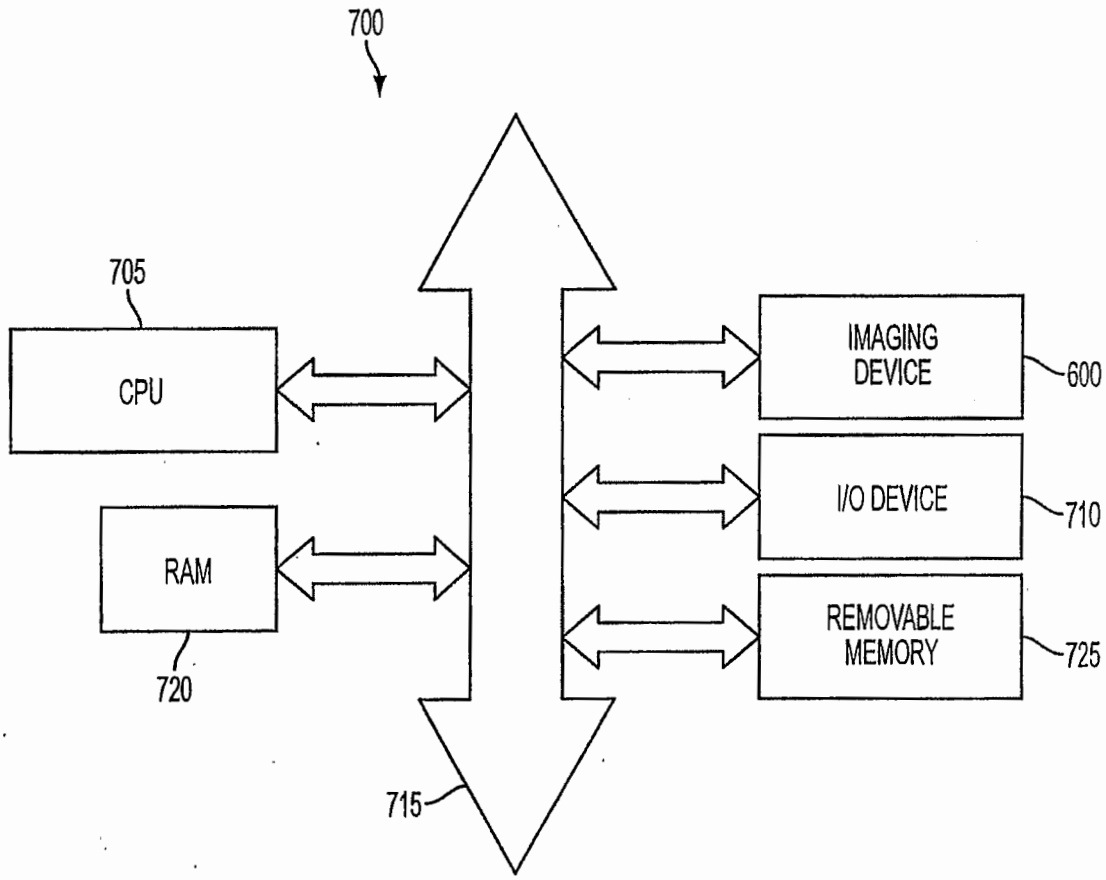

700
705
CPU
RAM
720
715
IMAGING DEVICE
600
I/O DEVICE
710
REMOVABLE MEMORY
725

FIG. 8