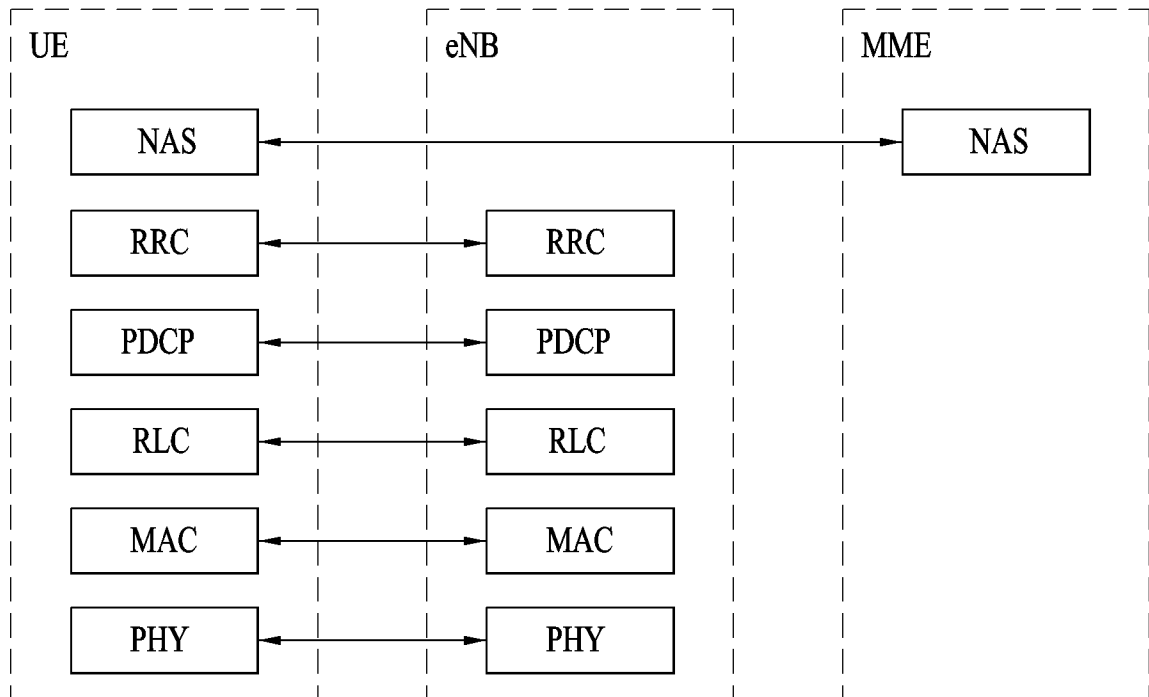
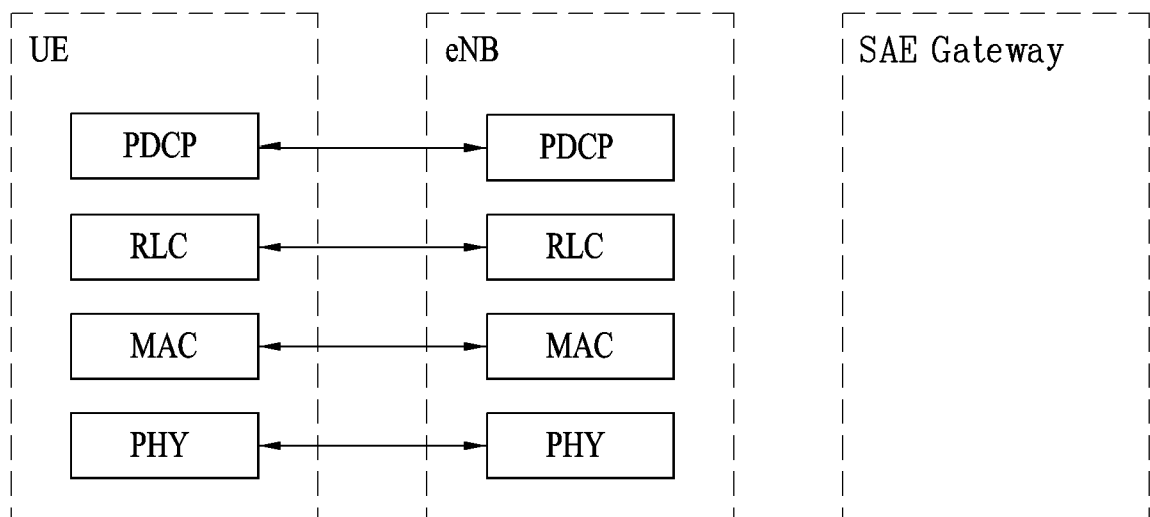


FIG. 1



(A) CONTROL-PLANE PROTOCOL STACK



(B) USER-PLANE PROTOCOL STACK

FIG. 2

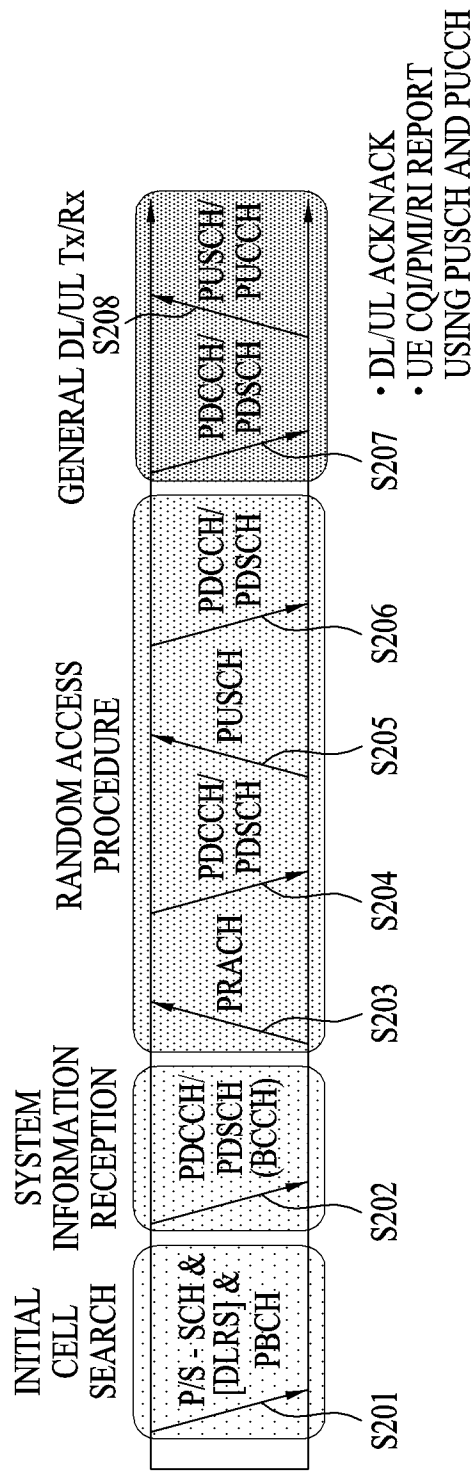


FIG. 3

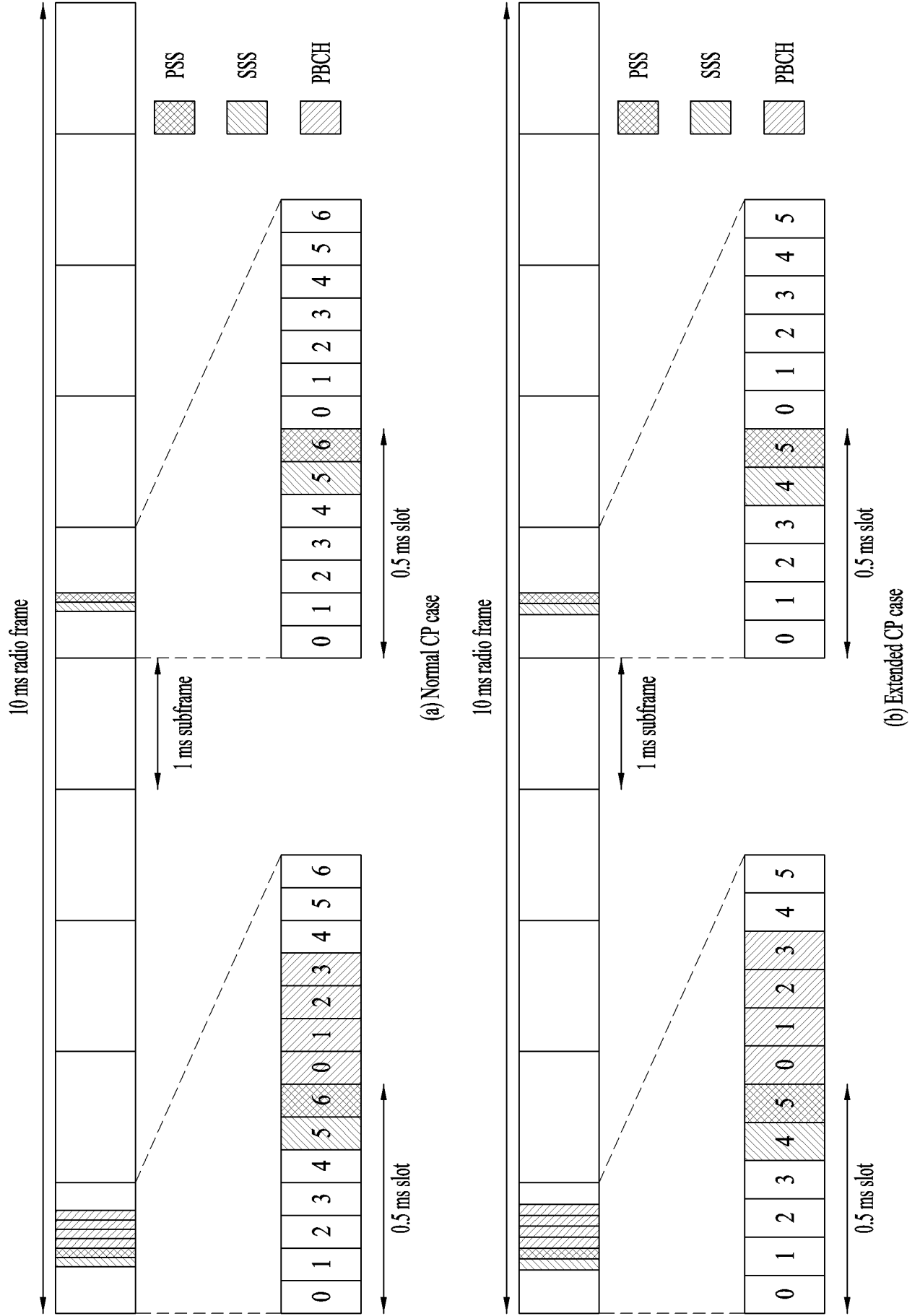


FIG. 4

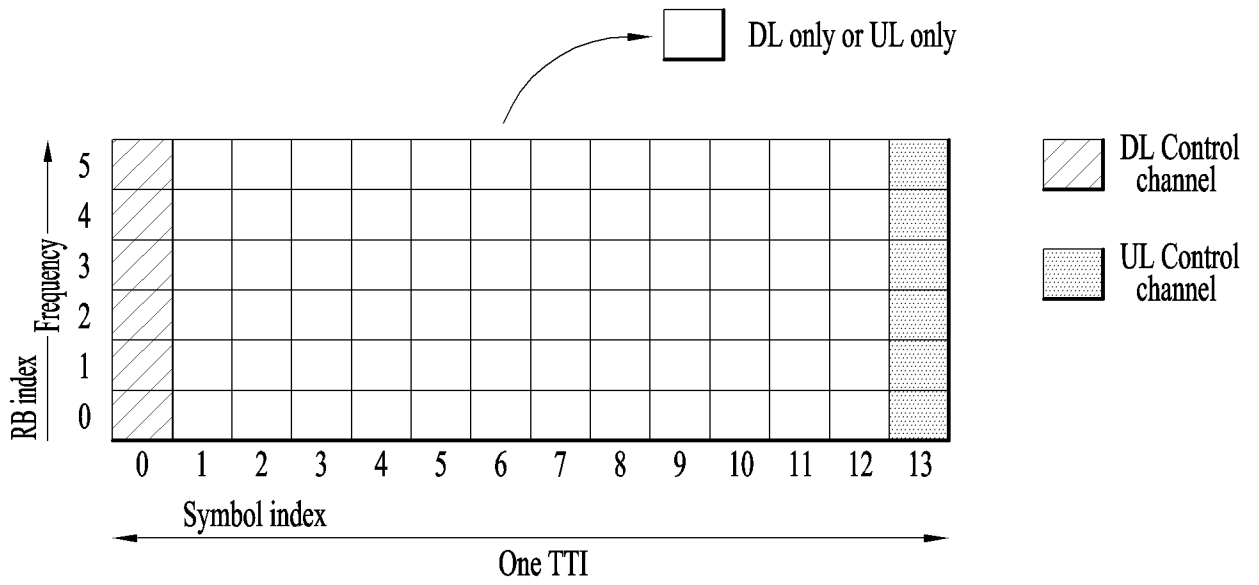


FIG. 5

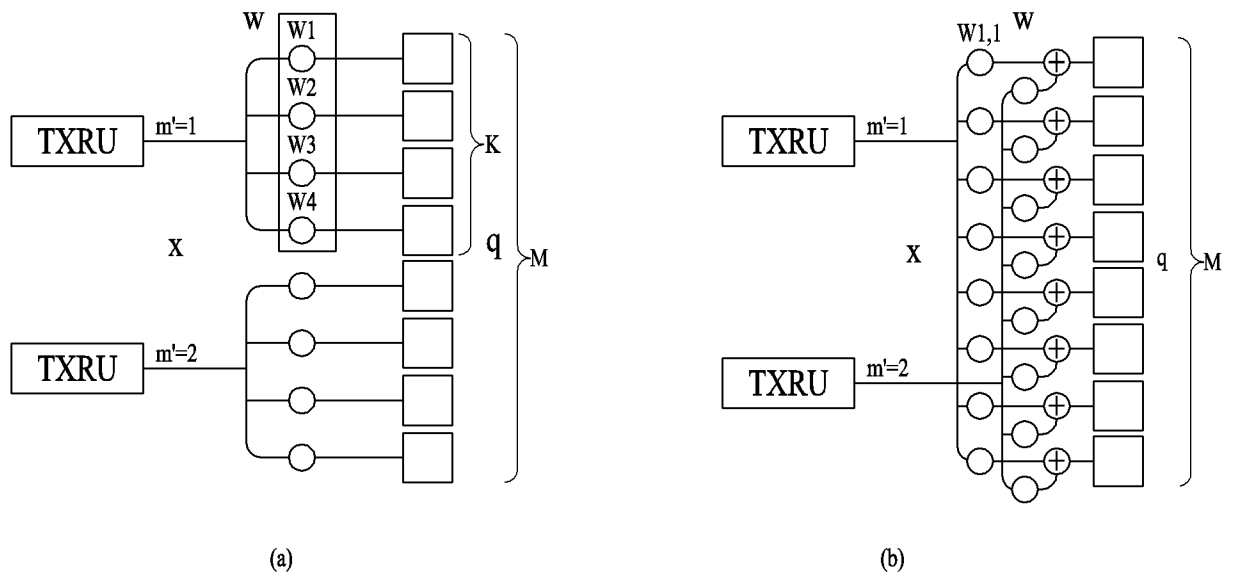


FIG. 6

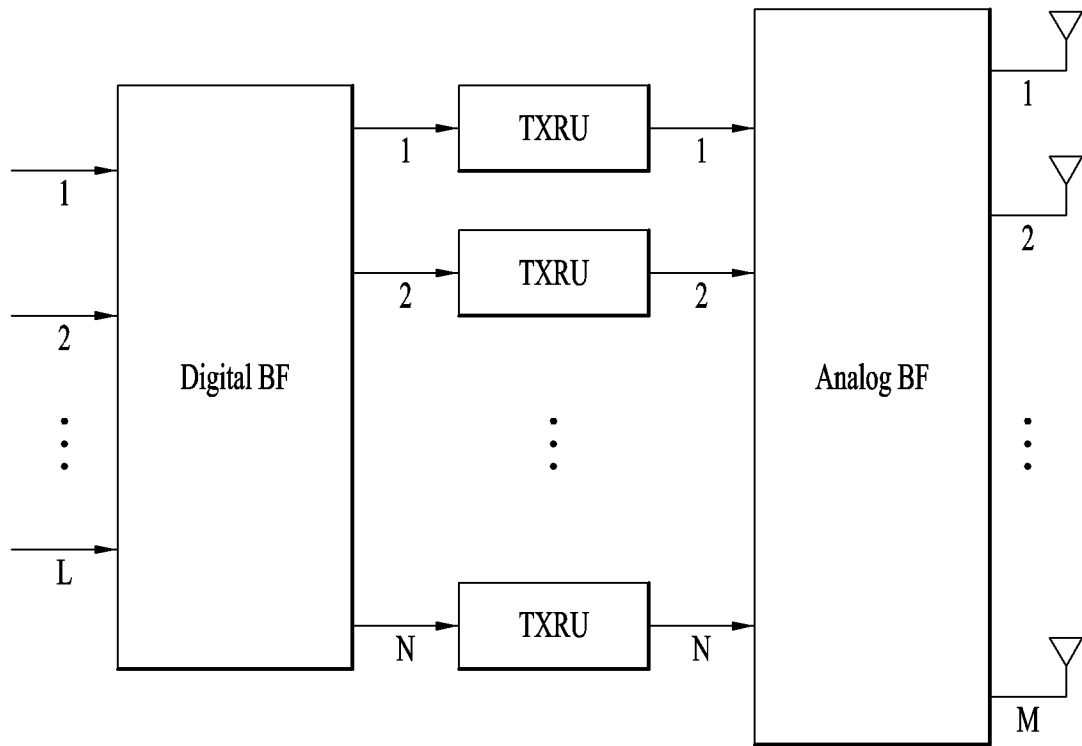


FIG. 7

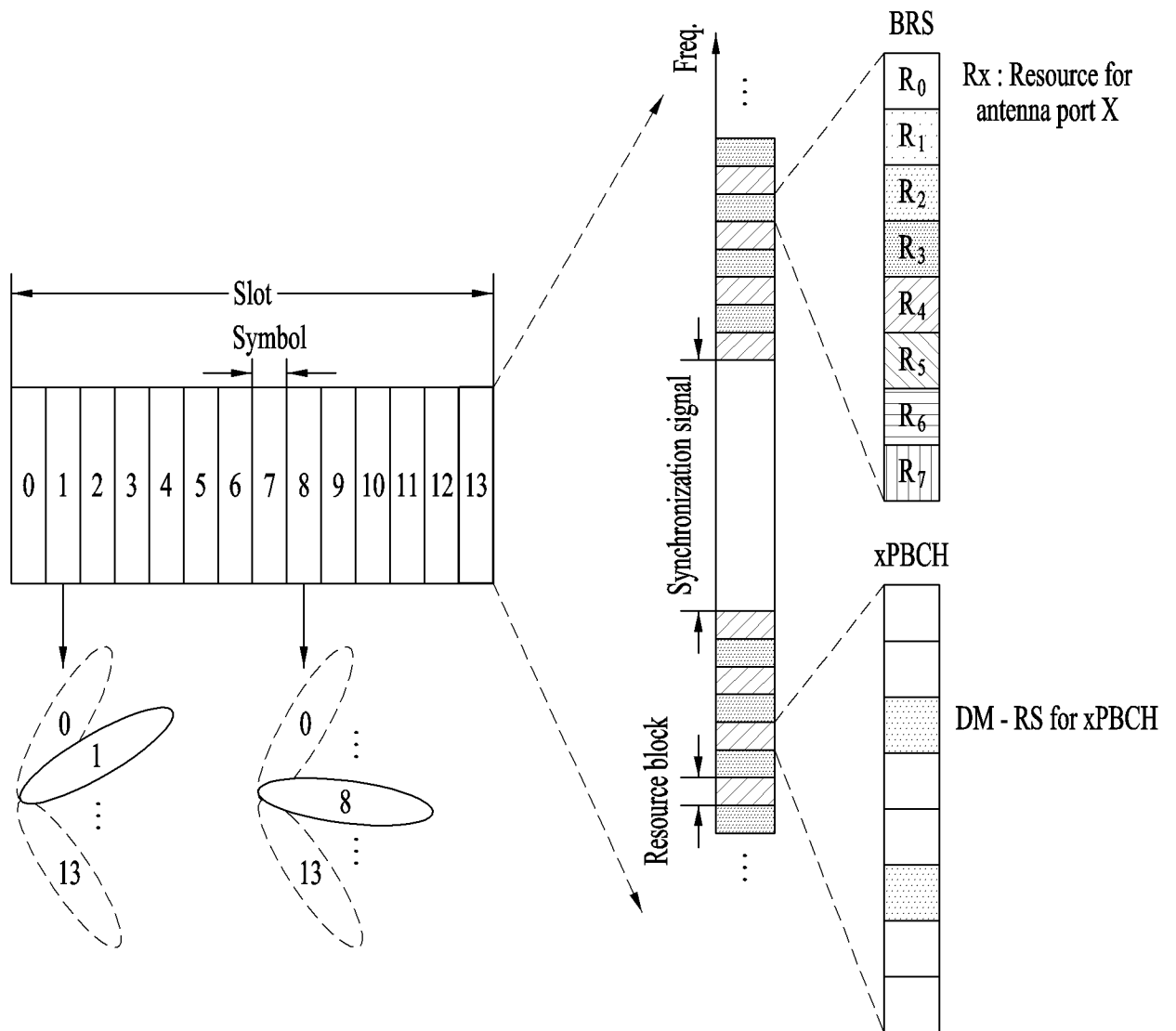


FIG. 8

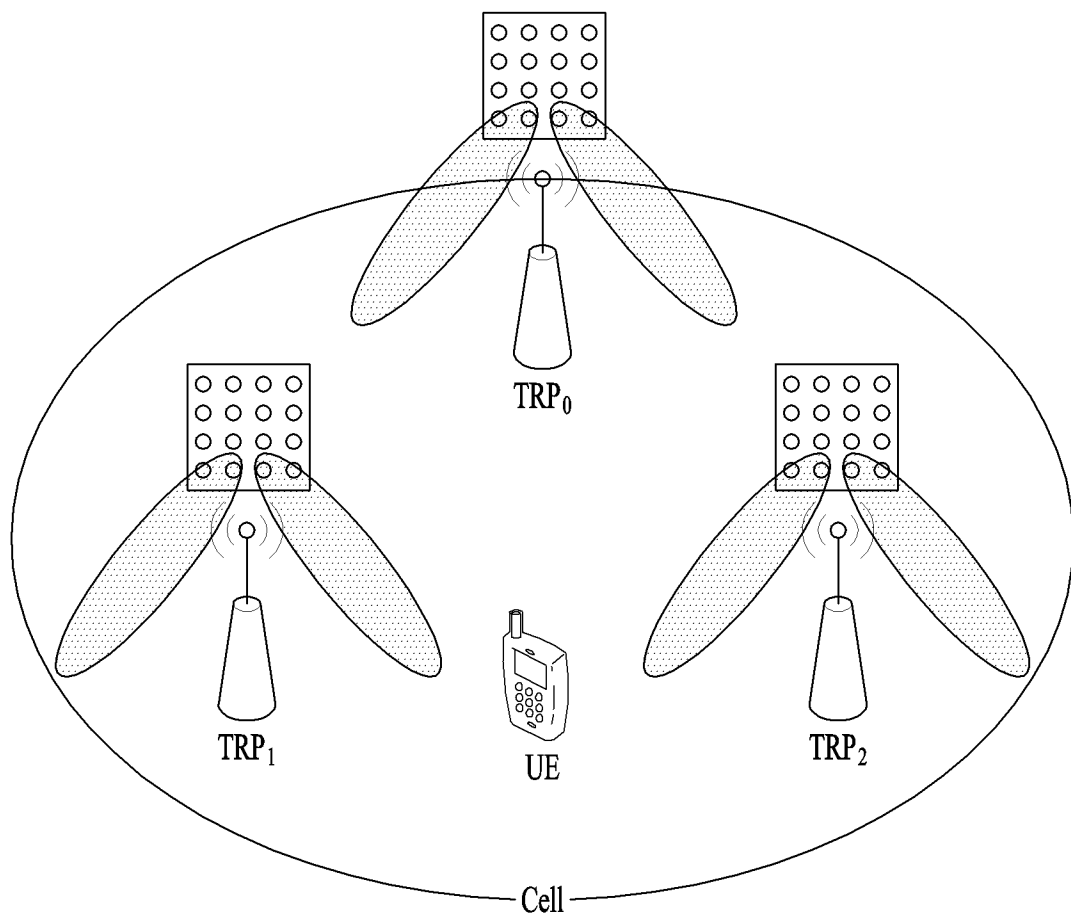
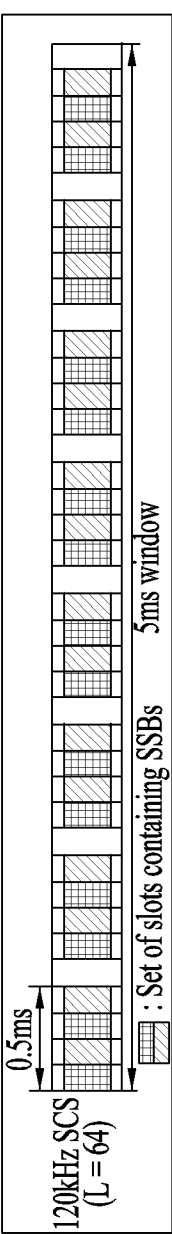
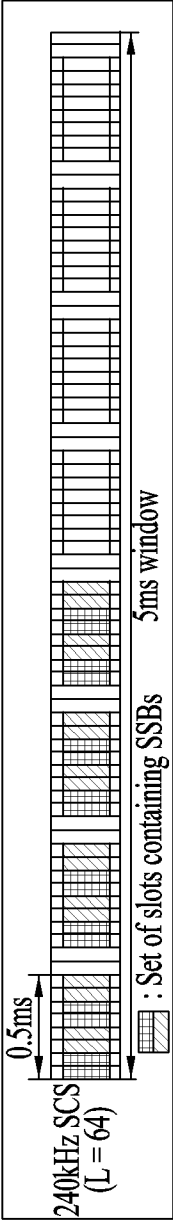


FIG. 9



(a)



(b)



FIG. 11

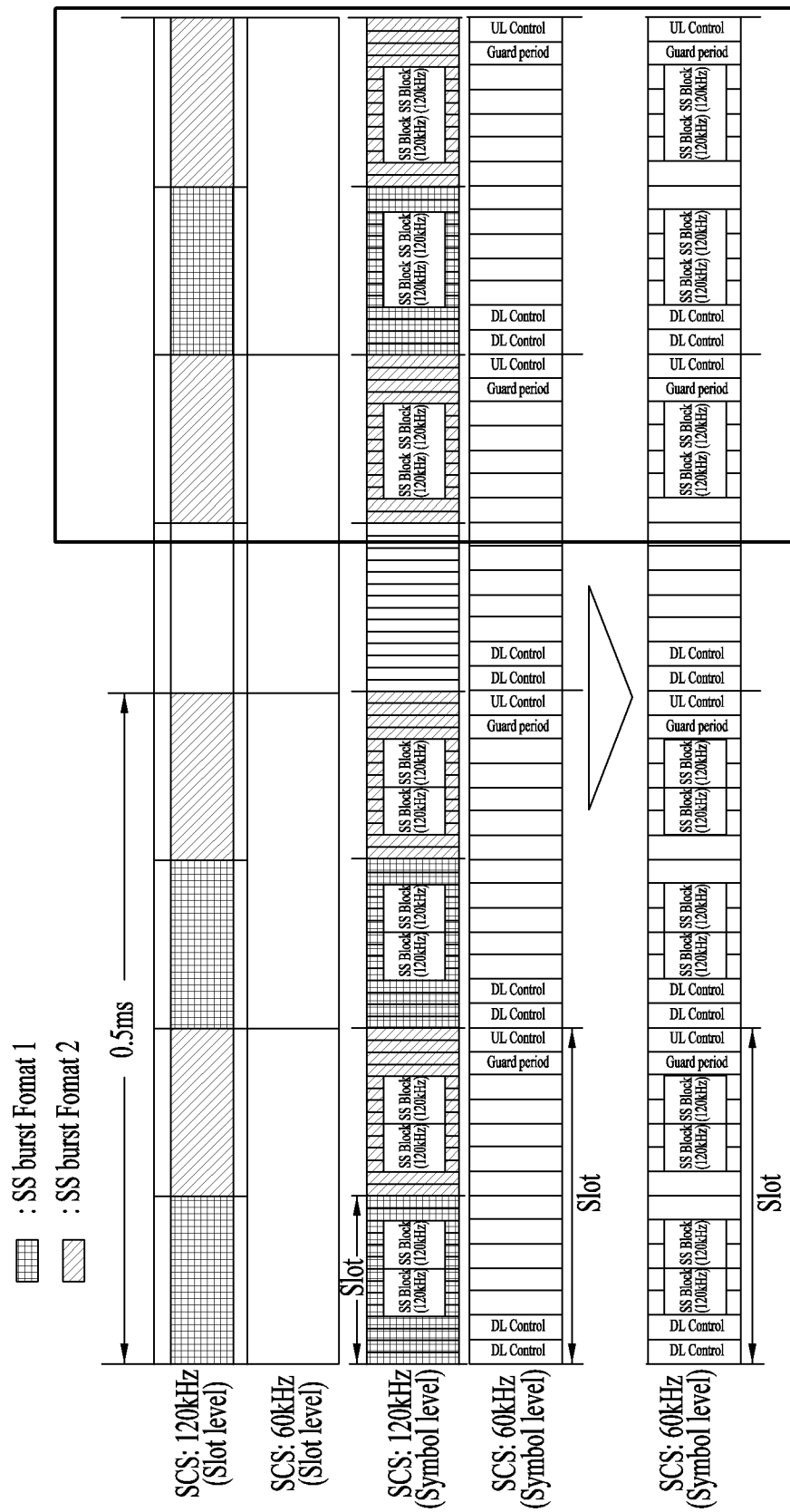


FIG. 12

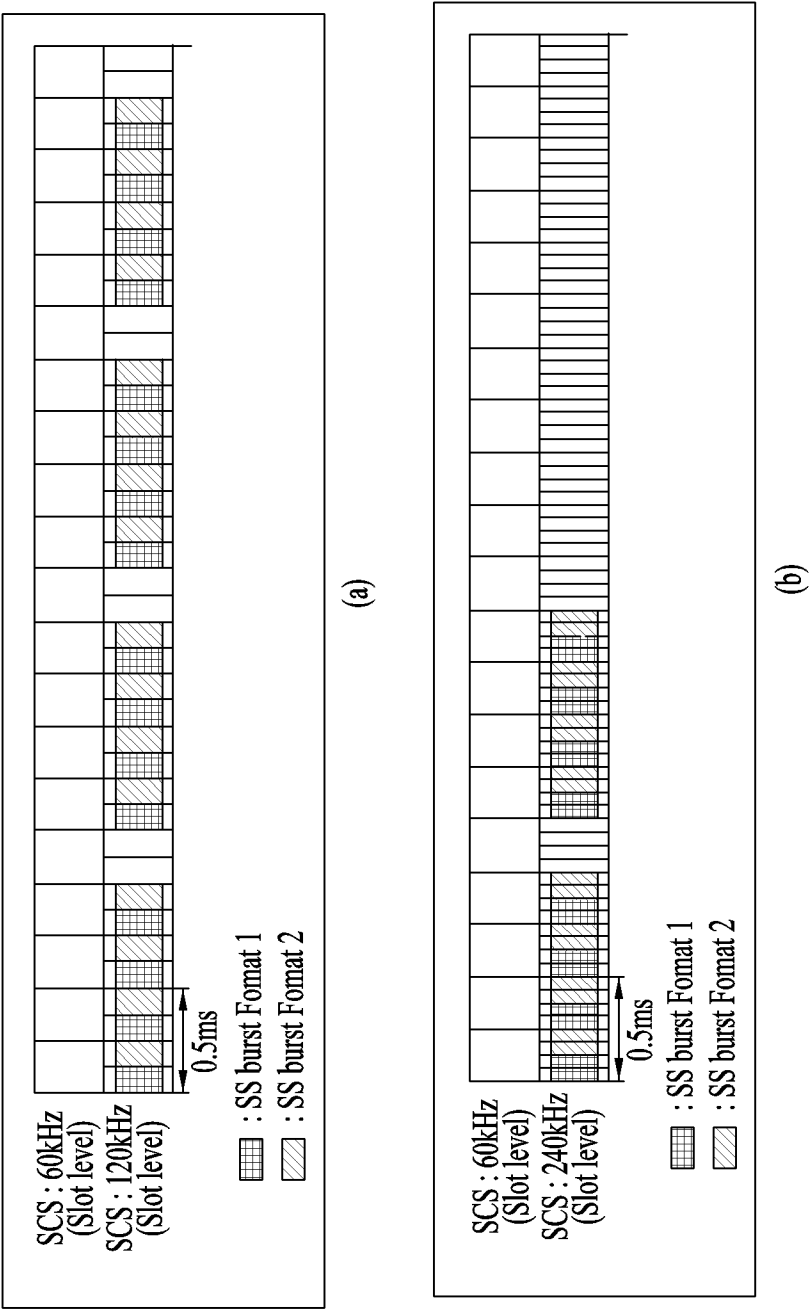


FIG. 13

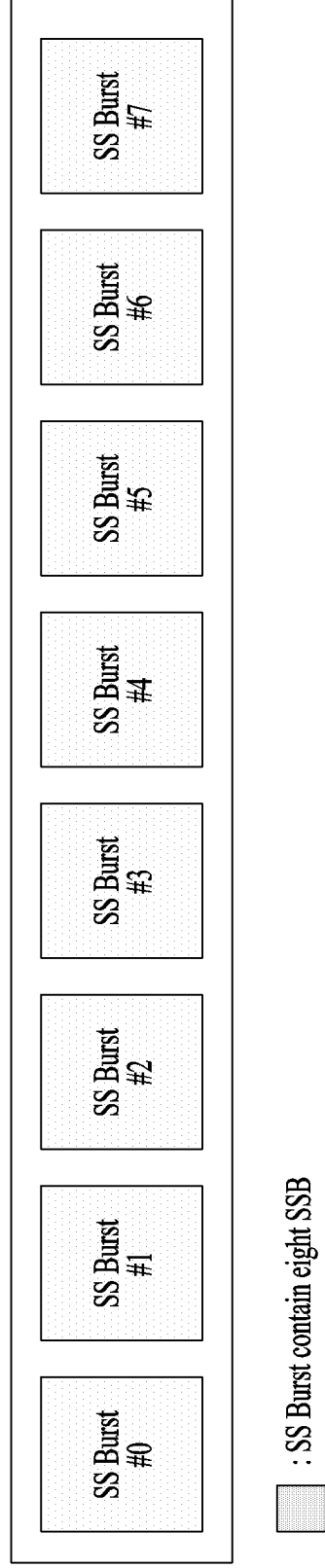


FIG. 14

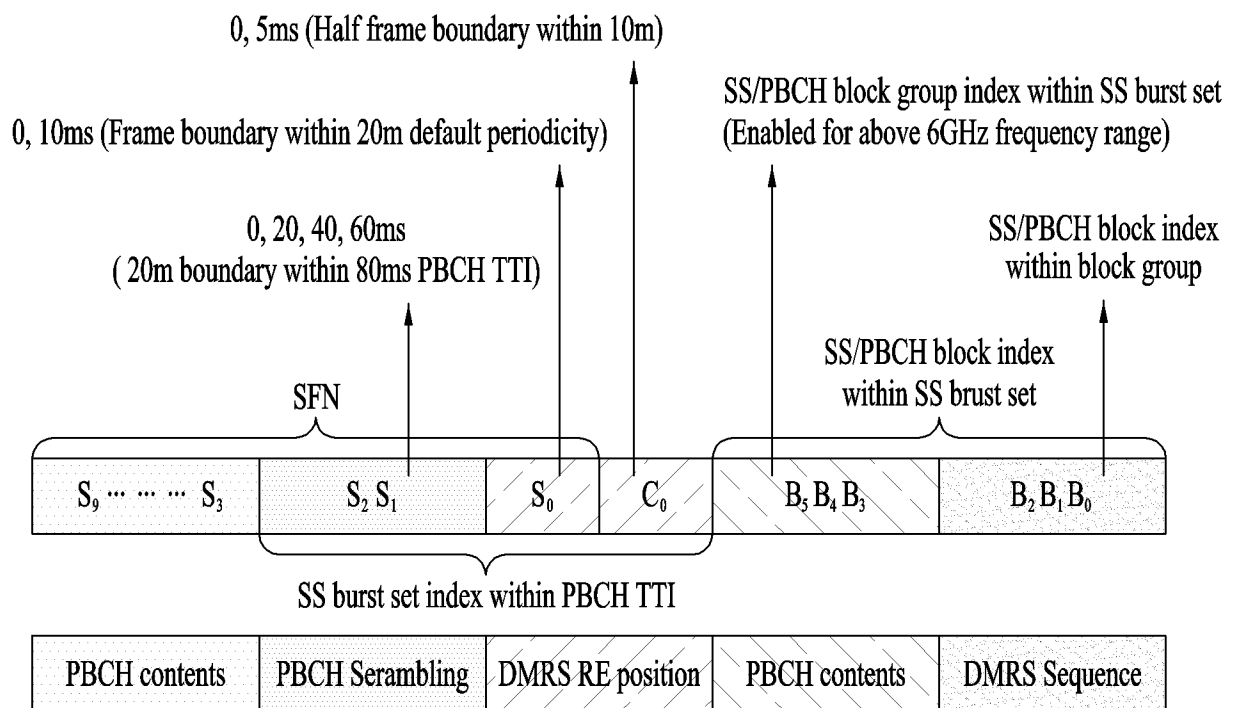


FIG. 15

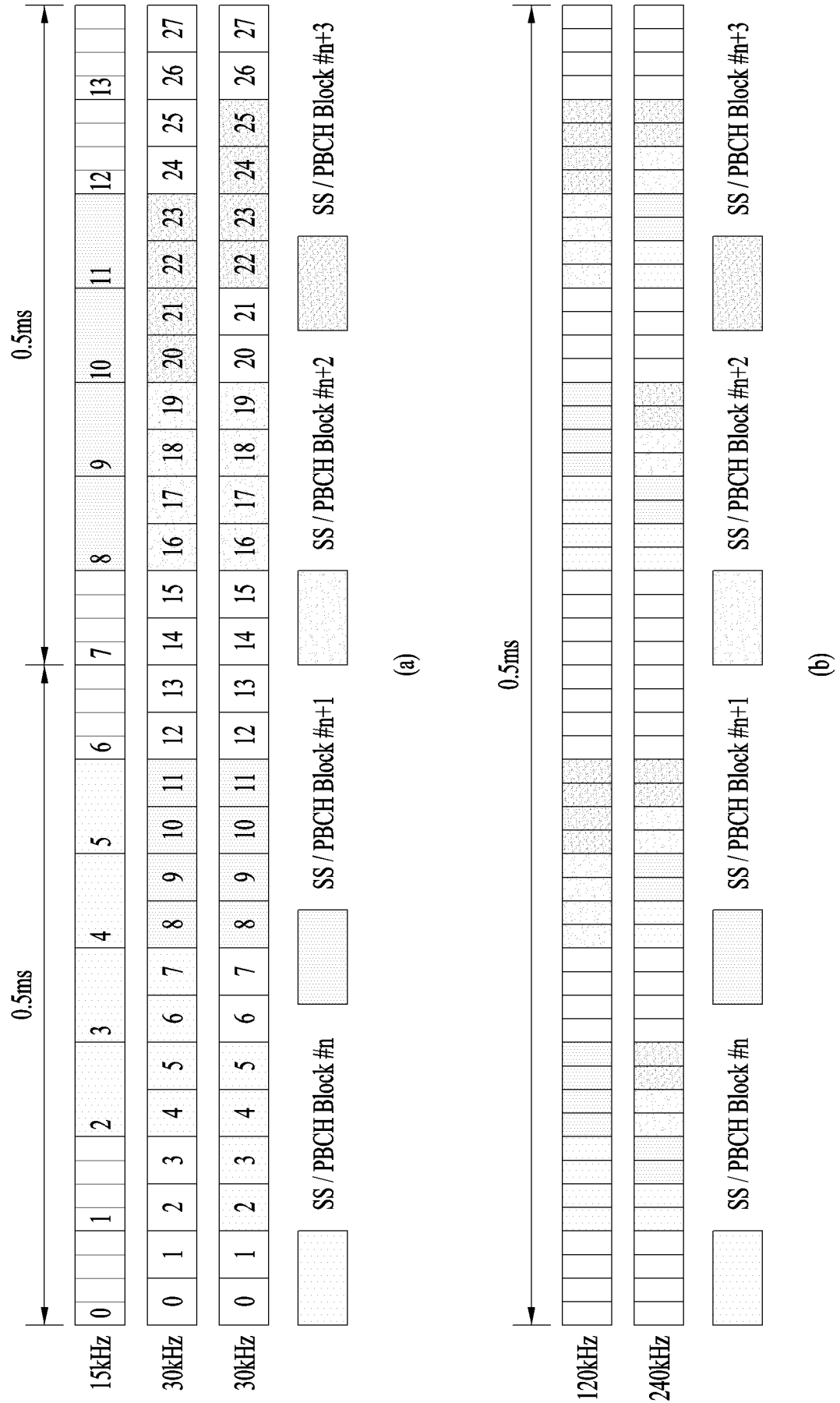


FIG. 16

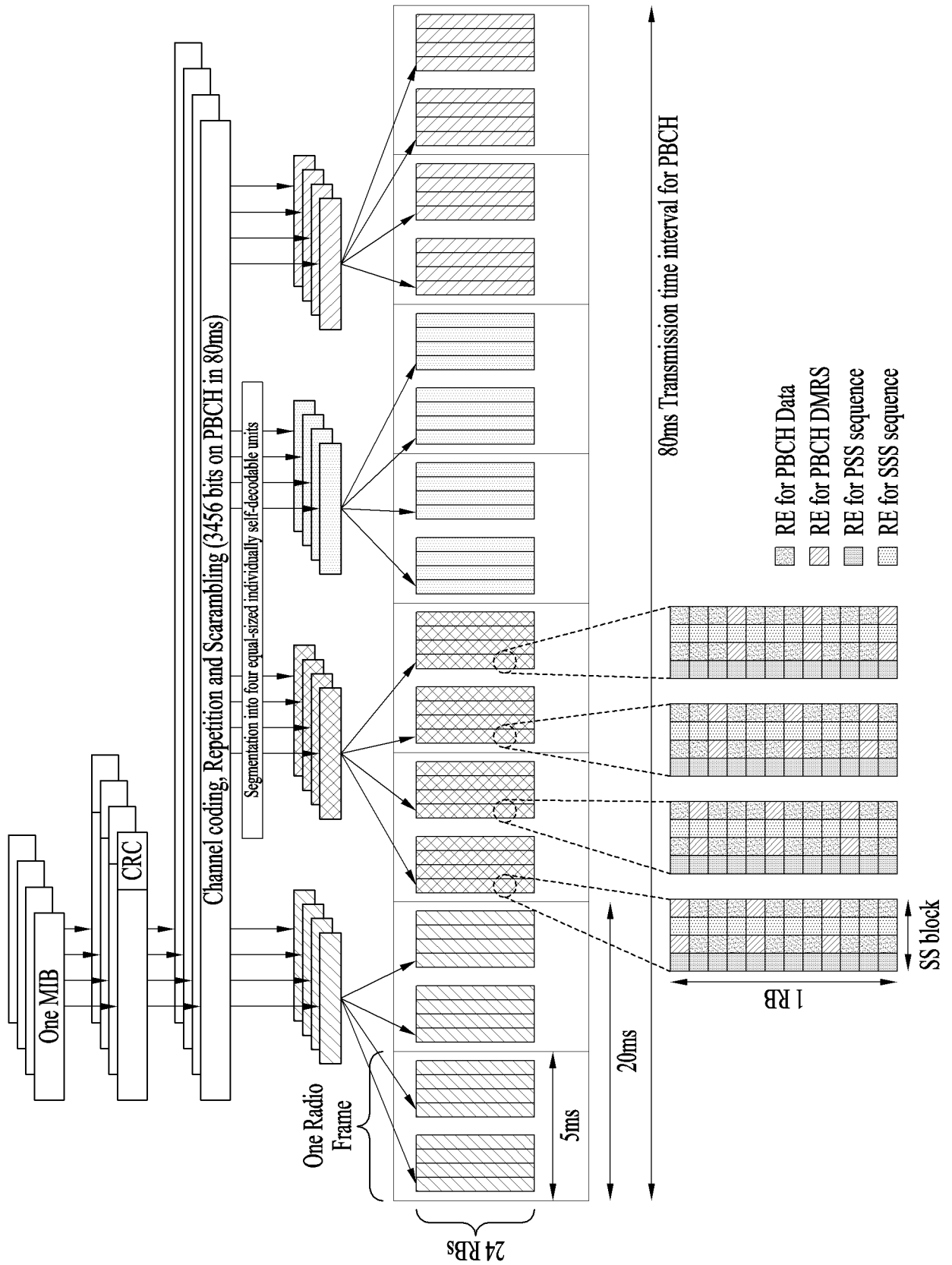


FIG. 17

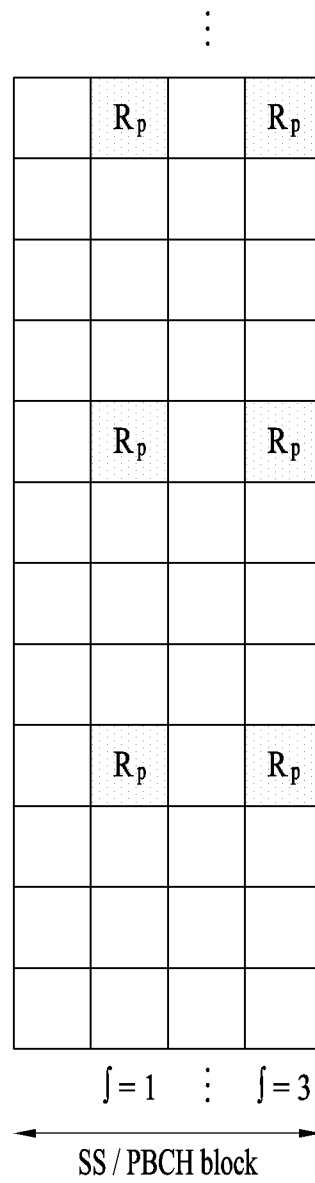


FIG. 18

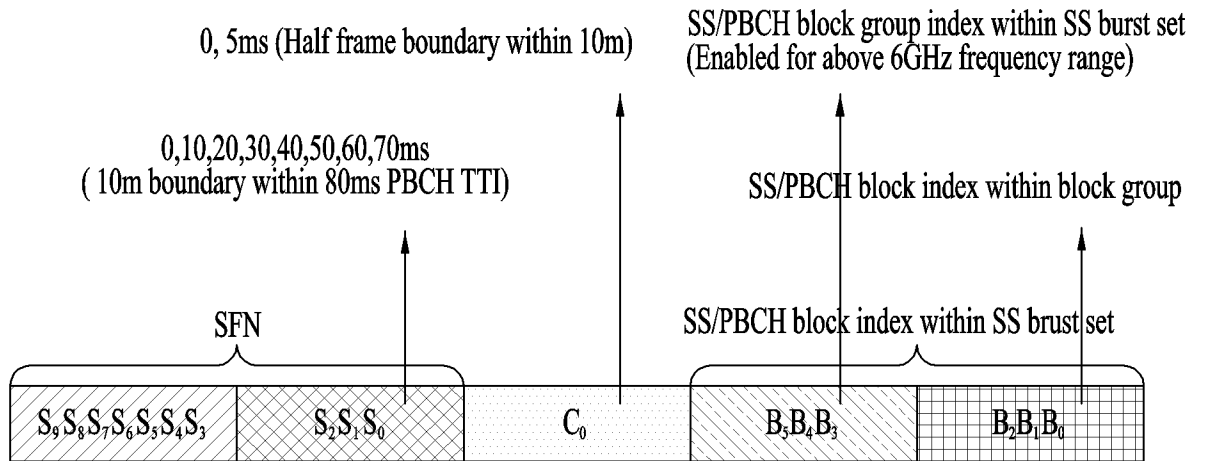


FIG. 19

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

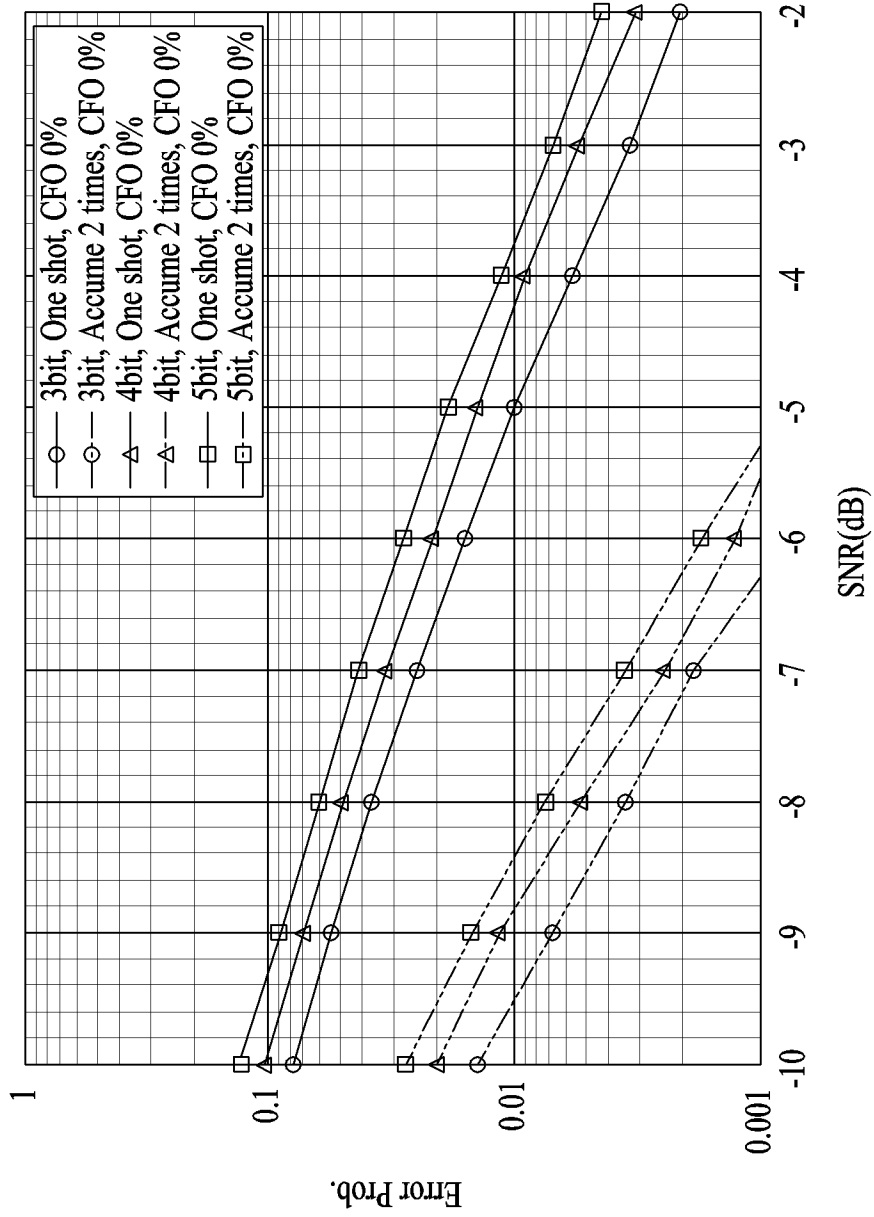


FIG. 20

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

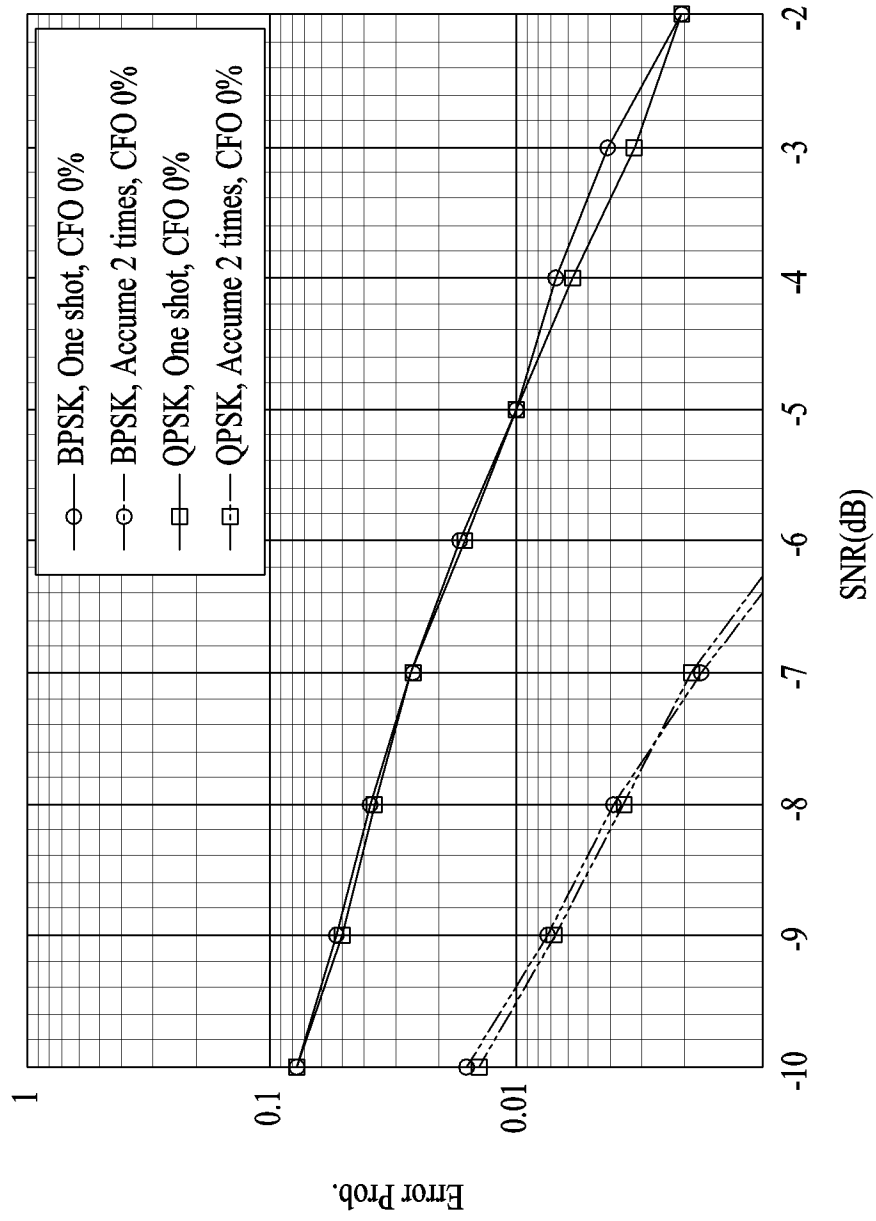


FIG. 21

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

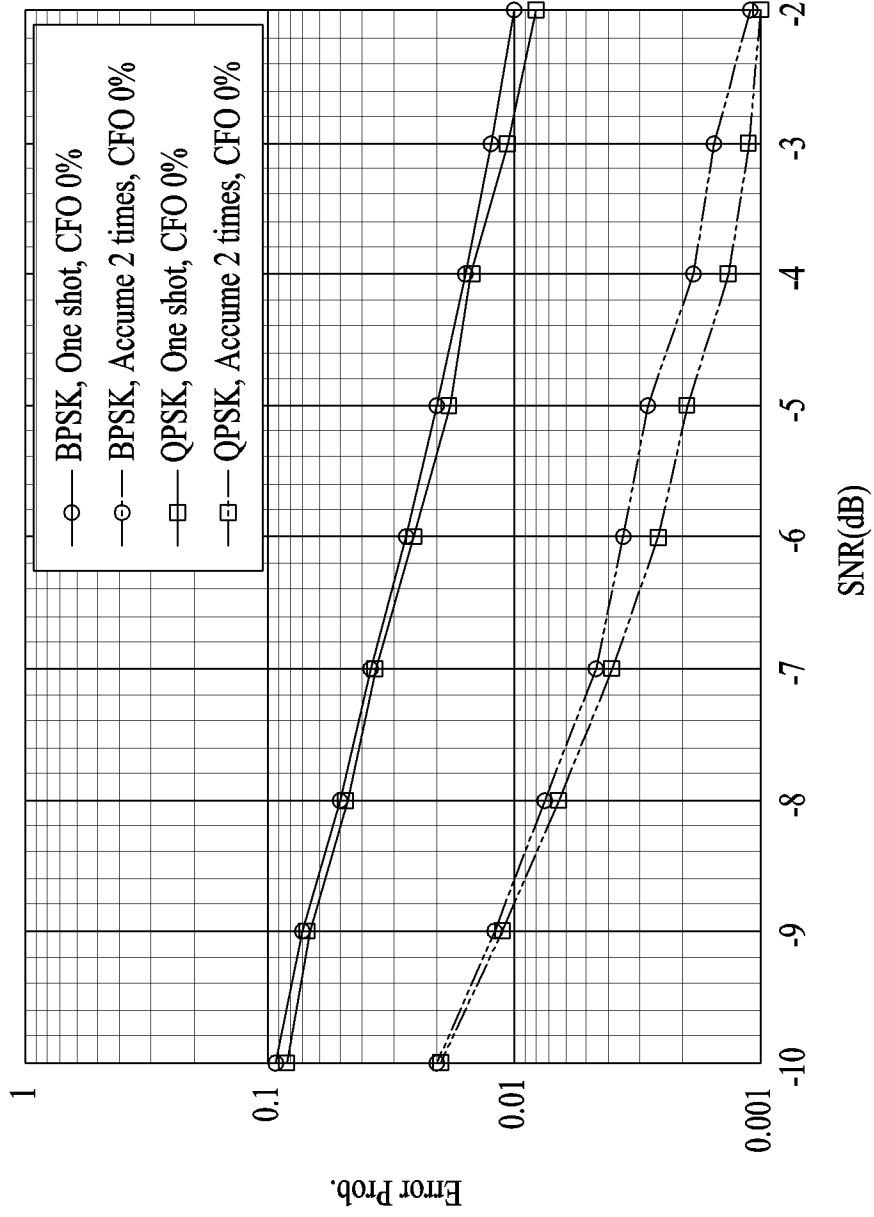


FIG. 22

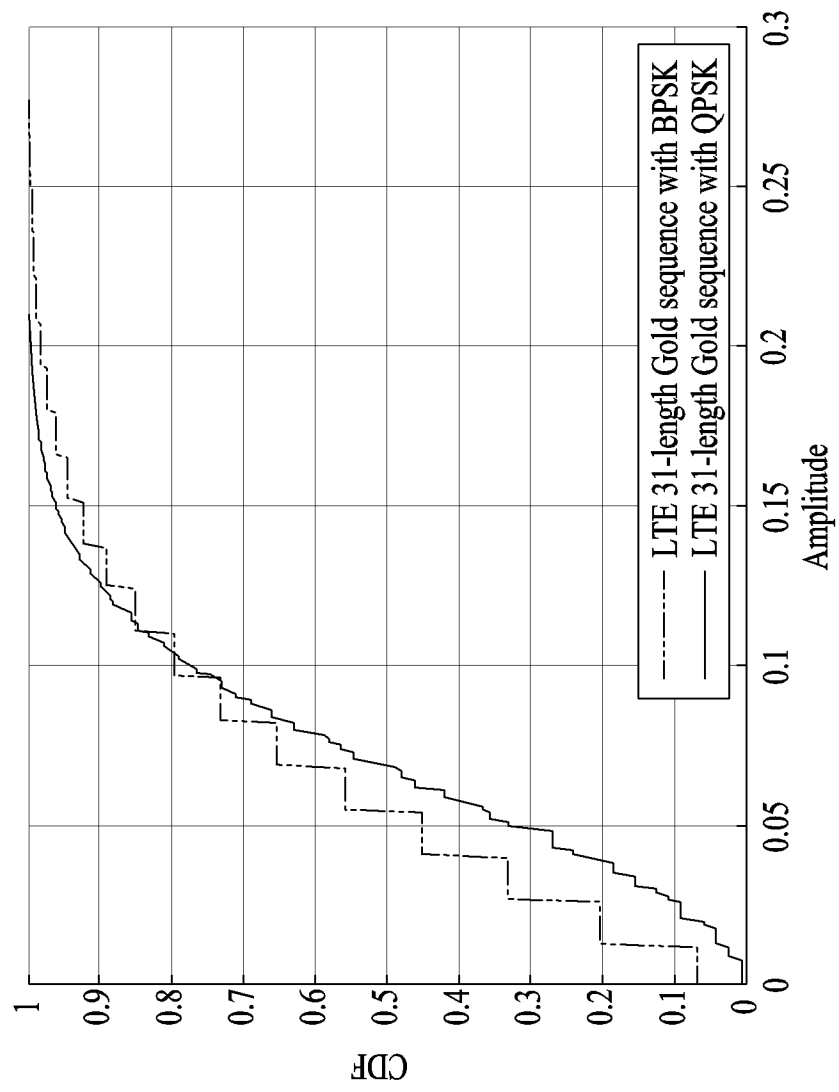


FIG. 23

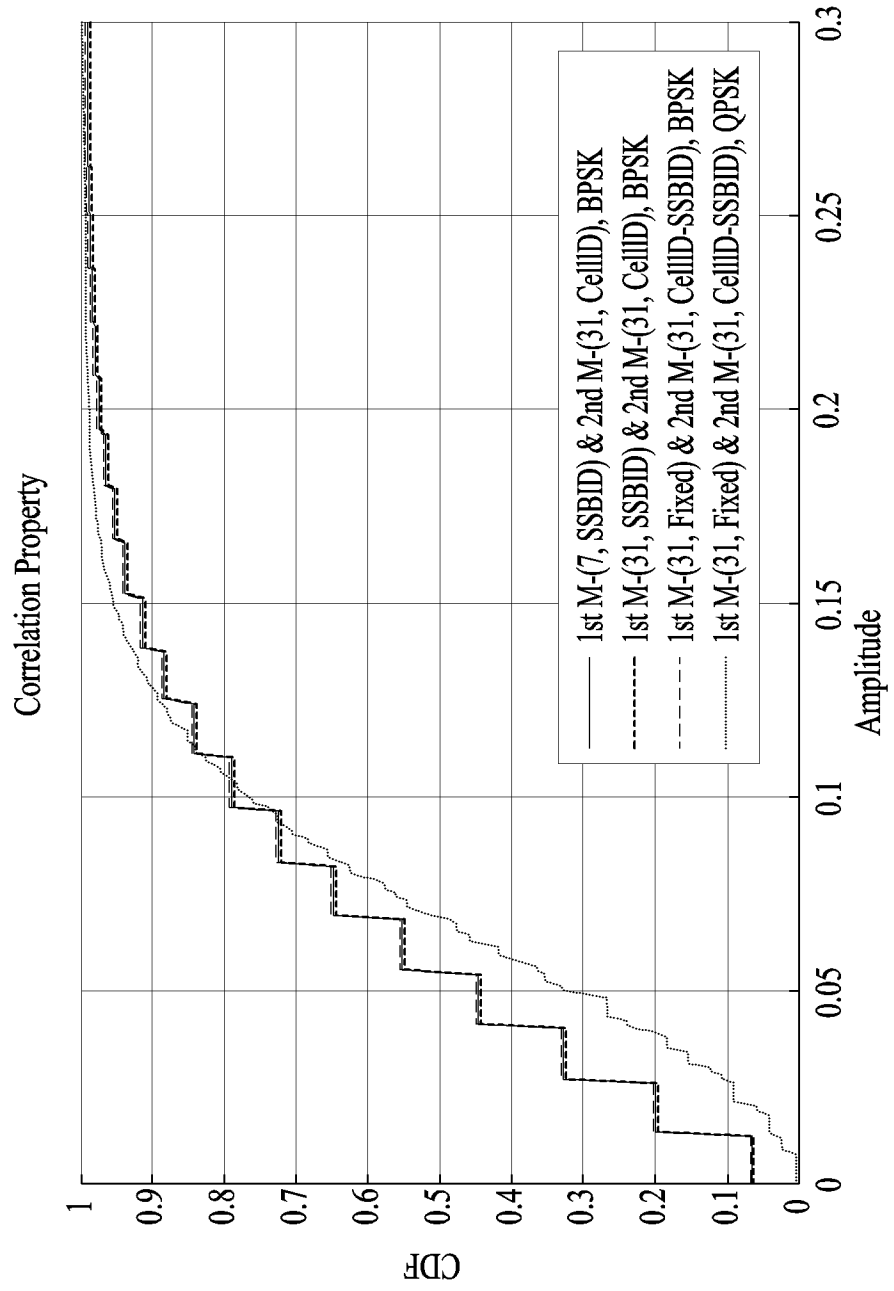


FIG. 24

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

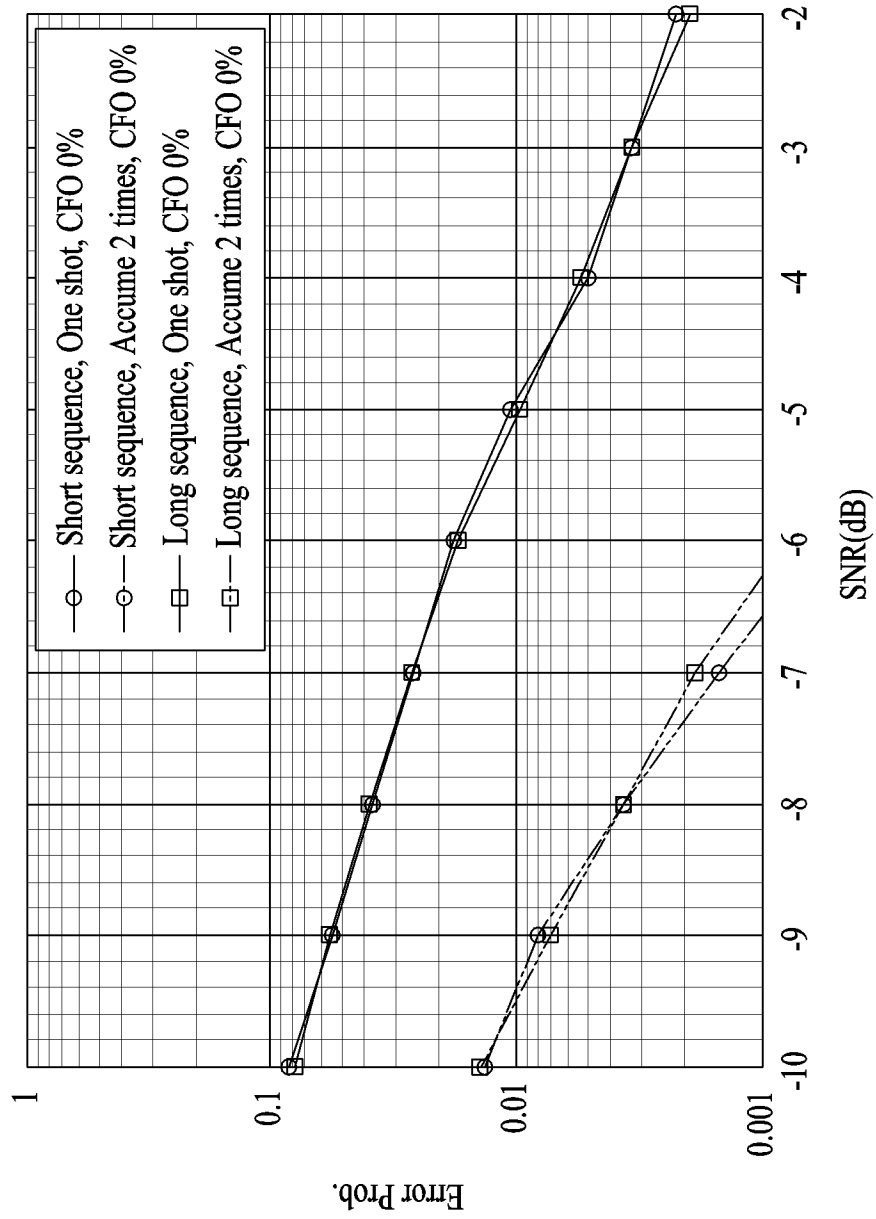


FIG. 25

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

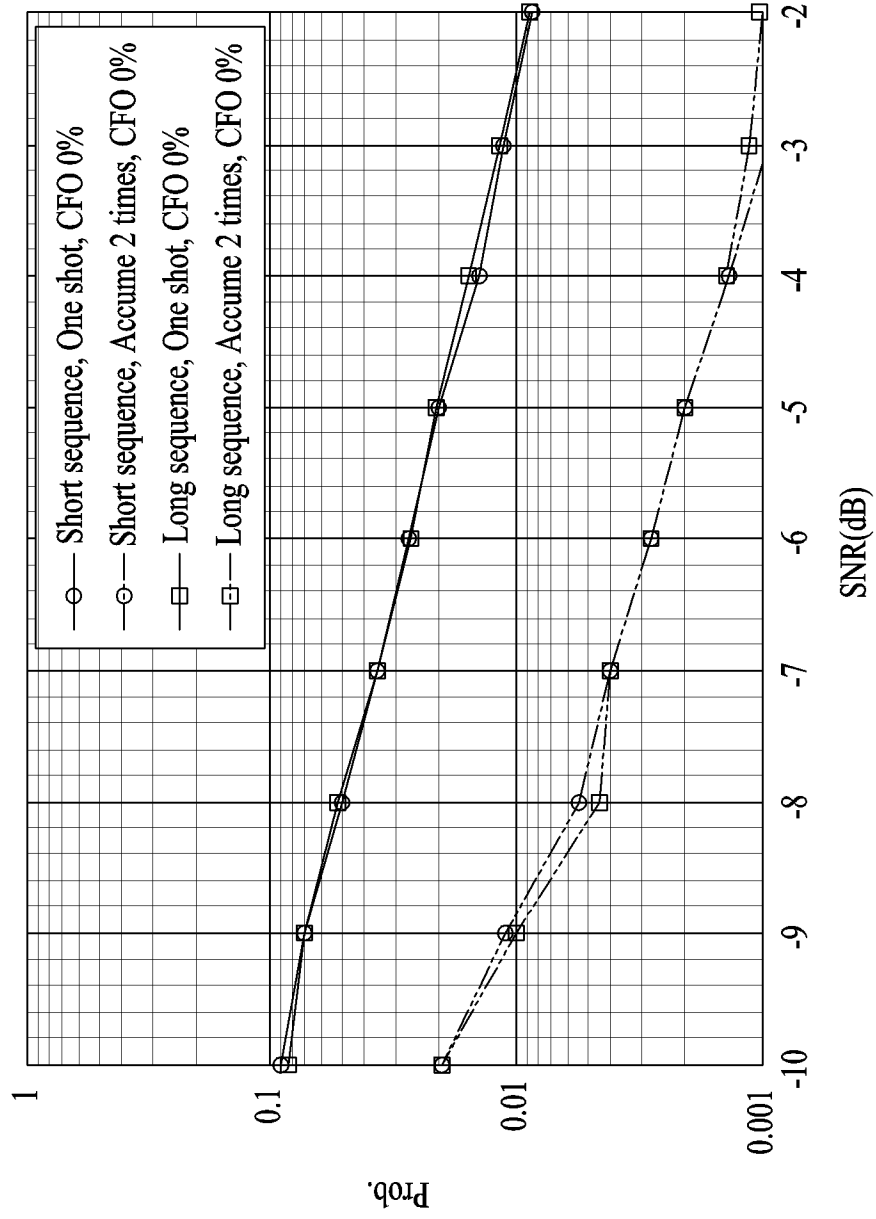


FIG. 26

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

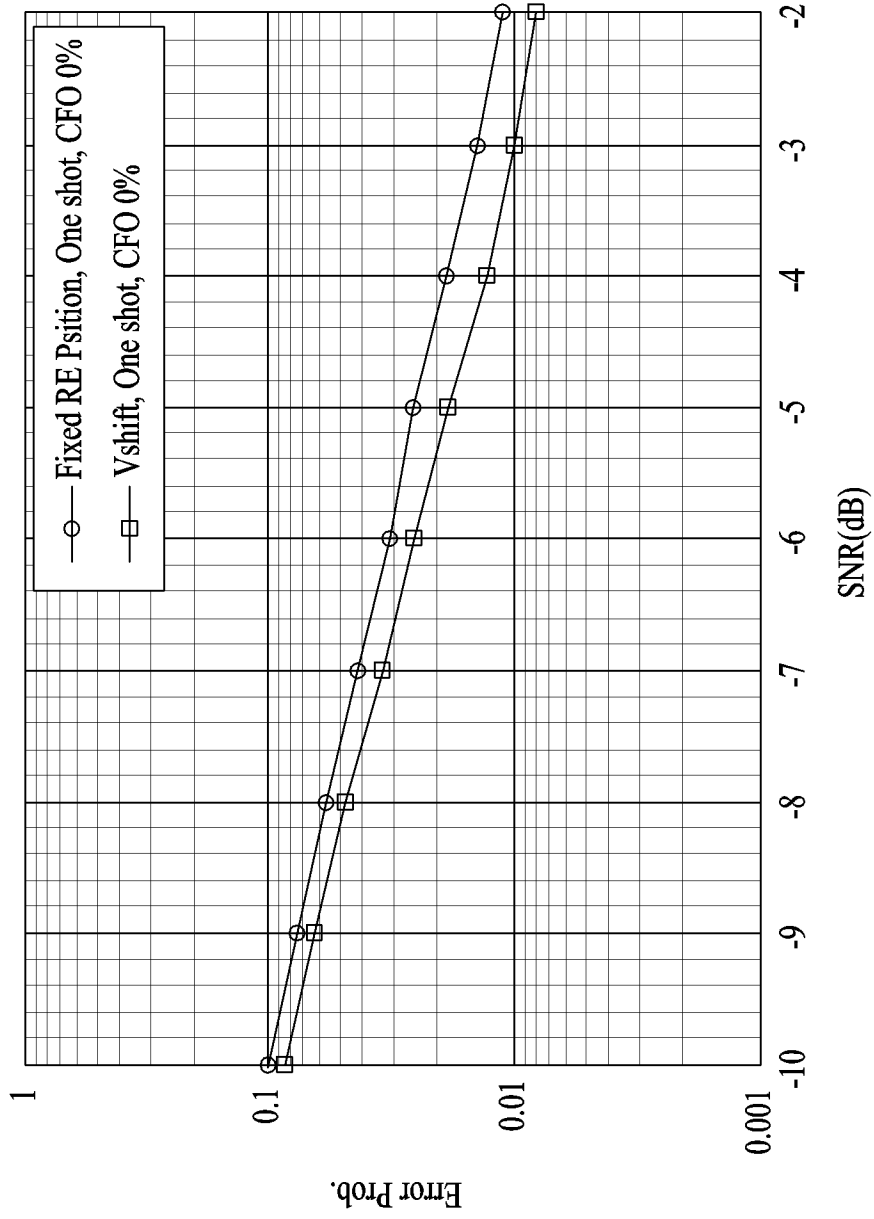


FIG. 27

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

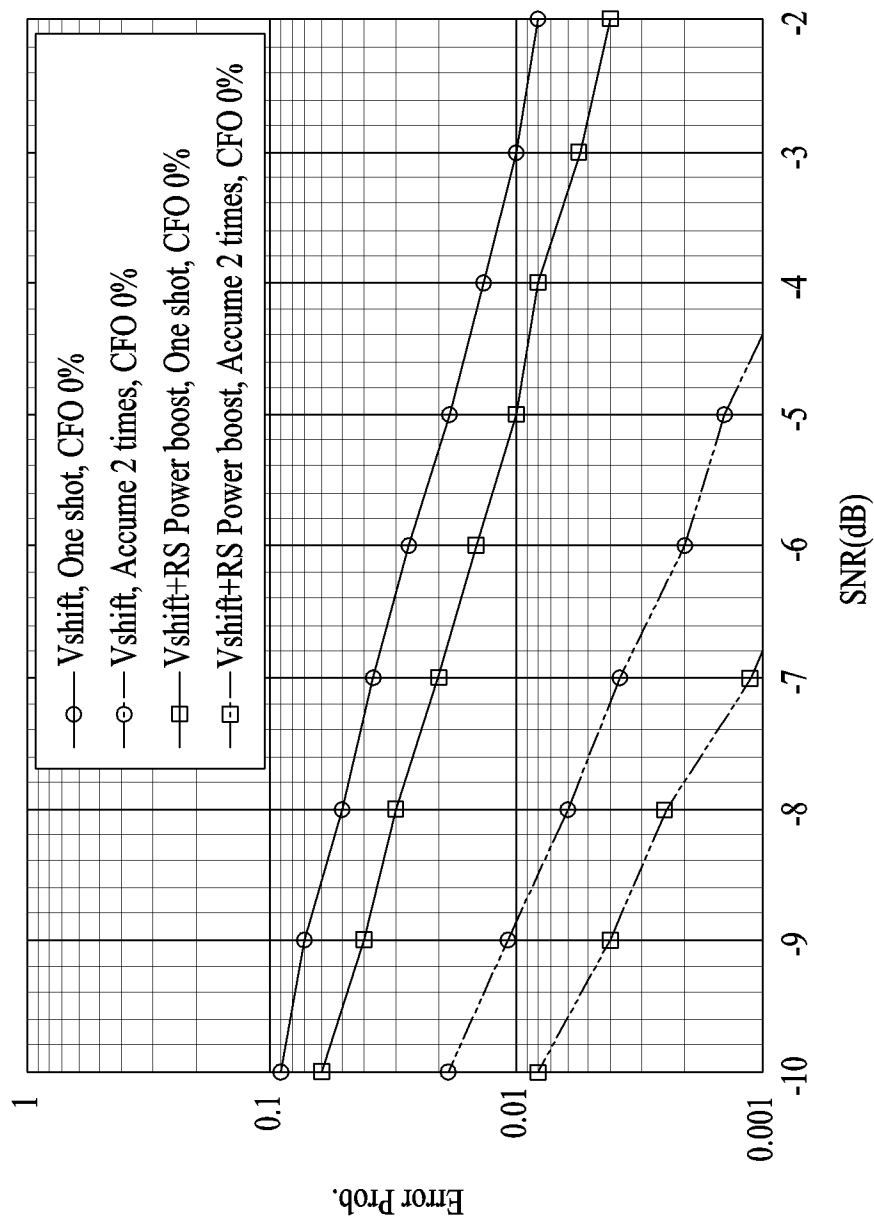


FIG. 28

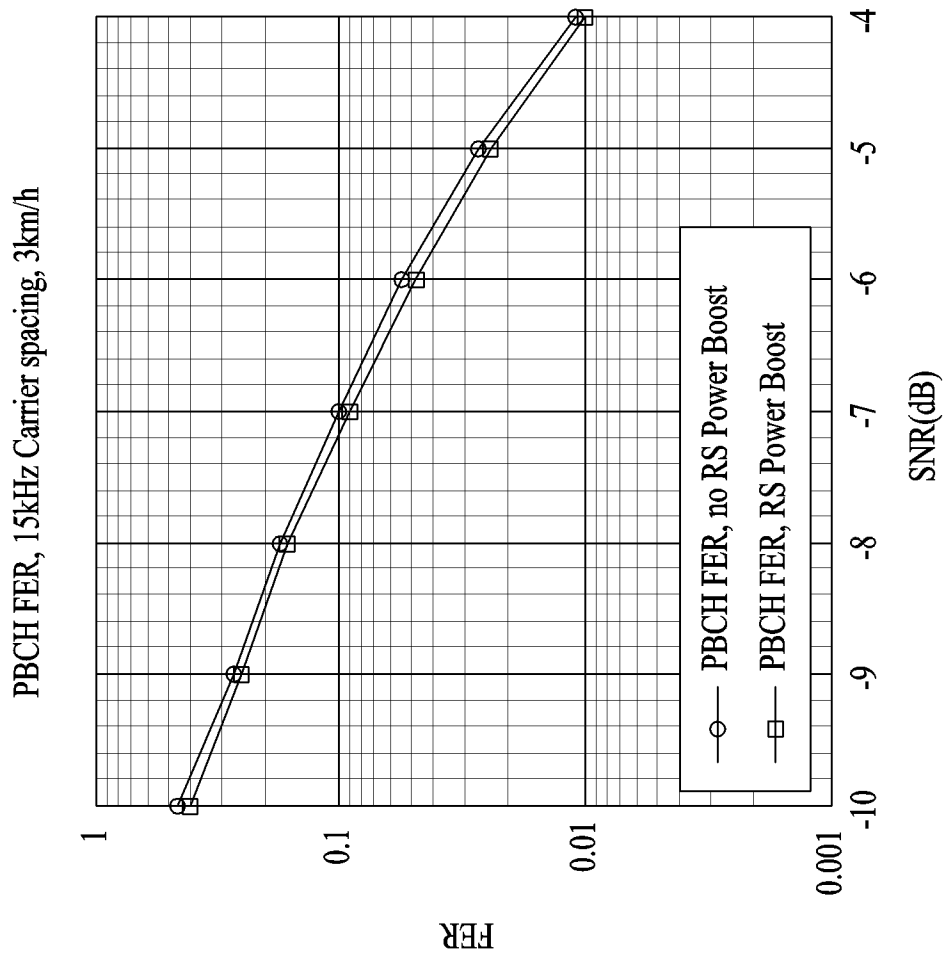


FIG. 29

PBCH FER, 15kHz Carrier spacing, 3km/h

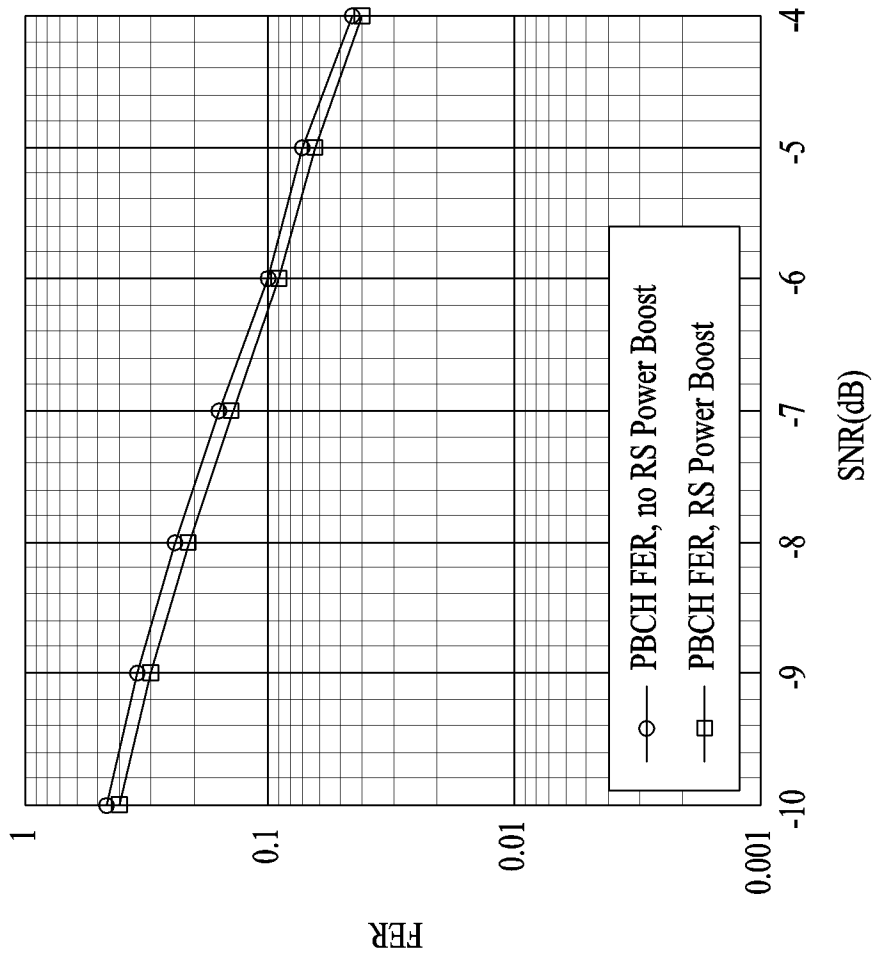


FIG. 30

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

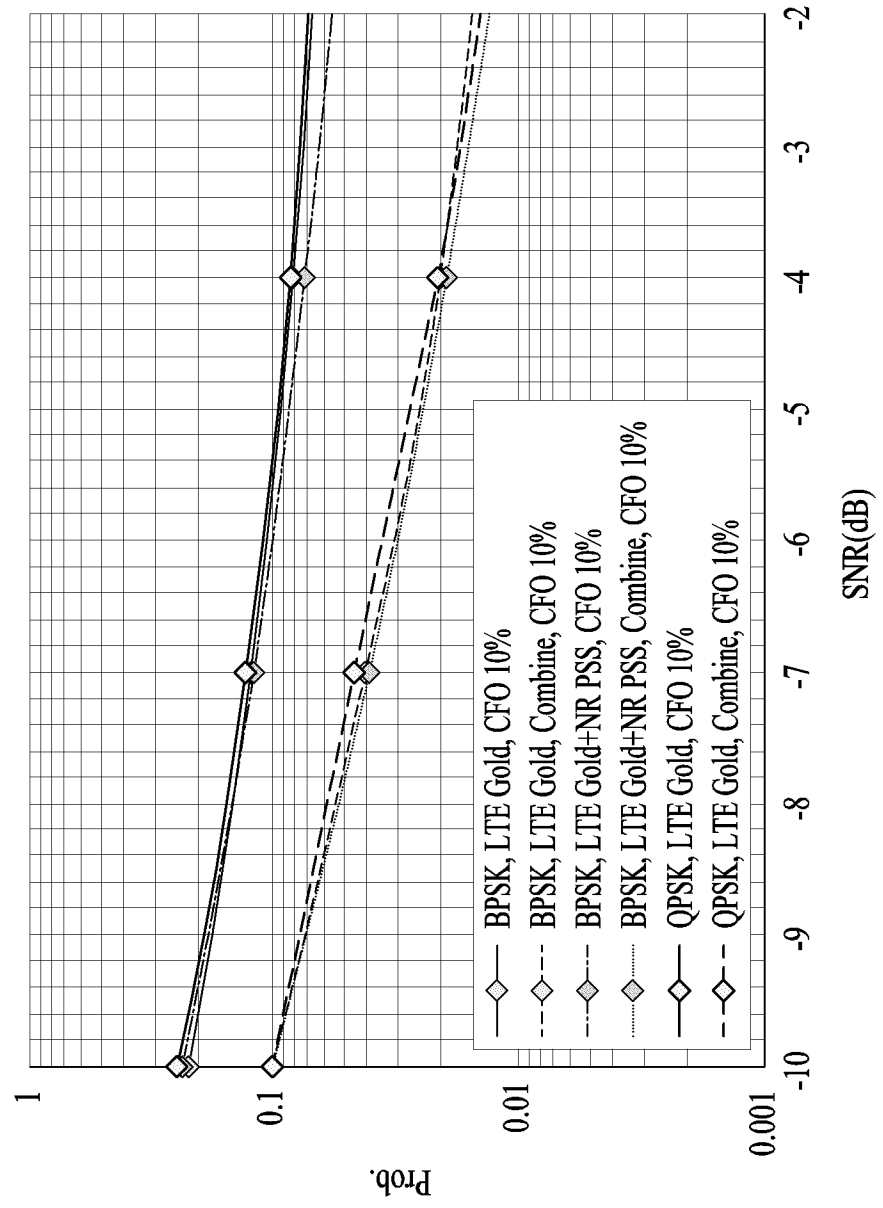


FIG. 31

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

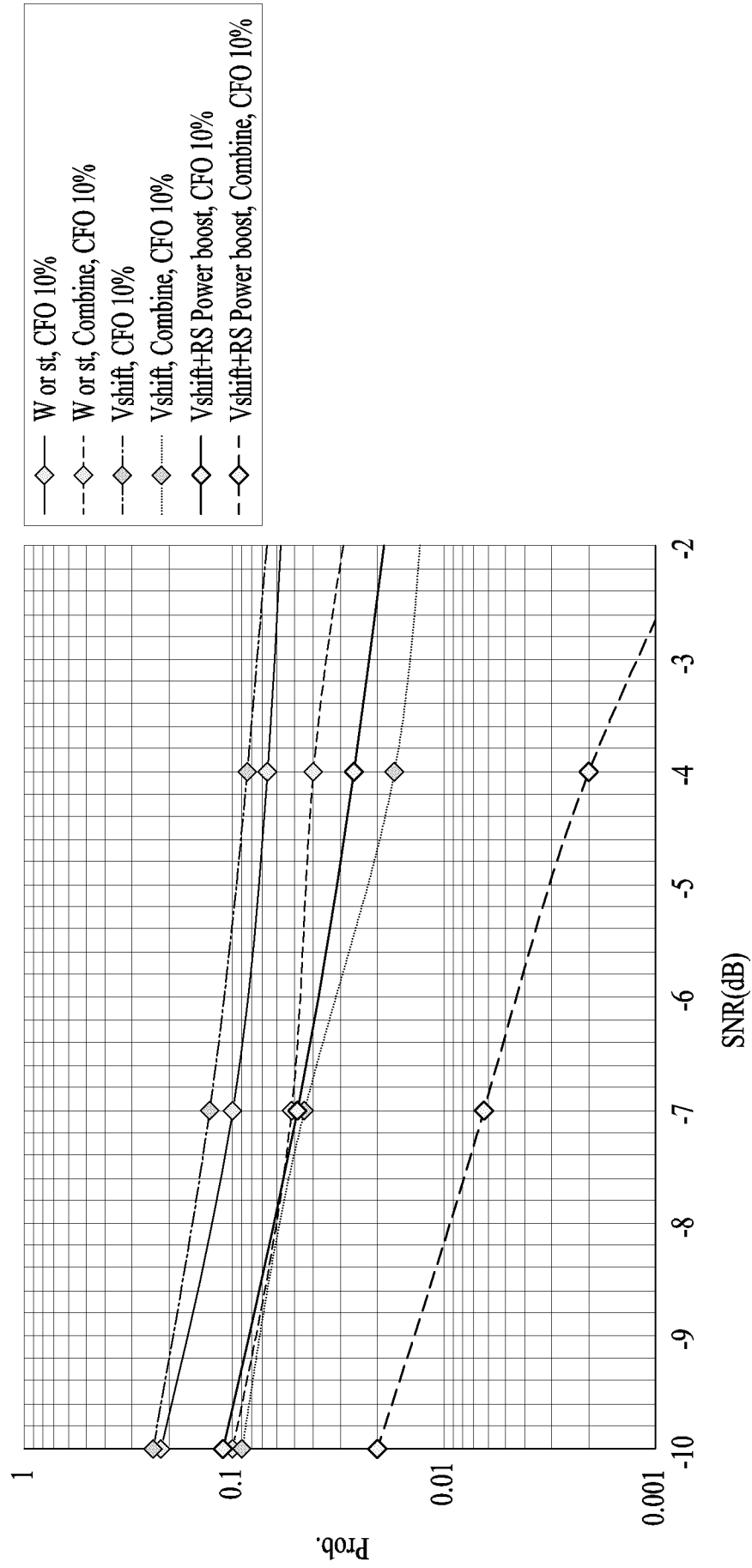


FIG. 32

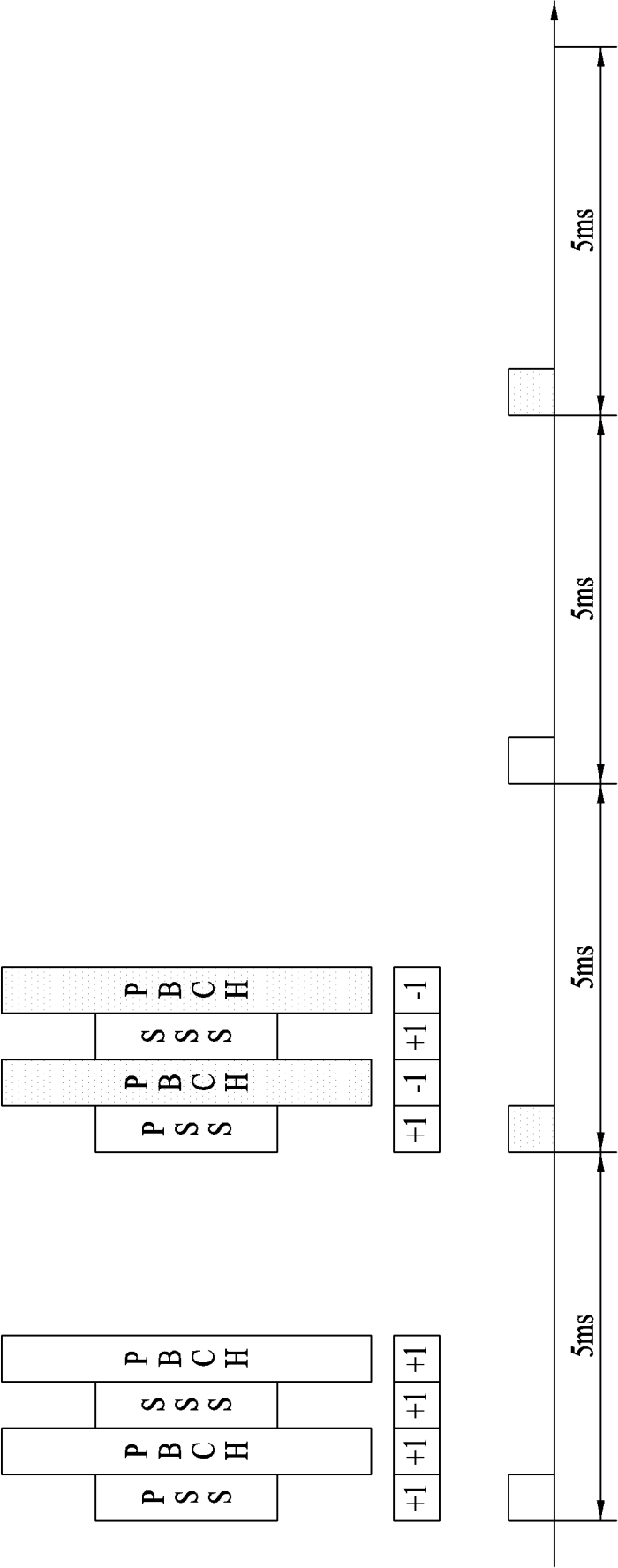


FIG. 33

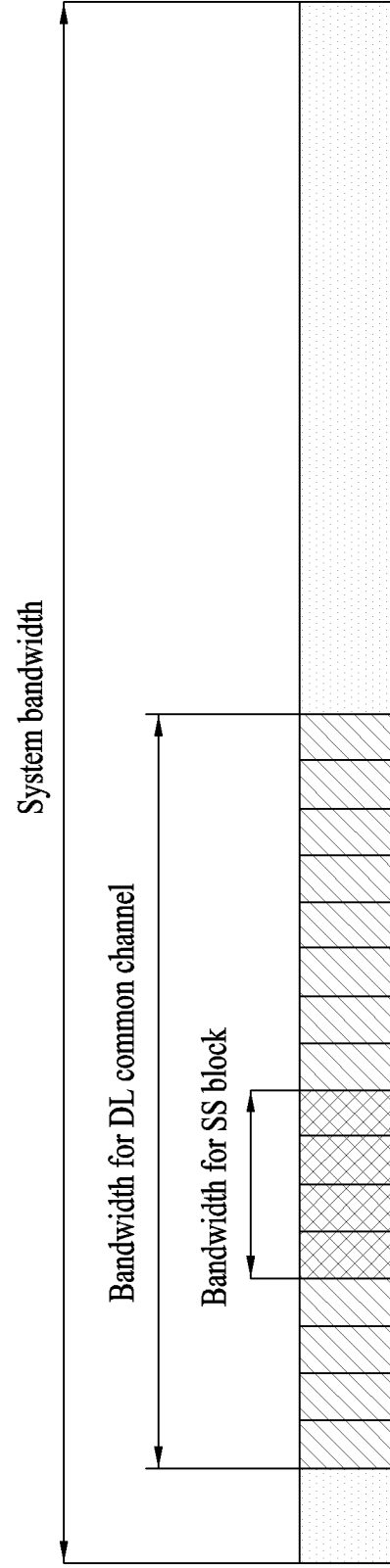


FIG. 34

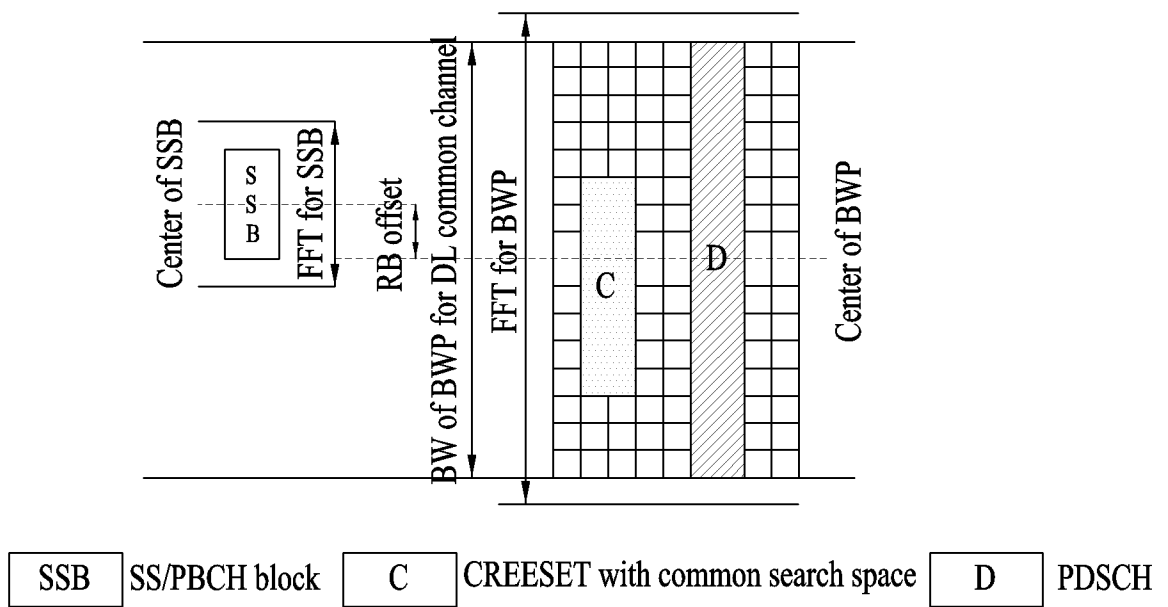


FIG. 35

