

TITLE OF THE INVENTION:

DISPLAY PANEL

ABSTRACT

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A display panel including a pixel array layer and a color resist layer. The pixel array layer is disposed on a substrate and includes a plurality of signal lines arranged along a direction. The color resist layer includes a plurality of pixel units. The pixel units are sequentially arranged along the direction, and each pixel unit includes a first color resist, a
10 second color resist and a third color resist. Each signal line corresponds to a boundary between the first color resist and the second color resist, a boundary between the second color resist and the third color resist, or a boundary between the third color resist and the first color resist. An interval between two adjacent signal lines corresponds to N color resists, wherein N is a positive integer greater than 1 and is not a multiple of 3.

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FIG. 3