

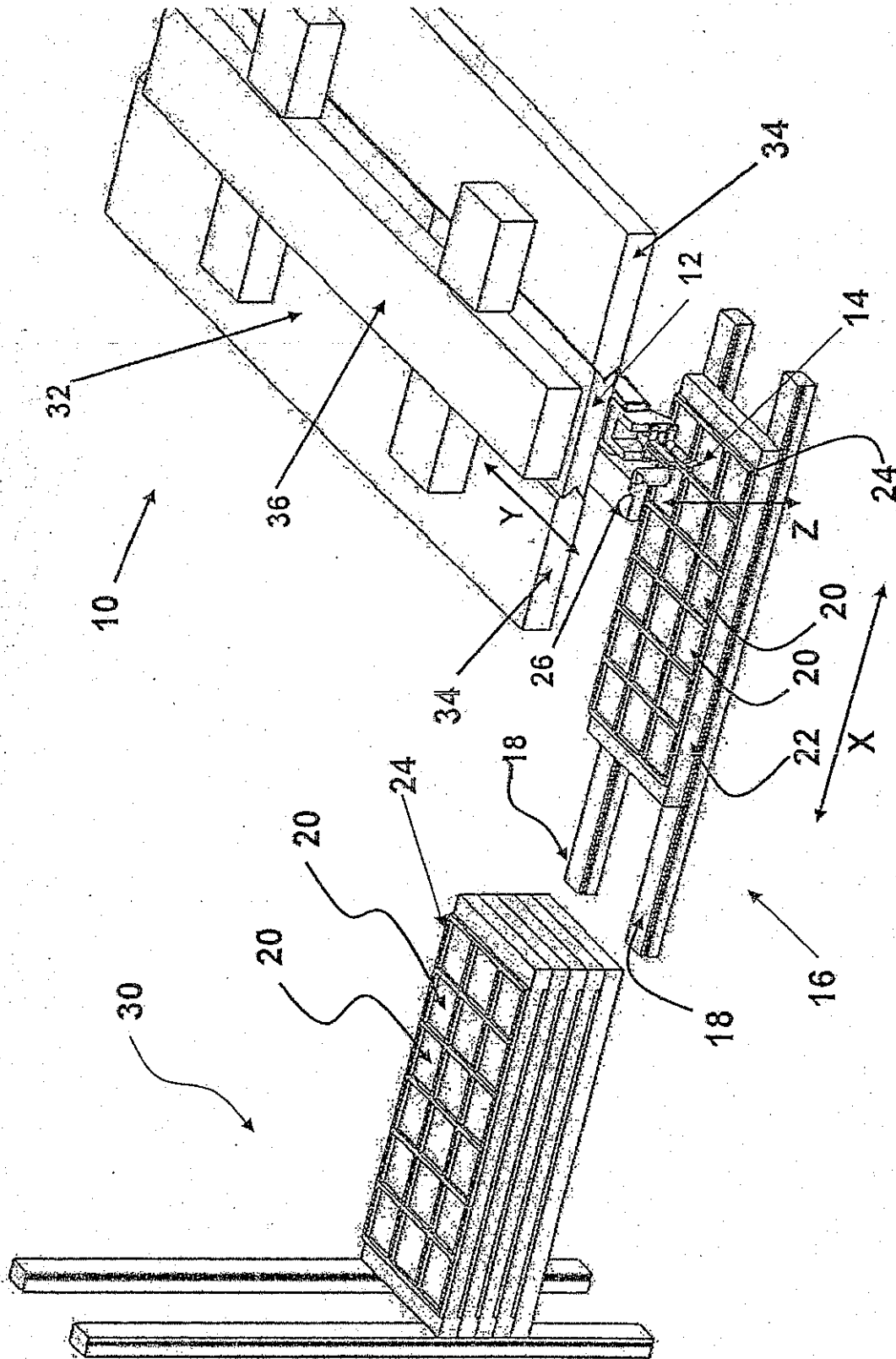


Fig. 1

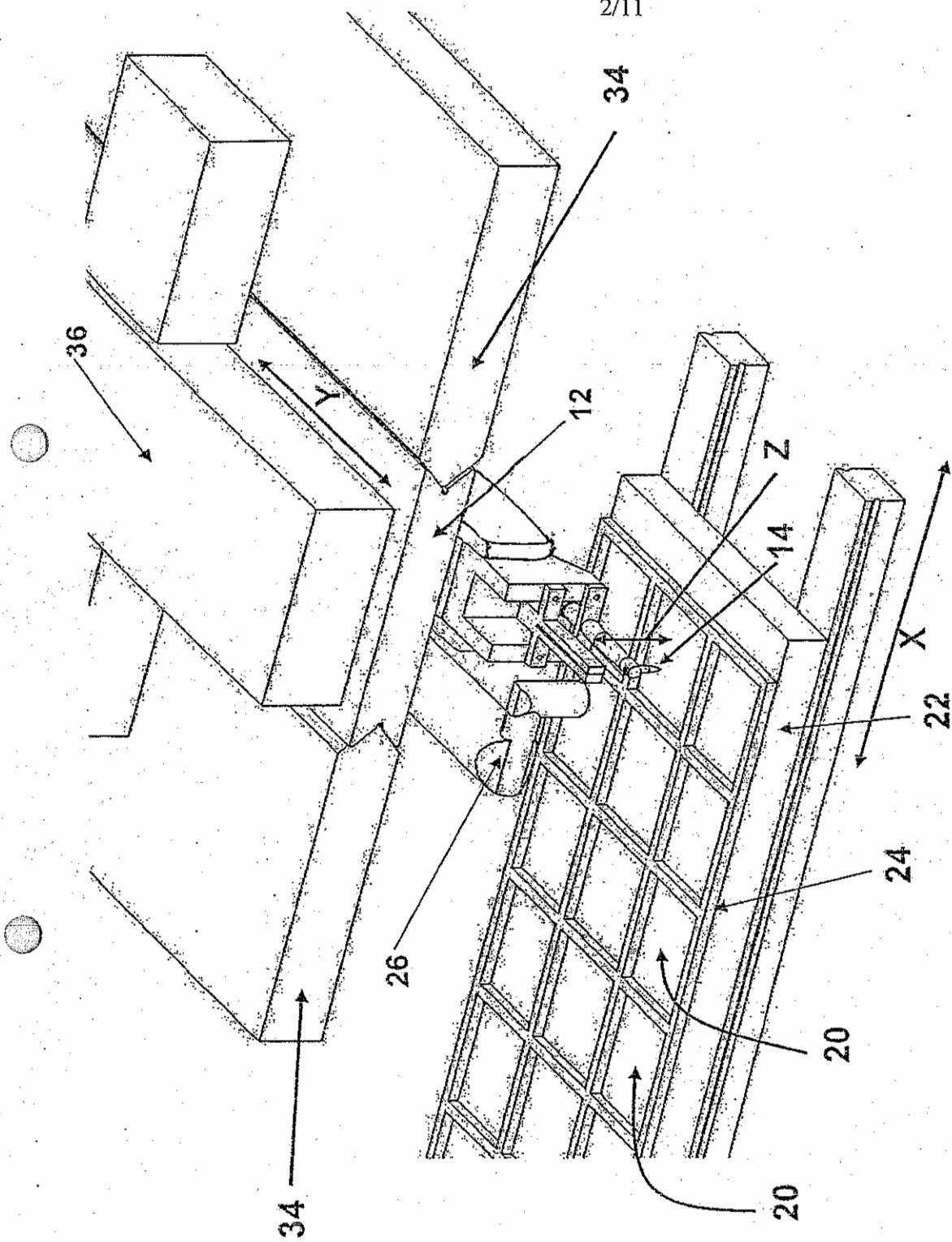


Fig. 1A

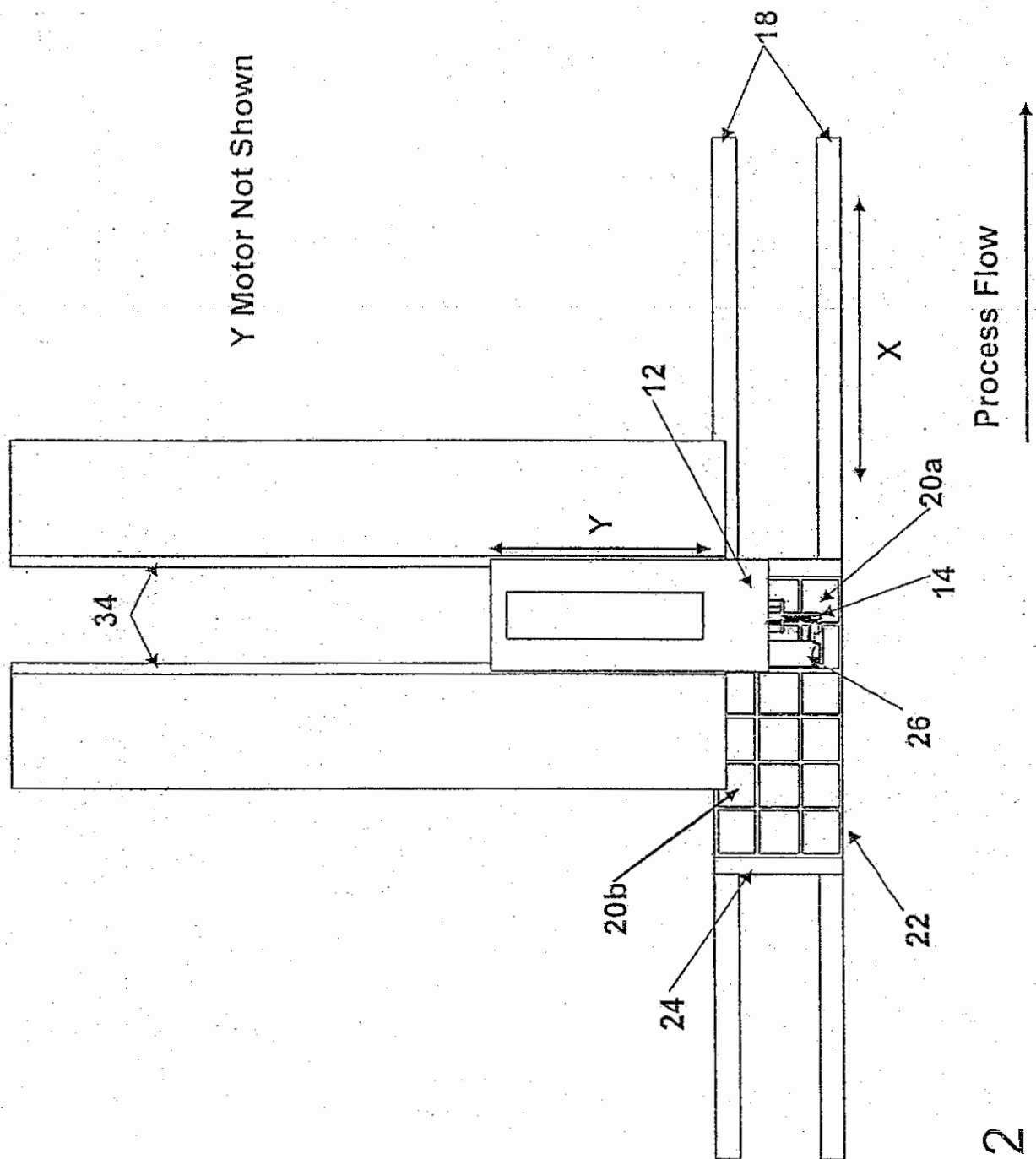


Fig. 2

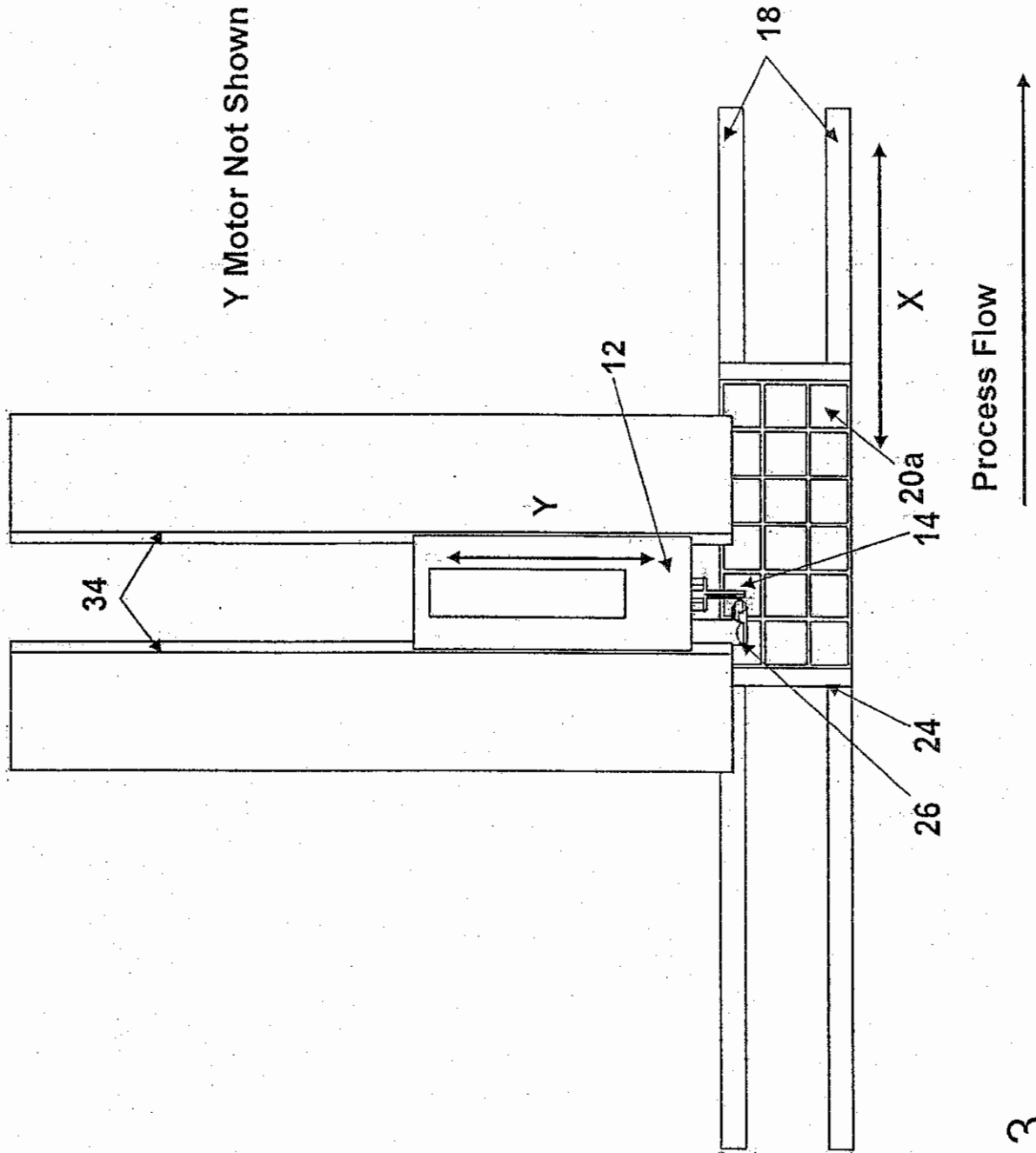


Fig. 3

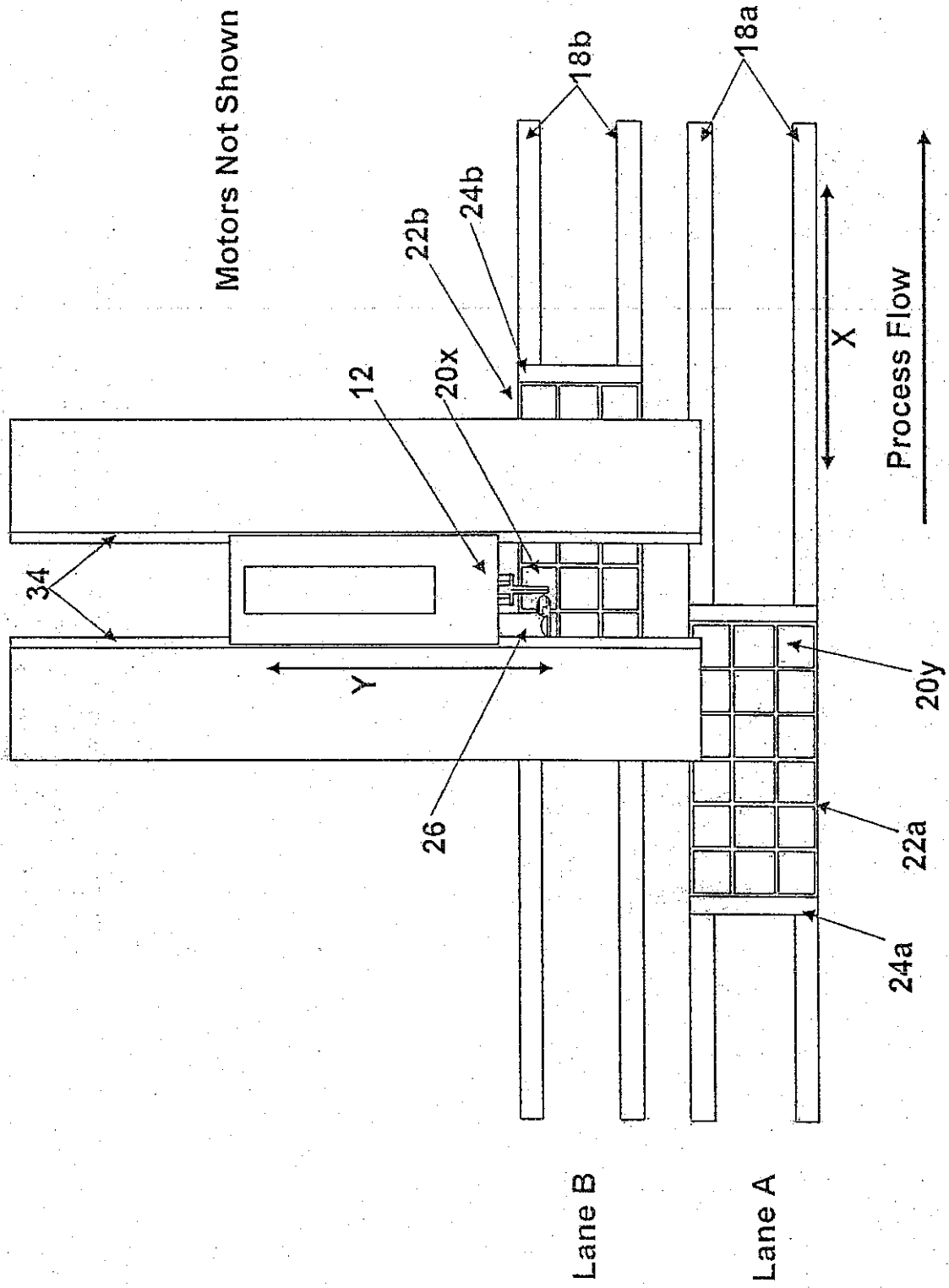


Fig. 4

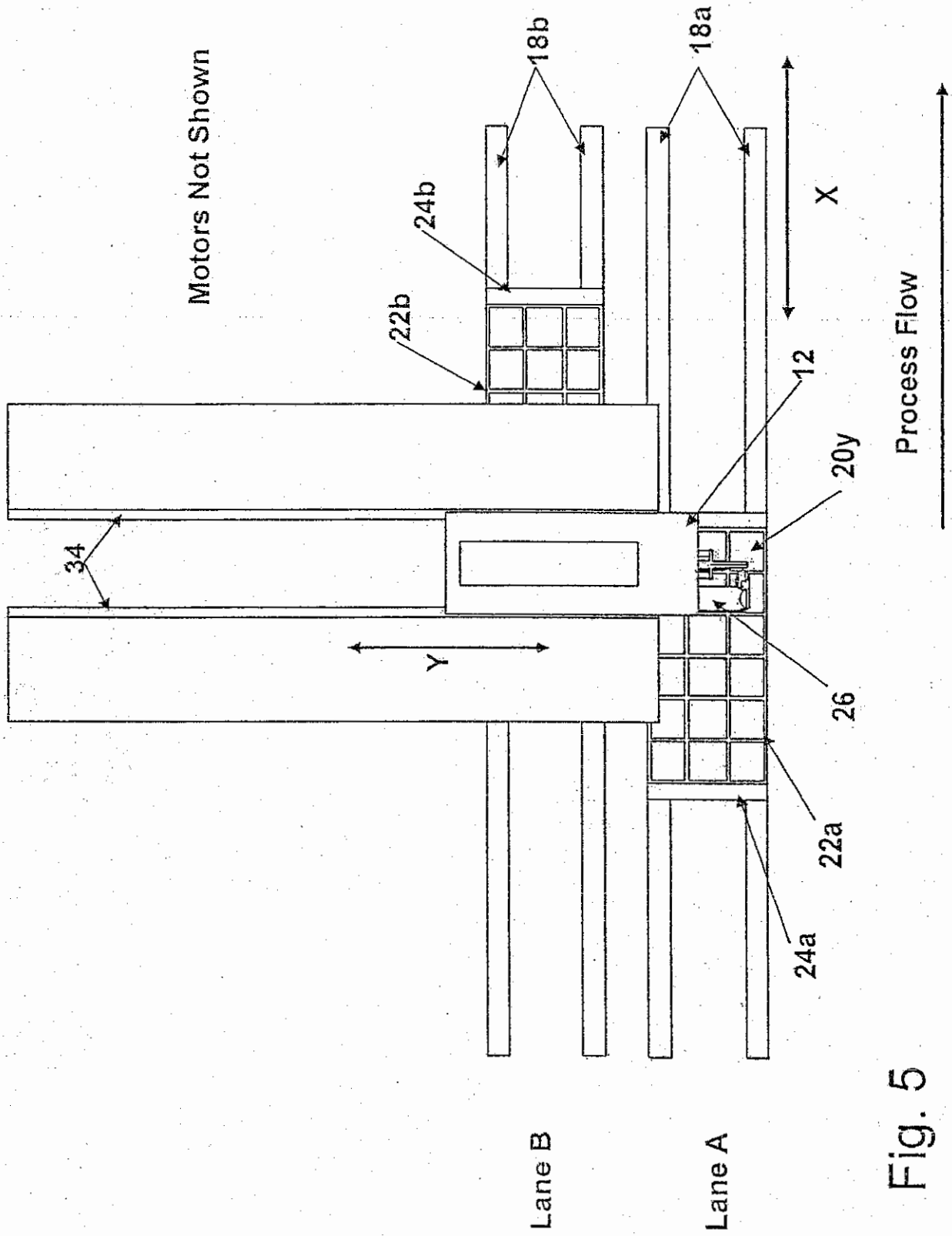


Fig. 5

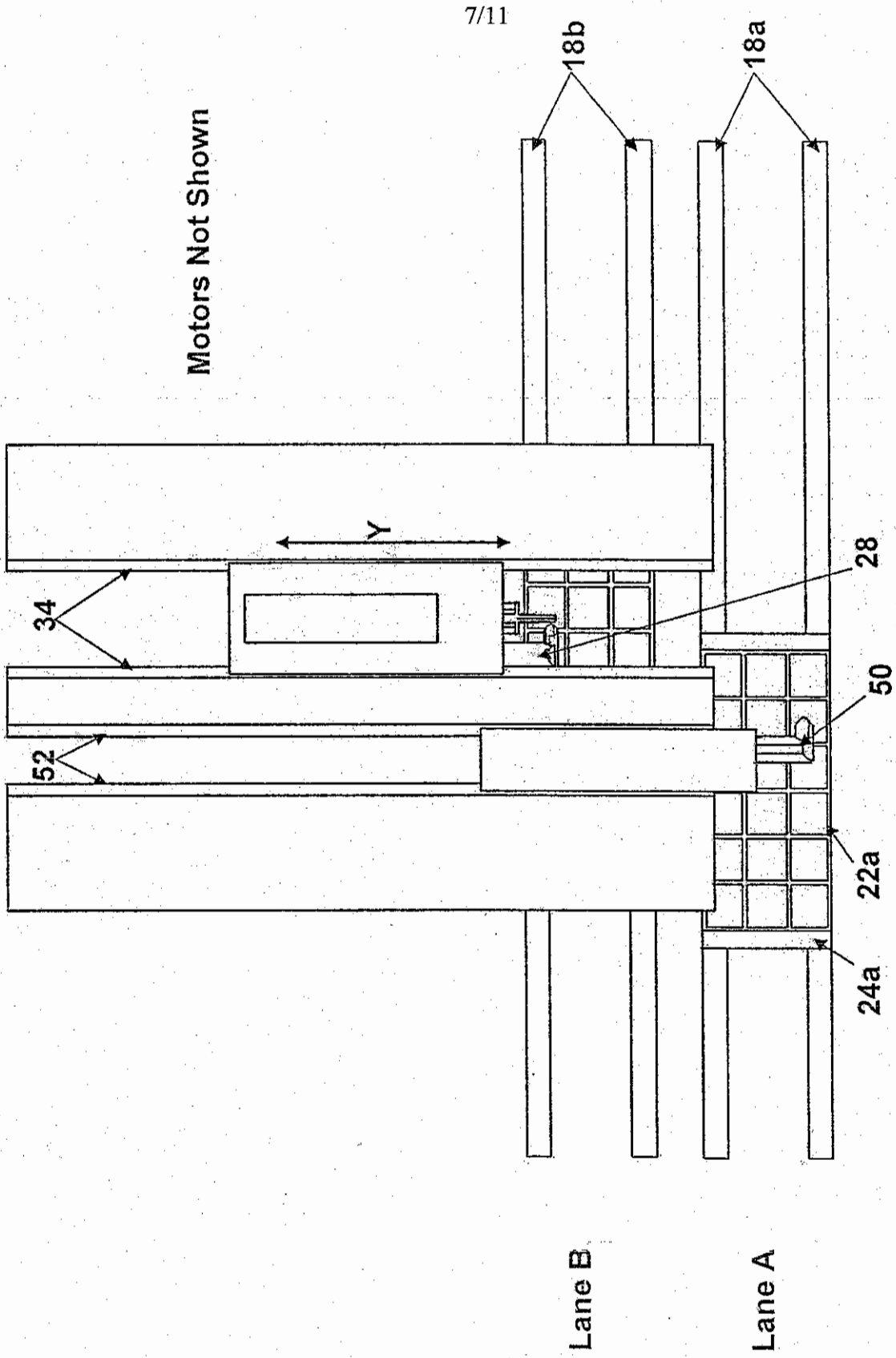


Fig. 6

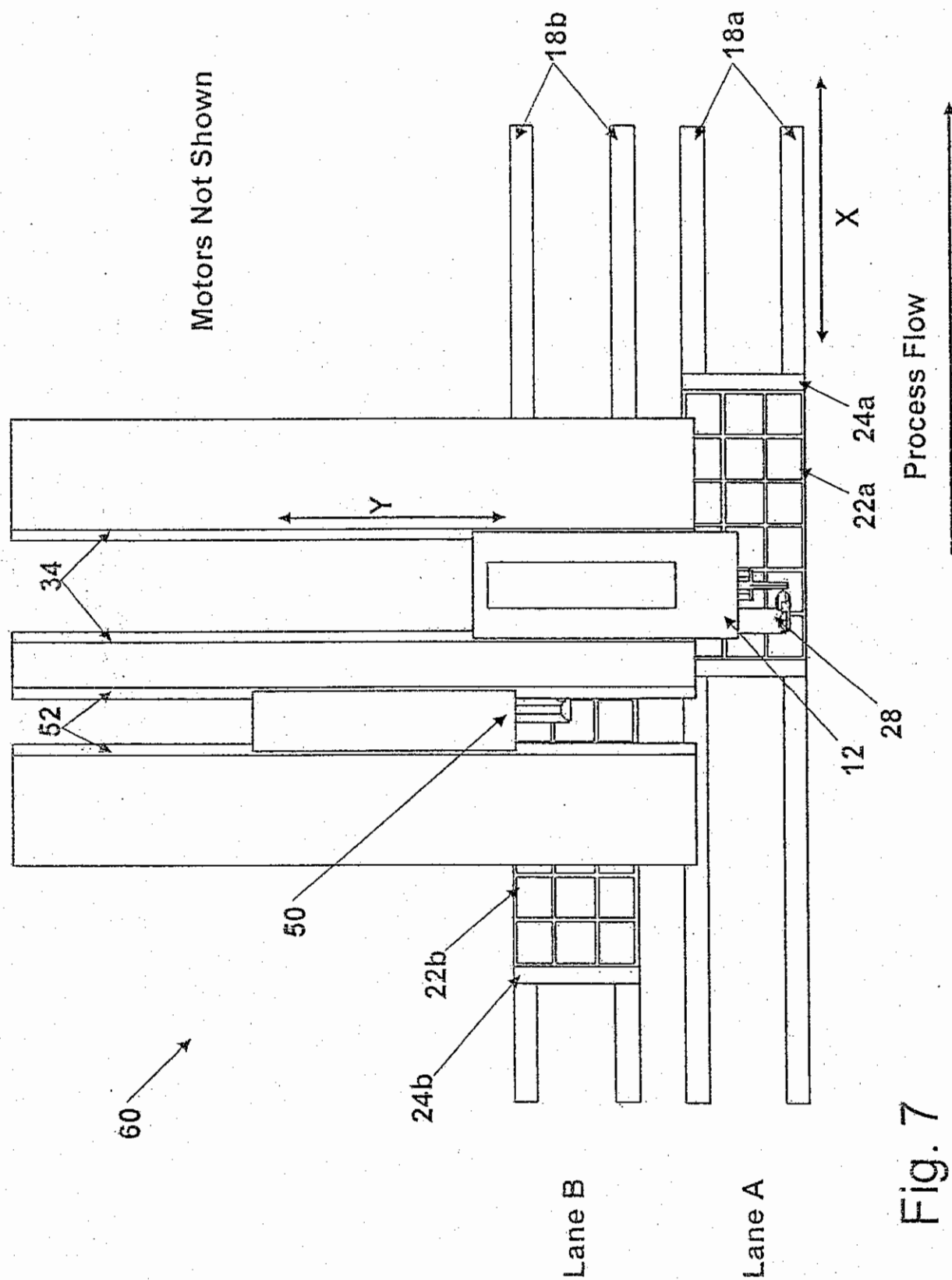


Fig. 7

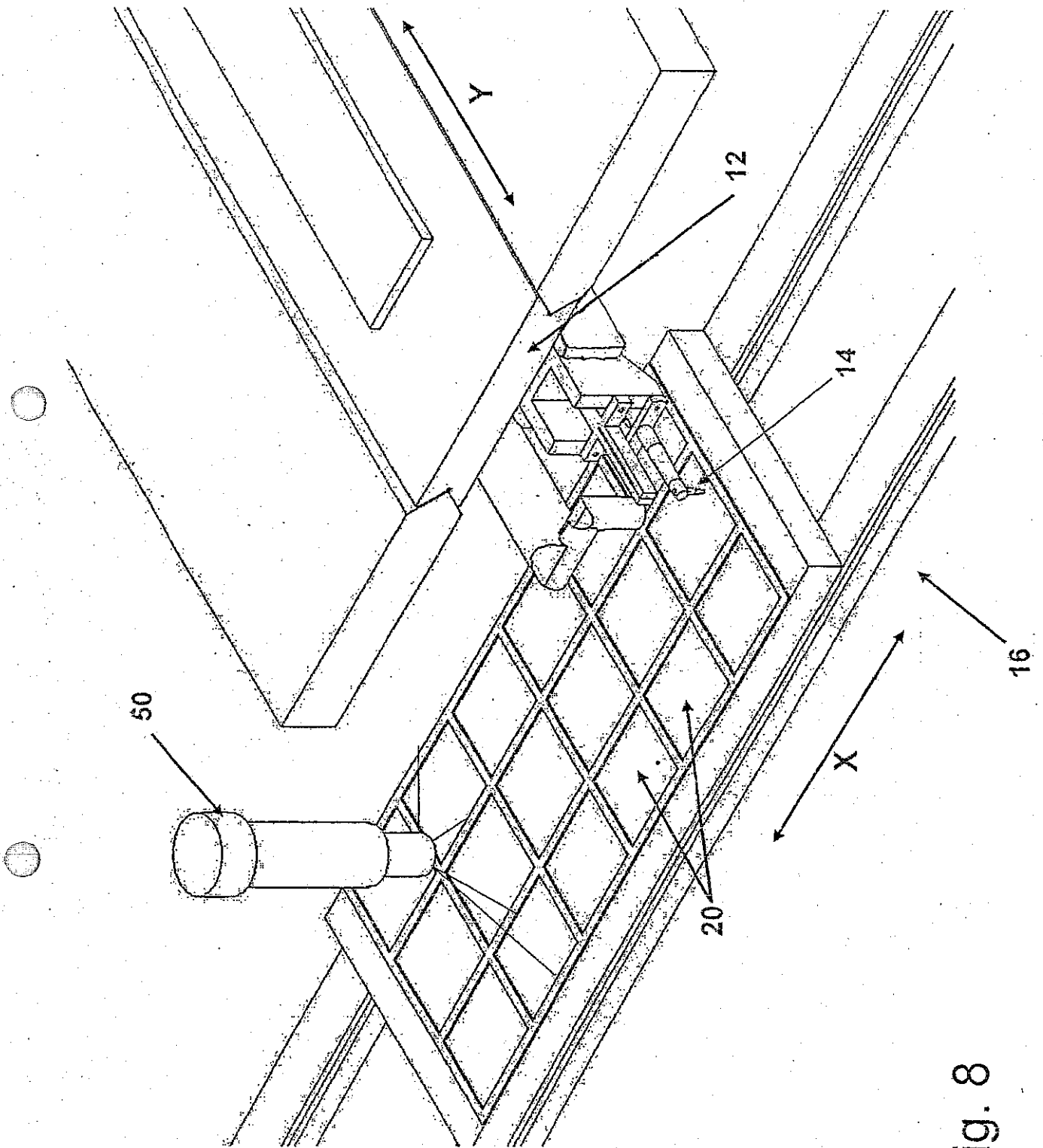


Fig. 8

Multi-Process Capability

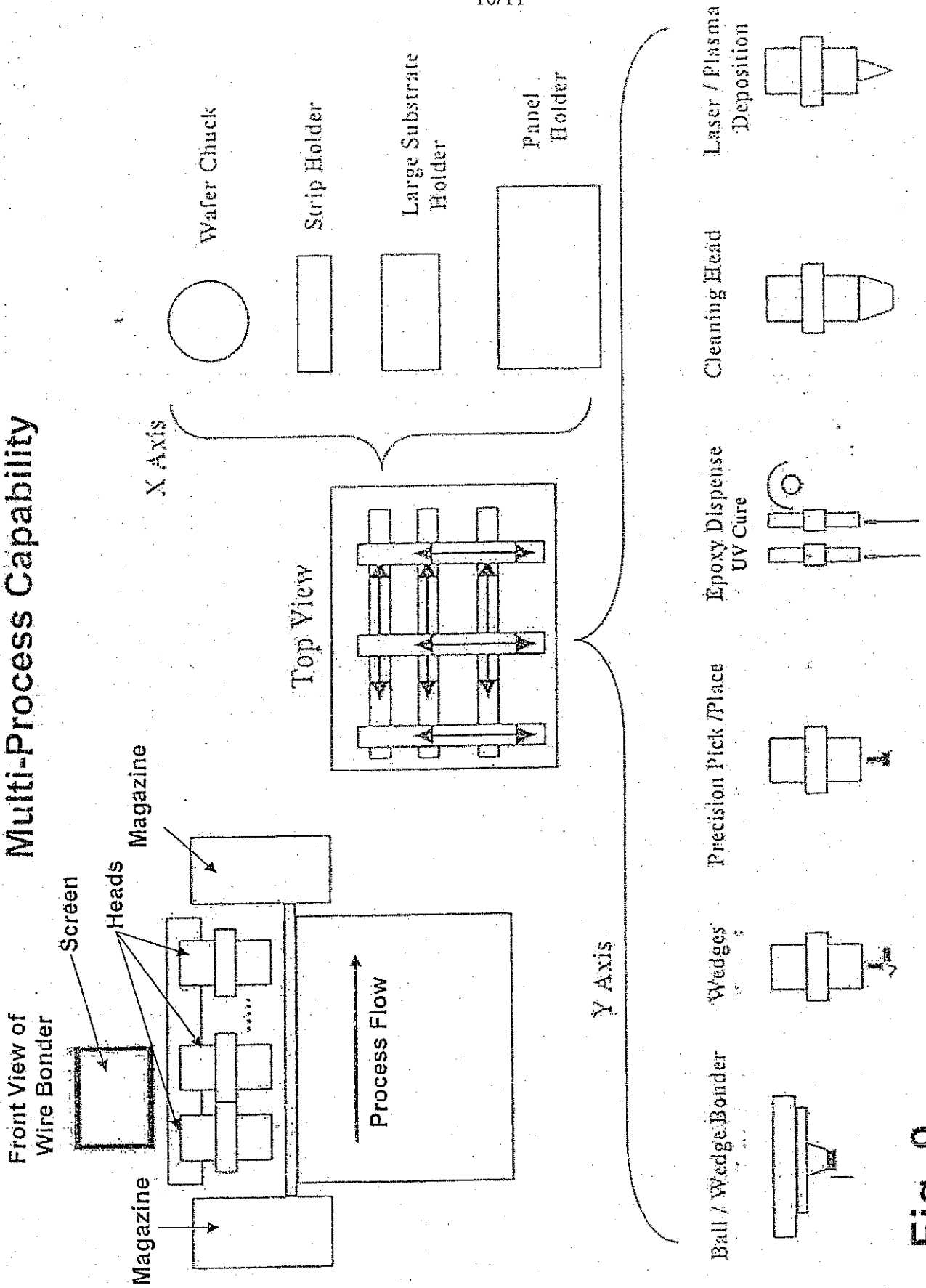


Fig. 9

11/11
Fig. 10

