

MULTI-STAGE SOLID STATE AMPLIFIER THAT EMULATES TUBE DISTORTION

BACKGROUND OF THE INVENTION

The invention pertains to amplifiers for musical instruments. In particular, the invention pertains to a solid state multi-stage amplifier which has distortion so that it sounds like a tube amplifier when overdriven.

Tube amplifiers are often preferred by musical artists because tubes produce a distorted output sound which is familiar and thought to be most pleasing. Solid state amplifiers are often preferred because they tend to be lighter, and are often less expensive to produce, are more durable and consume less power. It is difficult to make a solid state amplifier produce a distorted sound like a tube amplifier. Also, the supply of tubes available for use in amplifiers has become scarce and more expensive.

A known tube amplifier 10 is shown in Fig. 1. The amplifier is described with exemplary values of the various elements being noted for characterizing the operation of the device. The amplifier 10 illustrated in Fig. 1 is a pre-amplifier comprising four identical tube sections 12, 14, 16 and 18 (e.g., four 12AX7 tube sections), each tube section has a corresponding plate resistor 20 (100 K ohm) and a cathode resistor 22 (1.5 K ohm). Each cathode resistor 22 is bypassed with a capacitor 24 (2.2 uF). With these plate and cathode

resistor values, a typical 12AX7 amplifier tube section will idle at approximately 1 mA of plate current, approximately 1.5 volts at the cathode and about +200 volts at the plate from a +300 volt source. A positive
5 grid swing in excess of 1.5 volts peak will cause the grid to conduct. A normal guitar input is coupled to the grid of the first tube stage 12 by a coupling capacitor 34 and a grid resistor 36. A resistor 38 is coupled to the node between the capacitor 34 and grid
10 resistor 38 and provides a ground reference for the input to tube 12. Feedback capacitor 40 (10 PF) is coupled between the plate and grid of tube 12 and provides some control of high frequency roll-off, known as the Miller effect, which helps to keep the amplifier
15 stable at open input conditions. The signal from the plate of amplifier 12 is coupled to input of amplifier stage 14 via capacitor 42 and grid resistor 44. Resistor 46 provides a ground reference for stage 14. Resistors 44 and 46 act as a voltage divider. The
20 signal from stage 14 is likewise coupled to stage 16 via capacitor 48 and grid resistor 50, with resistor 52 providing a ground reference for the input and voltage division. Finally, the signal from stage 16 is coupled to the stage 18 via capacitor 54, grid resistor 56 and
25 reference resistor 58 to ground also with voltage

division. The output of stage 18 is coupled to the output of the pre-amplifier 10 by output capacitor 60.

The coupling capacitor values 42, 48 and 54, as well as the values of the divider resistances 44/46, 50/52 and 56/58 are chosen in a known manner to provide good distorted sound. Typically, with a guitar level input signal applied, the first stage 12 is clean and free of distortion, although with some high level guitars, even this stage clips at times. The first stage output signal level is high enough to cause input clipping at the second stage 14 because the grid of the second stage 14 is driven positive with respect to the cathode and conducts for a substantial portion of the input cycle. Input clipping at stage 14 results in an average negative voltage on the grid, causing the operating point of the second stage 14 to shift dramatically resulting in a significant amount of second harmonic distortion. The signal at the plate of the second stage 14 resembles a square wave with about two-thirds of the period spent in the positive half cycle. The plate of the second stage 14 has a high enough signal level to cause significant input clipping at the third stage 16. Here too, the grid swings positive with respect to the cathode. Thus, input clipping causes the operating point of the third stage 16 to shift. This is repeated yet one more time,

resulting in input clipping and operating point shift of the fourth stage 18. The output at the plate of the fourth stage 18 has gone through several different levels of clipping at the input and output and several
5 operating point shifts and is thus rich in harmonics. All of this essentially results in a characteristic sound which is referred to as good tube sound.

In the exemplary pre-amplifier 10 illustrated in Fig. 1, the available peak plate swing in the positive
10 direction for any stage is about 100 volts (i.e., about one-half the plate voltage). Further, each grid conducts at a positive peak swing of about 1.5 volts. The ratio of 100 to 1.5 or 66.7 is a high number, and its value is important to shift the operating point of
15 each stage enough to generate the appropriate amount of second harmonic distortion. The values of divider resistors 44/46, 50/52 and 56/58 are also critical, and are carefully chosen to set just the right amount of input clipping and resulting second harmonic distortion
20 to produce a pleasing sound.

Of note here are two key ingredients in the so called distorted tube sound. First, the tube characteristics themselves with the 100 volt output capability and only a 1.5 volt input clipping
25 capability or level is unique and required for successful generation of the second harmonic

distortion, and that so called tube sound. Secondly, the multiplicity of stages is necessary for a sustained distortion sound as the guitar output level drops after being plucked by the musician. Although more or fewer stages may be employed, at least three, and preferably four stages are required to achieve the desired distortion sound sought by most musicians.

There is, therefore, a need for a solid state amplifier which is capable of replacing the various tube stages in a multi-stage pre-amplifier, and which may be overdriven to emulate the tube sound produced by the such known tube amplifiers.

SUMMARY OF THE INVENTION

The invention is based upon the discovery that the distortion associated with the flow of current in the grid of a tube amplifier operated at high input levels is duplicated in a multi-stage solid stage amplifier by means of a clipping device in the coupling circuit between stages.

The invention comprises a multistage solid state amplifier for emulating the distortion associated with a tube pre-amplifier when overdriven. The invention includes a plurality of solid state tandem or series connected amplifier stages having an input circuit and an output circuit. Each downstream stage has its input

circuit coupled to the output circuit of an upstream stage. A clipping device such as a diode is coupled in the input circuit between the stages.

5 In a particular embodiment, each solid state amplifier stage includes a field effect transistor (FET) having its output terminal coupled to the input of the next downstream stage and the diode is located in the input circuit so as to duplicate the desirable input clipping characteristics of a tube amplifier,
10 wherein the ratio of the output capability to the input clipping level between stages is sufficient to result in adequate second harmonic distortion.

In another embodiment, each solid state amplifier stage comprises a transistor. An alternative
15 arrangement employs a Darlington transistor. When separate transistors are Darlington connected, an internal base resistor to ground may be employed to improve turn-off characteristics of each stage.

In yet another embodiment, an input diode is
20 employed with a multilevel bias circuit to duplicate the input characteristics of a tube amplifier.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic illustration of a known
25 vacuum tube amplifier which exhibits a desirable distorted output;

Fig. 2 is a schematic illustration of a multi-stage solid state amplifier according to the invention, employing FET devices, which emulates the distortion and sound produced by known tube amplifiers;

5 Fig. 3 is a schematic illustration of a particular embodiment of a multi-stage solid stage application according to the invention; and

10 Fig. 4 is a schematic illustration of another embodiment of the invention similar to the arrangement of Fig. 2 wherein each FET is replaced by a Darlington connected pair of transistors and a bias circuit with multiple biasing levels along with an input diode.

DESCRIPTION OF THE INVENTION

15 The present invention as illustrated in Fig. 2, is directed to a solid state multi-stage amplifier 110 which uses components arranged in a manner similar to the known device of Fig. 1. In Fig. 2, the components are numbered with reference numerals which correspond
20 to the reference numerals of Fig. 1 in a 100 series, and the tube sections of the known device have been replaced with four solid state amplifier stages 112, 114, 116 and 118. The devices illustrated are field effect transistors (FET) sometimes referred to as J-FET
25 devices, which are supplied by a 40 volt supply. Each amplifier 112-118 has a source S, a drain D and a gate

G terminal as illustrated. The drain D corresponds to the output of the device, and the gate G corresponds to the input of the device. Each stage 112-118 includes a drain resistor 120 similar in value to the plate resistor in Fig. 1 (100 K ohm). In addition, each stage employs a bias circuit including a resistor 122 (33 K ohm) and a bypass capacitor 124 (2.2 uF) in parallel. The source S is coupled through the source resistor 125 and the bias circuit to ground in a self biasing configuration, as shown. In addition, each source S has a source resistor 125 (1 K ohm) to set the gain nominally at 100, which is similar to most tube stages. The drain and source resistor values are adjusted so that each FET stage 112, 114, 116 and 118 with a pinch-off voltage of approximately 6 volts will idle at approximately 180 uA of source current. Each will have approximately +22 volts at the drain D and approximately +6 volts at the source S.

In Fig. 2, the drain D, or output, of stage 112 is coupled to the gate G, or input, of stage 114 by coupling capacitor 142 and gate resistor 144. Resistor 146 provides a ground reference to the gate G of stage 114. Also, resistors 144 and 146 act as a voltage divider. Similarly, as in Fig. 1, the successive stages 116 and 117 are coupled by a corresponding combination of a coupling capacitor, gate resistor and

reference resistor 148, 150 and 152 and 154, 156 and 158 respectively. The final stage 118 is coupled to the output by means of coupling capacitor 160. The input stage 112 has a Miller capacitor 140 between
5 drain D and the gate G, as illustrated.

In Fig. 2, without the clipping means, the available peak drain signal output capability or swing in the positive direction for any stage is about 18 volts, i.e., the difference between the drain D and the
10 source S. In each amplifier stage 112-118, the gate G operates as a diode which conducts at a positive peak of about 7 volts. Thus, the ratio of the drain output capability (18 volts) and the gate swing (7 volts) is about 2.57. This ratio is insufficient to cause
15 adequate second harmonic distortion.

In the present invention, clipping means is provided between the stages. In the embodiment disclosed, clipping may be achieved by means of diodes 162, 164 and 166 provided in parallel with the
20 corresponding reference resistors 146, 152 and 158 in each of the respective stages 114, 116 and 118. Each diode 162, 164 and 166 has its cathode coupled to ground and its anode coupled to a node between the divider resistors 144/146, 150/152 and 156/158 of each
25 stage. Each diode 162, 164 and 166 conducts in the forward direction and thereby establishes a clipping

level at approximately +0.5 volts change in gate swing to emulate distortion associated with grid conduction in a tube amplifier. The ratio of the drain signal output capability or swing (+18 V) to the gate clipping level (+0.5V) is $18/.5=36$, which is not as high as a tube circuit. However, while a higher ratio is desirable, in a solid state circuit, a ratio of about 30 is sufficient to cause adequate second harmonic distortion in each stage. Accordingly, the solid state, multi-stage pre-amplifier 110 of the invention produces distortion performance which is quite similar to that of the tube circuit in Fig. 1 by employing a low level input clipping means, such as diodes 162, 164 and 166.

It should be understood that other solid state devices may be employed other than the J-FET type devices and the diode illustrated. Also, gain, coupling and high-frequency characteristics may be tailored by changing the various element values. However, the clipping means provided between stages is effective to produce the operating second harmonic distortion which causes tube-like sound from a solid state amplifier.

Fig. 3 is an illustration of another embodiment of a multi-stage, solid state 210 amplifier according to the invention. Similar elements have similar reference

numbers as shown in Fig. 2 in a 200 series. In Fig. 3, however, a potentiometer 213 is substituted for the fixed divider resistors 144 and 146 between stages 112 and 114. The wiper is coupled to the input of stage 214 and the potentiometer 213 allows maximum distortion in the CW direction where the maximum signal is applied to stage 214.

At the output, a potentiometer 261 is coupled to the coupling capacitor 260. The wiper acts as the output terminal. The output level is maximum when the wiper is in the full CW position. The ability to independently vary the distortion and to vary the level adds versatility to the circuit and allows the artist to tailor the distorted sound and volume at will.

Fig. 4 is an illustration of another embodiment of a multi-stage, solid state 310 amplifier having stages 312, 314, 316 and 318 according to the invention. Similar elements have similar reference numbers as shown in Fig. 2 in a 300 series. In Fig. 4, however, a pair of Darlington connected transistors 312A-312B, 314A-314B, 316A-316B and 318A-318B is substituted for each corresponding FET 112, 114, 116 and 118. A multilevel biasing system with an input diode 374 is also provided. An internal base resistor 370 for each stage may be employed for favorably affecting the turn-off characteristics.

In Fig. 4, the circuit function is similar to that of Fig. 2. However, the use of the Darlington connected transistors allows cost and performance advantages. The FET devices described above require a
5 tight pinch-off voltage limit which increases the price considerably. For the transistor devices, the input essentially looks like two diodes which is predictable in terms of performance.

The amplifier 310 of Fig. 4 uses a pair of NPN
10 type Darlington connected transistors 312A-312B, 314A-314B, 316A-316B and 318A-318B for each stage. The NPN type is chosen because the supply is a positive 40V. A negative supply would allow an opposite device type. Alternatively, a single transistor would operate in the
15 embodiment discussed herein. However, a single transistor would not offer a gain or input impedance to match that of the tube or the FET. Thus, paired transistors are preferred. An integrated Darlington NPN transistor may also be used. However, the discrete
20 transistors offer slightly better performance at high frequencies, because access to the internal base connection is available. In addition, discrete transistors cost less.

The design of Fig. 4 requires a circuit
25 arrangement to bias the transistors. Accordingly, a separate bias supply VCC is provided. The bias voltage

for the first stage 312 is adjusted to a first level and a common bias voltage is used for the remaining three stages 314-318. In the exemplary embodiment, all stages 312-318 employ respective 470K bias supply resistors 338, 346, 356 and 358 and 150K collector resistors 320. Each inter-stage has a corresponding input diode 362, 364, 366 to cause the operating point shift therein. Further, each stage has a 470K base resistor 370 from the available internal base to ground in the base emitter circuit between the transistors to improve the turn-off characteristics. The input stage 312 has an input diode 374 for better tube input circuit emulation as discussed hereinafter.

Many of the newer or more contemporary guitars provide a relatively high output voltage, e.g., 3-5V. It is thus important to have good preamp input overload. Accordingly, the first stage 312 has been biased to duplicate the input overload of a typical 12AX7 tube grid by providing a multilevel bias arrangement.

In the arrangement, bias circuit 380 includes a divider network including resistors 382, 384, 386 and 388 and filter capacitors 391. Resistor 382 is coupled to the anode of input diode 374 and establishes a diode bias level (e.g., 3.5V DC) for stage 312. Resistor 384 is coupled to the base resistor 338 for establishing

the base bias of stage 312 (e.g., 2.5V DC). Resistor 386 and diode resistor 388 establish a common bias voltage for the downstream stages 314-318, as shown (e.g., 1.5V DC).

5 As noted in the exemplary embodiment, VCC is divided to establish a 2.5 volt base bias supply, a 3.5 volt diode supply and a common 1.5 volt supply. In each of the stages 314-318, the corresponding clipping diode 362, 364, 366 is also connected to the common 1.5
10 volt supply.

 In the exemplary embodiment, with the first stage input transistor 312A base is biased at 2.5 volts, the emitter of output transistor 312B finds itself at about 1.5 volts (i.e., two diode drops 0.5V), which is the
15 same value as the cathode in the previously described tube circuit (Fig. 1). This means that a negative peak swing of 1.5 volts at the input of this stage 312 will cause the operating collector current to go to zero (negative clipping). Such is also the case for that of
20 the tube circuit of Fig. 1. Thus, the circuit of Fig. 4 looks like a tube circuit input.

 The first stage input diode 374, conducts at 0.5V. The 3.5 volt supply, is 1 volt greater than the base supply voltage. The sum of the diode bias voltage of
25 3.5V plus the diode drop of .5V equals 4V which is 1.5V above the base bias voltage of 2.5V. This means that

a positive peak of 1.5 volts at the input of this stage will cause the input diode 374 to conduct and force an operating point shift (positive clipping). This is also the case for that of the tube circuit. Thus, the
5 circuit of Fig. 4 matches the input dynamic range of the tube circuit.

As noted in Fig. 2, capacitor 340 provides some controlled high frequency roll-off (Miller effect), and the emitter of 312B has series resistors 322 and 325 to
10 ground, with resistor 322 bypassed with capacitor 324. This circuit arrangement provides idle current and gain values to cause overload conditions that match a typical tube first stage. A series circuit, including a resistor 390 and capacitor 392 is coupled across the
15 emitter circuit to achieve a high frequency boost.

The first stage output signal is coupled to the input of the second stage 314 through capacitor 342 and resistor 344 in a way similar to the arrangement of Fig. 2. The remaining three stages 314, 316 and 318
20 are similar, although component values may change to achieve the desired amount of clipping, operating point shift and frequency response to produce a pleasing overload sound. For example, inter-stage coupling is provided by capacitors 342, 348 and 354 and resistors
25 344, 350 and 356. In each downstream stage 314-318, the emitters have dual series resistors 322-325 to

ground and the ground resistor 322 is bypassed with capacitor 324. Also, each stage employs series resistors 390 and capacitors 392 to ground to provide high frequency boost. The output is delivered via capacitor 360.

The available peak collector swing in the positive direction for any stage is about 20 volts. Each of the three inter-stage clipping diodes 362, 364 and 366 conducts in the forward direction at approximately 0.5 volts. Thus, the ratio of input swing to clipping voltage $20/.5 = 40$ is adequate to cause the operating point shift of each stage, and the distortion performance is very similar to that of a tube circuit.

It should be understood that the input diode 374 may be employed with an appropriate bias circuit in the arrangement of Figs. 2 or 3. This circuit allows the circuit to have the same input characteristics as a tube circuit.

In the arrangement of Fig. 4, a volume potentiometer similar to the potentiometer 213 may be inserted in the circuit after capacitor 342, and a master volume potentiometer similar to the potentiometer 261 may be connected after capacitor 360.

It should be noted that this invention is not simply a diode clipping means, and an operating point shifter. Several U.S. patents discuss operating point

shift by means of a diode, with resulting second harmonic distortion. The invention shows the important discovery that a diode with a mere 0.5V forward voltage clipping value, when driven from a typical solid state device with an output capability of about 20 to 30 volts will closely match the particular output/input ratio of the existing tube circuits and therefore closely emulate the tube sound. The present invention also teaches that multiple operating point shifts produced by multiple stages generate a multiplicity of levels and amounts of second harmonic distortion over a wide range input signal levels. The invention further shows a way to duplicate the input overload characteristic of the typical first stage of a tube circuit with a solid state device. The result then, is the so-called tube distortion sound; a sound that can be generated almost exactly with solid state devices.

While there have been described what are at present considered to be the preferred embodiments of the present invention, it will be apparent to those skilled in the art that various changes and modifications may be made therein without departing from the invention, and it is intended in the appended claims to cover such changes and modifications as fall within the spirit and scope of the invention.



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WHAT IS CLAIMED IS:

1 1. A solid state amplifier for emulating the
2 distortion associated with a flow of current in the
3 grid of an overdriven multi-stage tube amplifier at
4 high input signal levels resulting in a desirable input
5 clipping characteristic comprising:

6 a plurality of series connected solid state
7 devices each including an input circuit and an output
8 circuit having an output signal capability, each
9 downstream stage having its input circuit coupled to
10 the output circuit of an upstream stage; and

11 clipping means in the input circuit between each
12 of the stages for establishing a clipping level between
13 such stages, duplicating in such solid state amplifier
14 the desirable input clipping characteristic of a tube
15 amplifier wherein the output signal capability and the
16 input clipping level between stages are in a ratio
17 sufficient to result in adequate second harmonic
18 distortion.

1 2. The amplifier of claim 1 wherein each device
2 comprises a field effect transistor.

1 3. The amplifier of claim 1 wherein each device
2 comprises a transistor.

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1 4. The amplifier of claim 1 wherein each device
2 comprises a Darlington transistor.

1 5. The amplifier of claim 4 wherein the
2 Darlington transistor is integrated.

1 6. The amplifier of claim 4 wherein the
2 Darlington transistor comprises discrete elements.

1 7. The amplifier of claim 6 wherein the
2 Darlington transistor includes a base-emitter circuit
3 and a bias resistor coupled between the base-emitter
4 circuit and ground.

1 8. The amplifier of claim 1 wherein the clipping
2 means comprises a diode.

1 9. The amplifier of claim 8 wherein the output
2 signal capability is effective to forward bias the
3 diode in the input circuit of the next amplifier stage.

1 10. The amplifier of claim 8 wherein the diode
2 produces forward voltage clipping at about .5V.

1 11. The amplifier of claim 1 wherein the ratio is
2 at least about 30.





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1 12. The amplifier of claim 1 further including
2 variable input means for at least one of said input
3 circuits for varying the distortion.

1 13. The amplifier of claim 12 wherein variable
2 input means comprises a potentiometer coupled in output
3 circuit of a first stage, said potentiometer having a
4 wiper coupled to the input of the second stage and the
5 clipping means.

1 14. The amplifier of claim 1 further comprising
2 variable output means coupled in the output circuit of
3 the last stage for varying an overall output level of
4 the amplifier.

1 15. The amplifier of claim 14 wherein the
2 variable output means comprises a potentiometer.

1 16. The amplifier of claim 1 further comprising
2 biasing means for the solid state devices.

1 17. The amplifier of claim 16 further comprising
2 multilevel biasing means including an upstream biasing
3 means for a first upstream one of the devices and a
4 downstream biasing means for the downstream devices.

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1 18. The amplifier of claim 17 wherein the
2 upstream biasing means comprises a base circuit biasing
3 means coupled to a base element of the upstream solid
4 state device.

1 19. The amplifier of claim 1 further comprising
2 input diode means coupled between an input of the first
3 upstream stage and a reference.

1 20. The amplifier of claim 19 further comprising
2 upstream biasing means for the diode.

1 21. The amplifier of claim 1 further comprising
2 a diode circuit biasing means for establishing a diode
3 circuit bias level, a base circuit biasing means for
4 establishing a base circuit bias level and a downstream
5 biasing means for establishing a downstream bias level
6 and wherein the diode biasing level is greater than the
7 base circuit bias level which in turn is greater than
8 the downstream bias level.

1 22. The amplifier of claim 1 further comprising
2 means for boosting frequency response at high frequency
3 levels coupled to each stage.

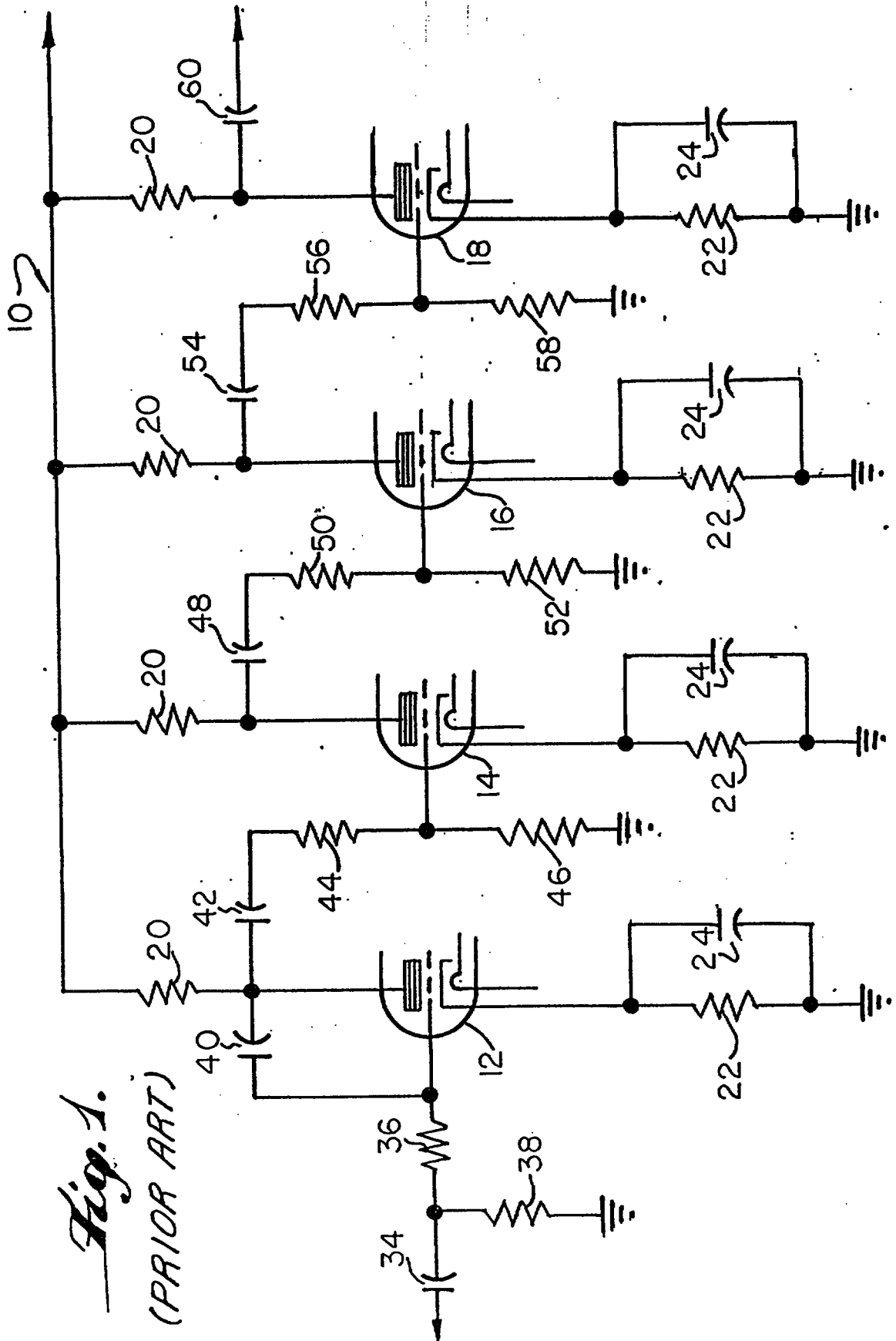
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ABSTRACT**MULTI-STAGE SOLID STATE AMPLIFIER THAT EMULATES TUBE DISTORTION**

A multi-stage solid state amplifier emulates the distortion associated with grid current flow in a multi-stage tube amplifier by means of a clipping device in the circuit between each series connected stage. In a particular embodiment, each stage includes a field effect transistor (FET) and the clipping device is a diode. In another embodiment, each stage includes a Darlington connected pair of transistors. An input diode and a multilevel biasing circuit emulates a tube circuit input.

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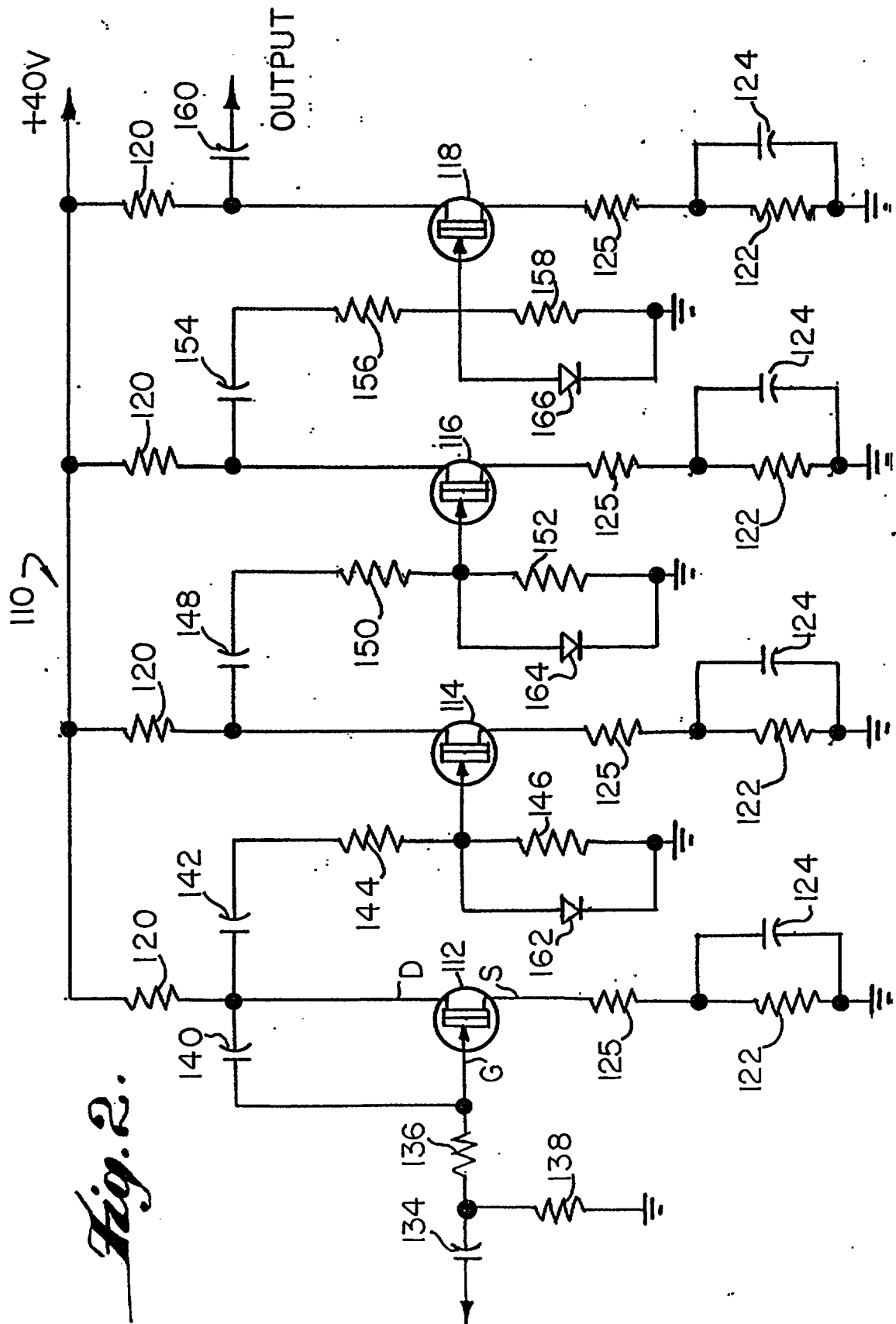
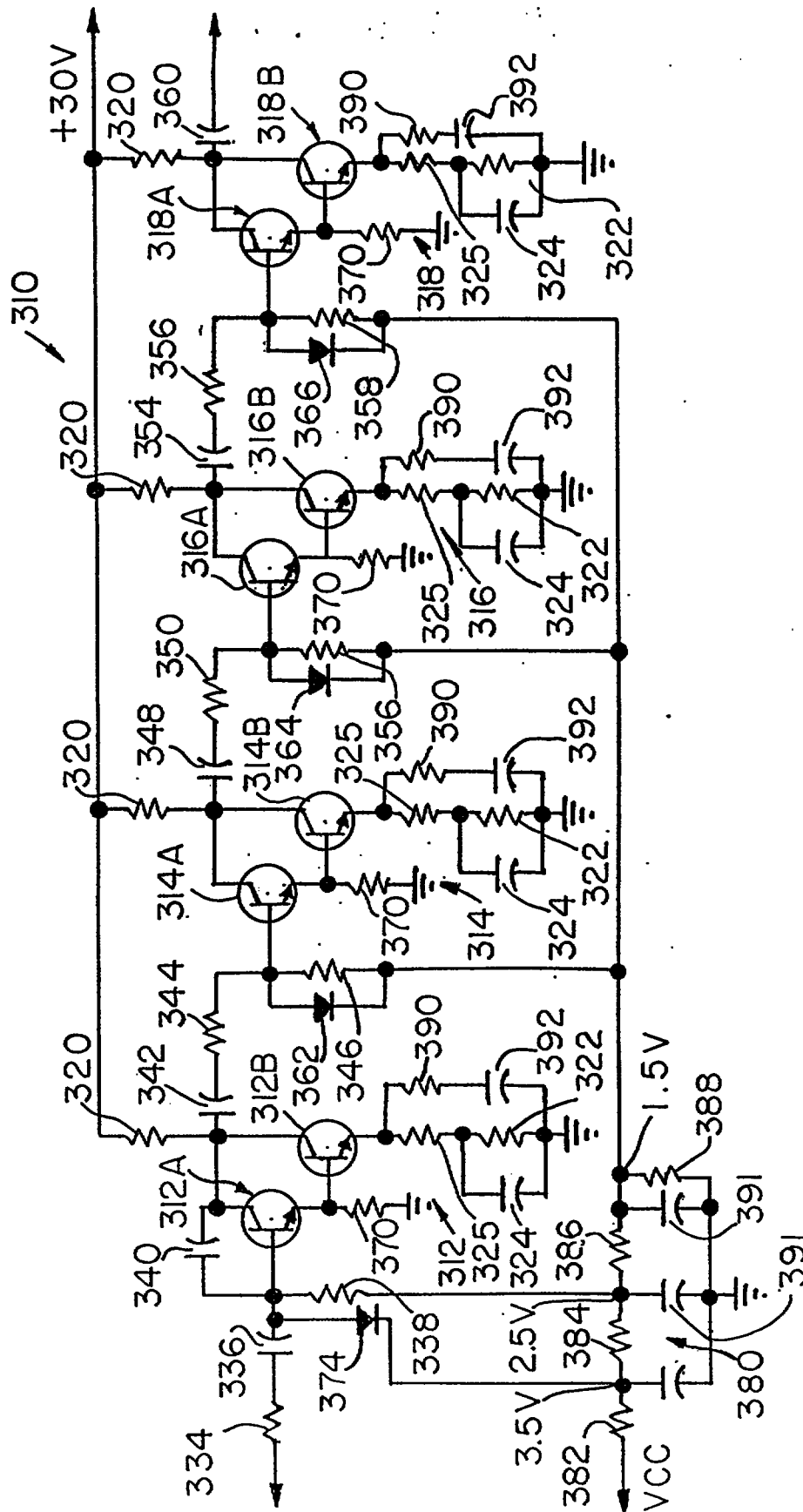




Fig. 3.

Fig. 7.

CLAIMS

1. A solid state amplifier for emulating the distortion associated with a flow of current in the grid of an overdriven multi-stage tube amplifier at high input signal levels resulting in an input clipping characteristic, the amplifier comprising:

a plurality of series connected solid state amplifier stages (112-118) for amplifying a signal, each including an input circuit and an output circuit having an output signal capability, each amplifier stage (112-116) located downstream of a first one (112) of said amplifier stages having its input circuit coupled to the output circuit of one of said amplifier stages located immediately upstream thereof; and

clipping means (162-166) connected in the input circuit of each amplifier stage downstream of the first one of said amplifier stages (112) for establishing a clipping level between such amplifier stages, duplicating in such solid state amplifier the input clipping characteristic of a tube amplifier.

2. The amplifier of claim 1, wherein each amplifier stage comprises a field effect transistor (112-118).

3. The amplifier of claim 1, wherein each amplifier stage comprises a transistor (312A-318B).

4. The amplifier of claim 1, wherein each amplifier stage comprises a Darlington transistor (312A-318B).

5. The amplifier of claim 4, wherein the Darlington transistor is integrated.



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6. The amplifier of claim 4, wherein the Darlington transistor comprises discrete elements.

7. The amplifier of claim 6, wherein the Darlington transistor includes a base-emitter circuit and a bias resistor (370) coupled between the base-emitter circuit and ground.

8. The amplifier of claim 1, wherein the clipping means comprises a diode (162-166).

9. The amplifier of claim 8, wherein the output signal capability is effective to forward bias the diode (162-166).

10. The amplifier of claim 8, wherein the diode (162-166) produces forward voltage clipping at about 0.5V.

11. The amplifier of claim 1, further including variable input means (213) for at least one of said input circuits (214) for varying the distortion.

12. The amplifier of claim 11, wherein variable input means comprises a potentiometer (213) coupled in output circuit of a first one (212) of said amplifier stages, said potentiometer having a wiper coupled to the input of a second one (214) of said amplifier stages and the clipping means (262).

13. The amplifier of claim 1, further comprising variable output means (261) coupled in the output circuit of a last one (218) of said amplifier stages for varying an overall output level of the amplifier.



14. The amplifier of claim 13, wherein the variable output means comprises a potentiometer (261).

15. The amplifier of claim 1, further comprising biasing means for the solid state devices.

16. The amplifier of claim 15, further comprising multilevel biasing means (380,390) including an upstream biasing means (380) for a first upstream one (312) of the amplifier stages and a downstream biasing means (390) for the downstream amplifier stages (312-316).

17. The amplifier of claim 16, wherein the upstream biasing means comprises a base circuit biasing means (370) coupled to a base element of the upstream solid state amplifier stage.

18. The amplifier of claim 1, further comprising input diode means (374) coupled between an input of the first upstream amplifier stage (312) and a reference.

19. The amplifier of claim 18, further comprising upstream biasing means (382) for the diode means (374).

20. The amplifier of claim 1, further comprising a diode circuit biasing means for establishing a diode circuit bias level, a base circuit biasing means for establishing a base circuit bias level and a downstream biasing means for establishing a downstream bias level and wherein the diode biasing level is greater than the base circuit bias level which in turn is greater than the downstream bias level.

21. The amplifier of claim 1, further comprising means (390,392) for boosting frequency response at high frequency levels coupled to each stage.



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22. The solid state amplifier of any of claims 1 to 21, wherein the output signal capability and the input clipping level between the amplifier stages have a ratio sufficient to result in second harmonic distortion.

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23. The amplifier of claim 22, wherein the ratio is at least 30.

24. The solid state amplifier of claim 1, wherein each
10 amplifier stage comprises a single ended solid state amplifier.

25. The solid state amplifier of claim 1, further
comprising interstage coupling means
15 (342,344,348,350,354,356) for establishing a selected relationship between the output capability of each amplifier stage and the clipping level.

AMENDED SHEET

ABSTRACT**MULTI-STAGE SOLID STATE AMPLIFIER THAT EMULATES TUBE DISTORTION**

A multi-stage solid state amplifier emulates the distortion associated with grid current flow in a multi-stage tube amplifier by means of a clipping device in the circuit between each series connected stage. In a particular embodiment, each stage includes a field effect transistor (FET) and the clipping device is a diode. In another embodiment, each stage includes a Darlington connected pair of transistors. An input diode and a multilevel biasing circuit emulates a tube circuit input.

(Fig. 1)