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(71) Applicant: RCA THOMSON LICENSING CORPORATION [US/US]; CN 5312, Two Independence Way, Princeton, NJ 08540 (US).

(72) Inventor: WHITE, Hugh, Edward ; 171 Marshall Corner Woodsville Road, Pennington, NJ 08534 (US).

(74) Agents: TRIPOLI, Joseph, S. et al.; GE & RCA Licensing Management Operation Inc., CN 5312, Princeton, NJ 08540 (US).

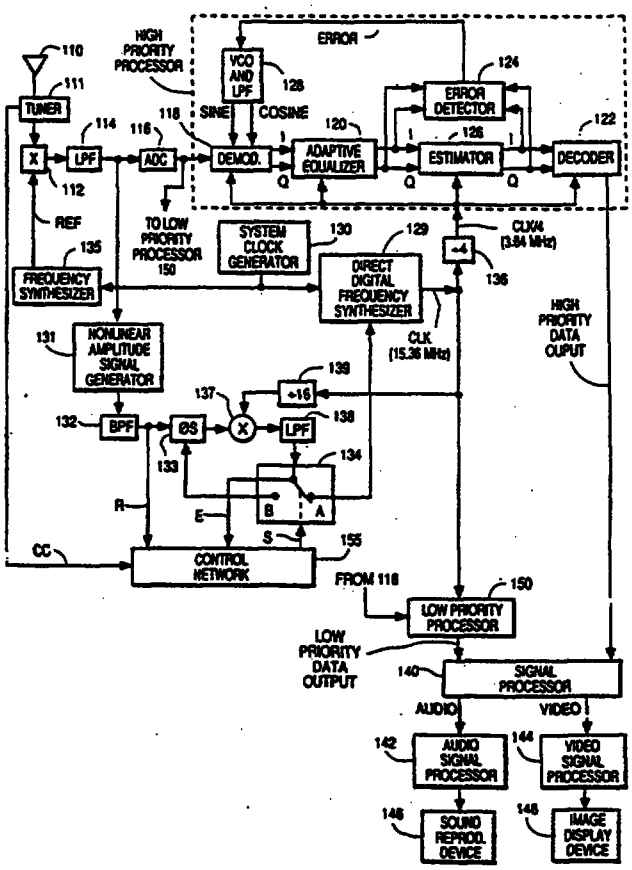
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(54) Title: CLOCK SIGNAL GENERATOR FOR A DIGITAL TELEVISION RECEIVER

(57) Abstract

A high definition television receiver including digital signal processing circuits (118-128, 150) receives a high definition television signal containing narrowband high priority information from which a system clock is derived (155, 129), and low priority wideband information. After a channel change, a clock signal developed 137-139) from narrowband information associated with the previously tuned channel is used (134) as the receiver clock until a clock signal with a desired phase characteristic is developed (131, 133) from the narrowband information of the newly tuned channel.



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