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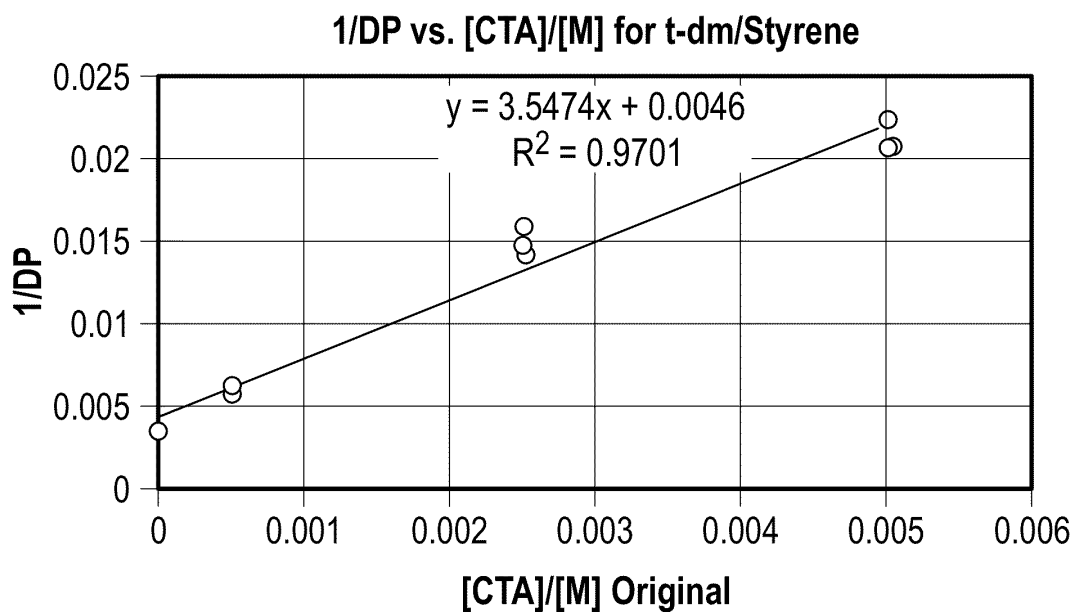


FIG. 1

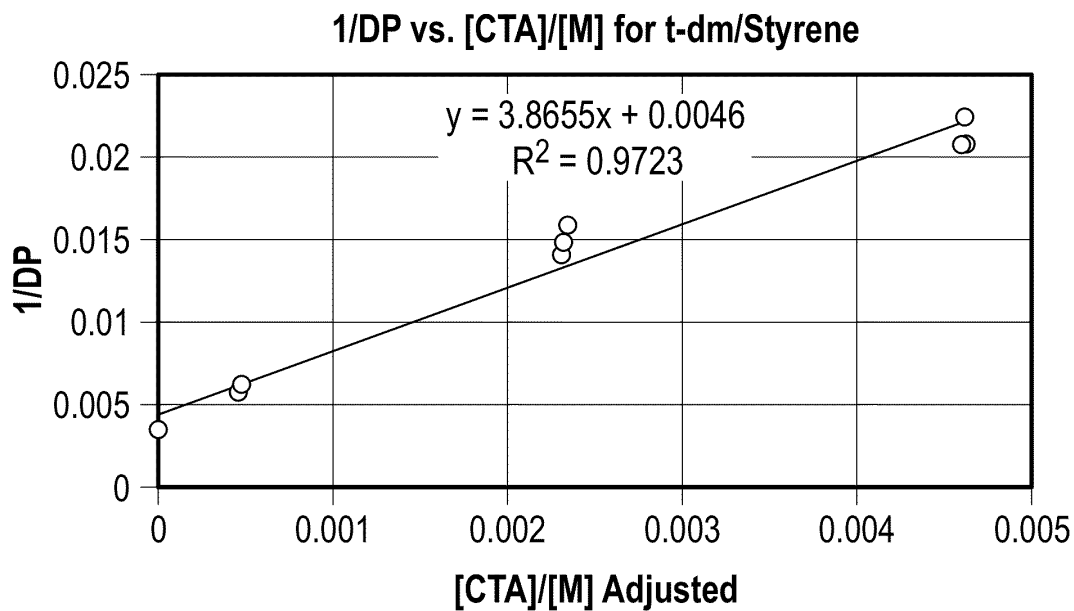


FIG. 2

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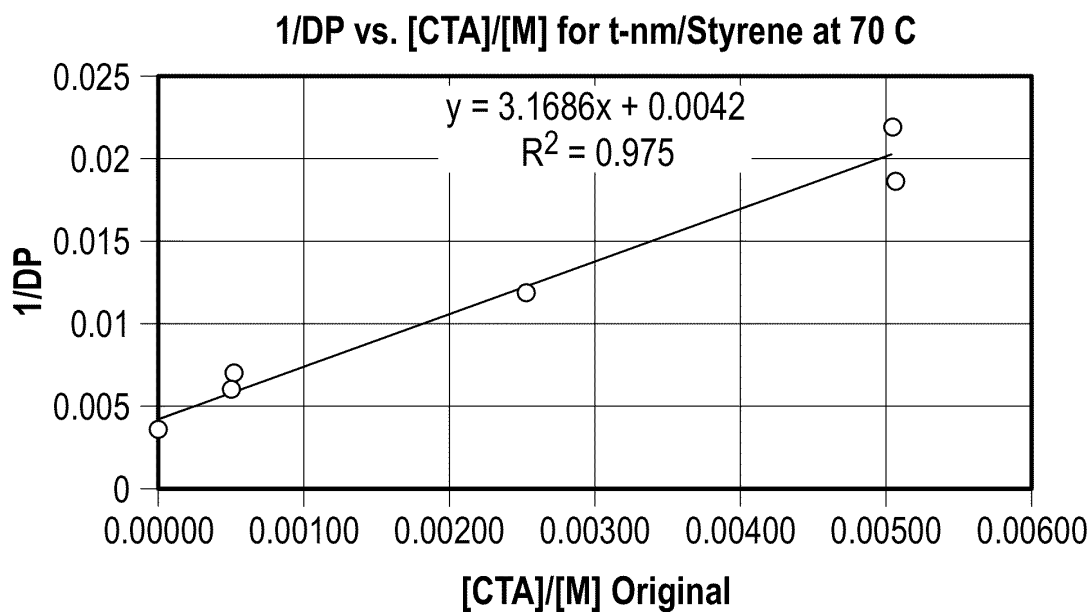


FIG. 3

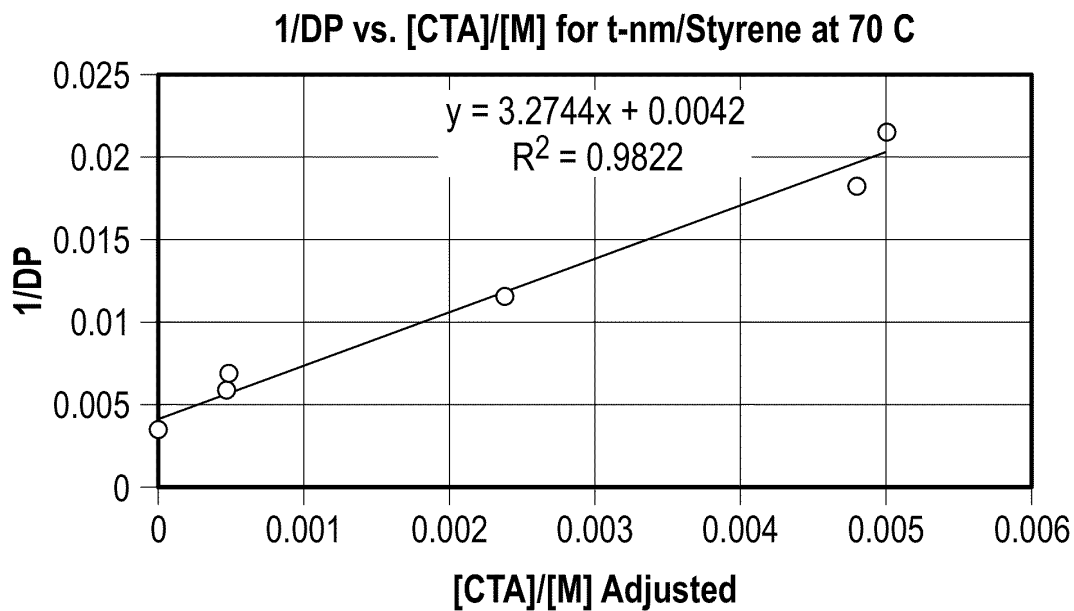


FIG. 4

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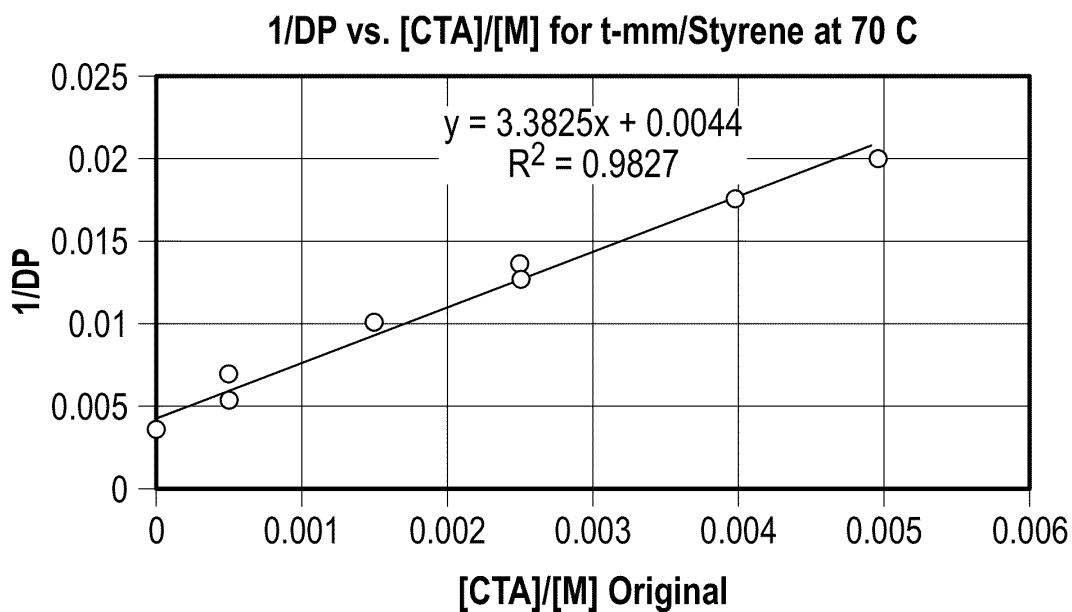


FIG. 5

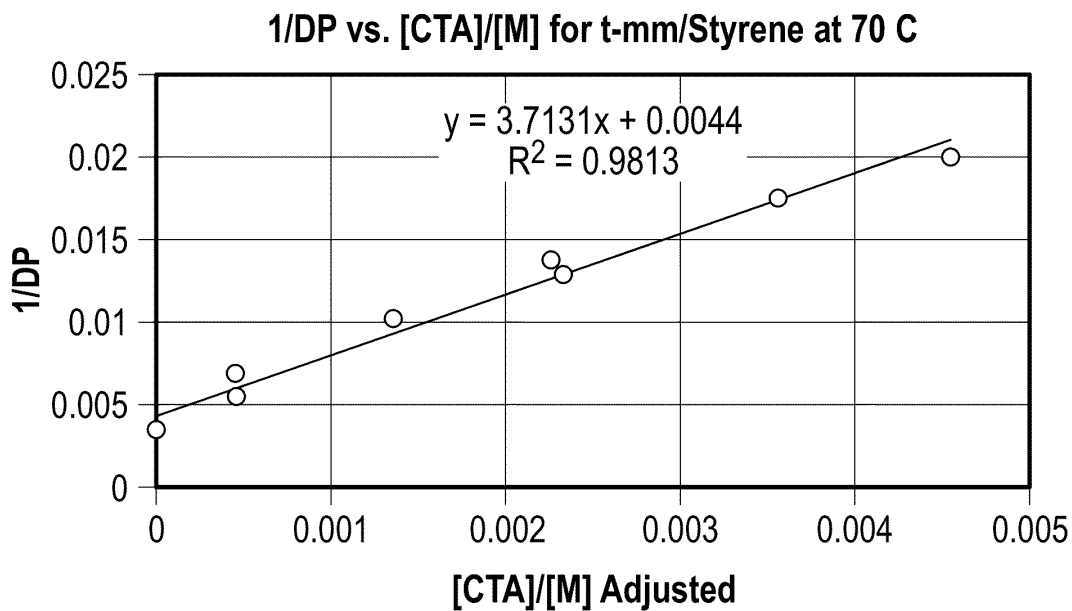


FIG. 6

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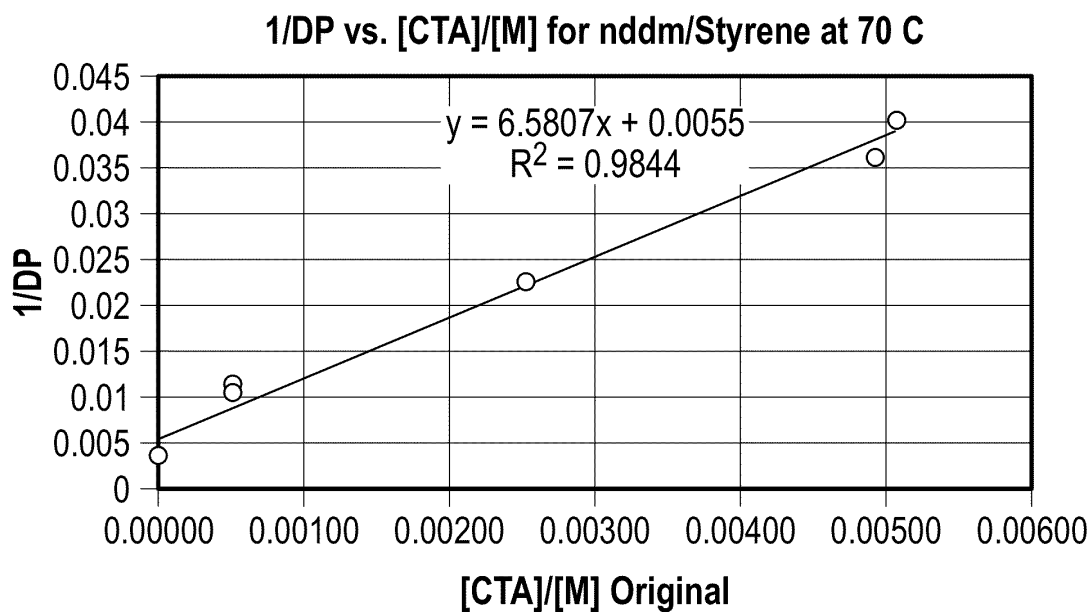


FIG. 7

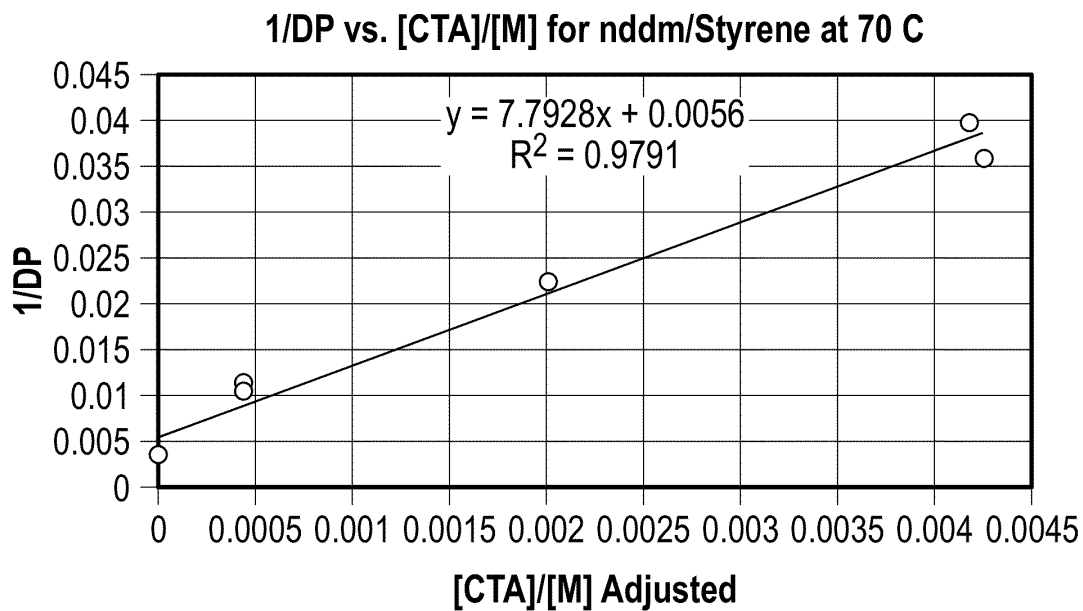


FIG. 8

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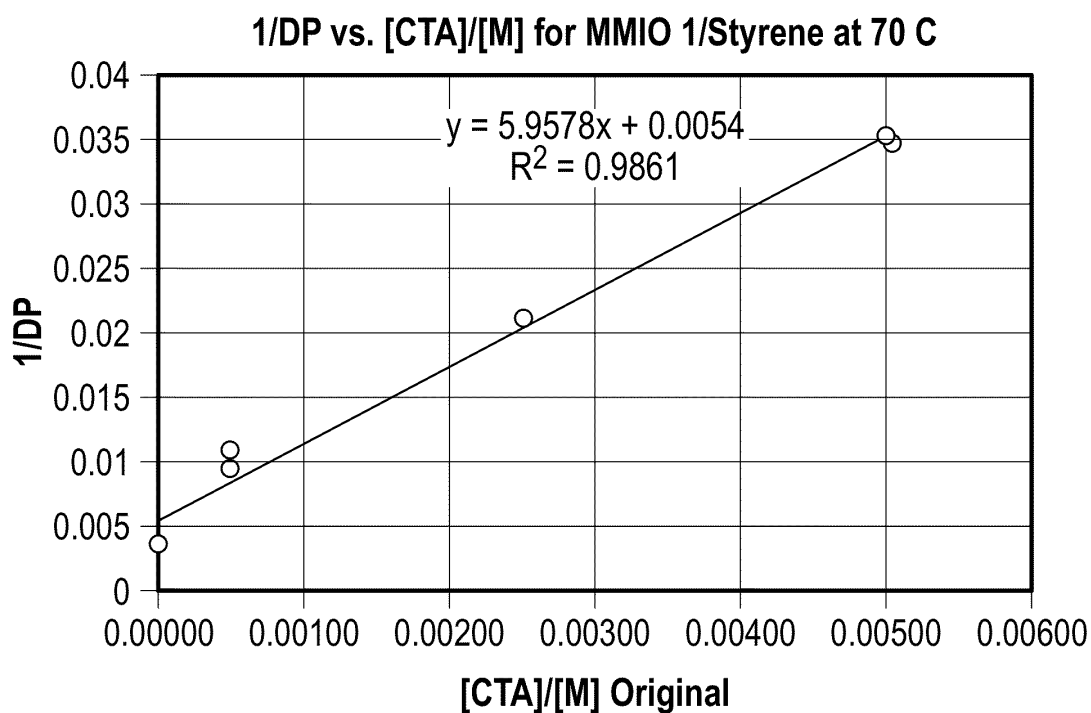


FIG. 9

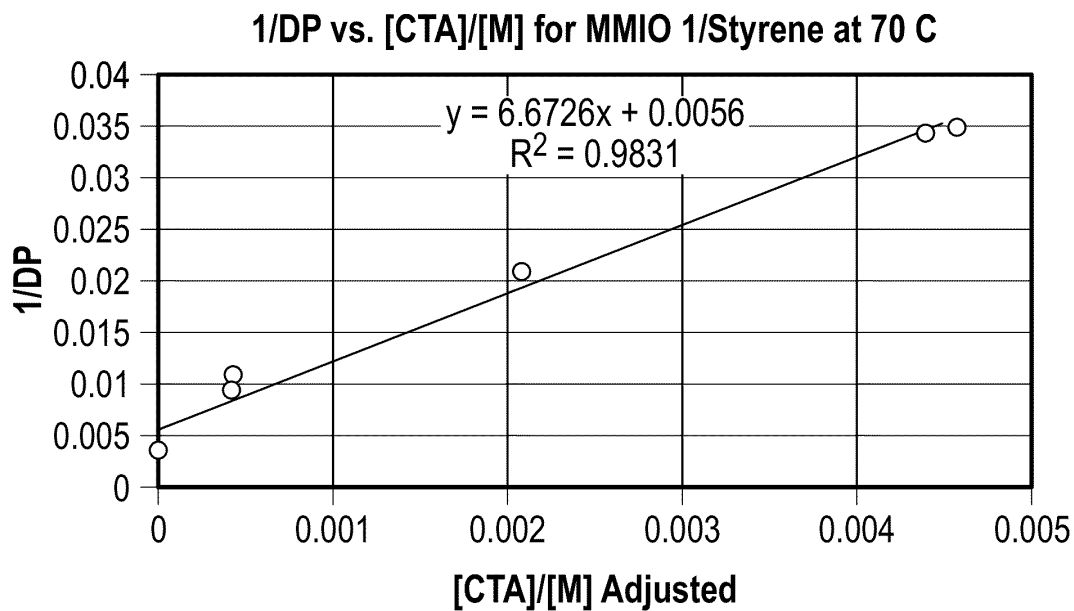


FIG. 10

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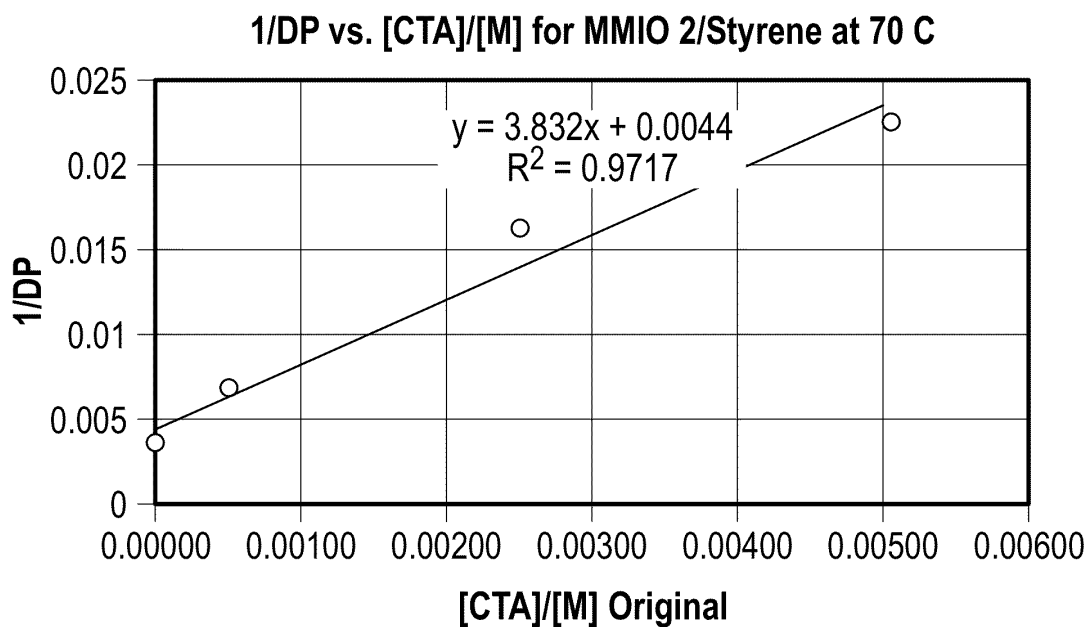


FIG. 11

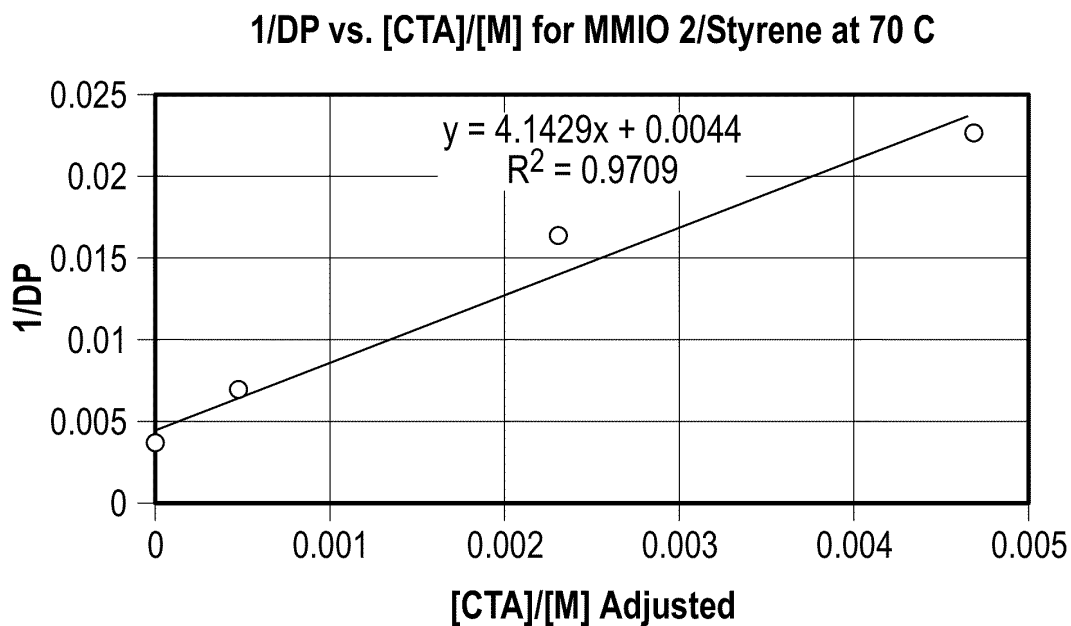


FIG. 12

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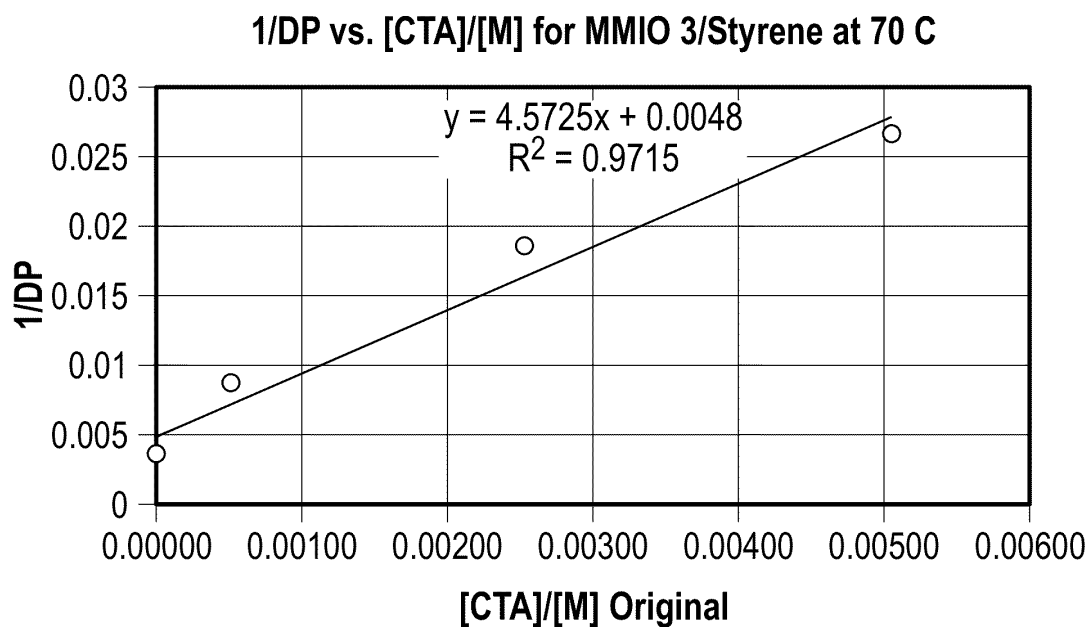


FIG. 13

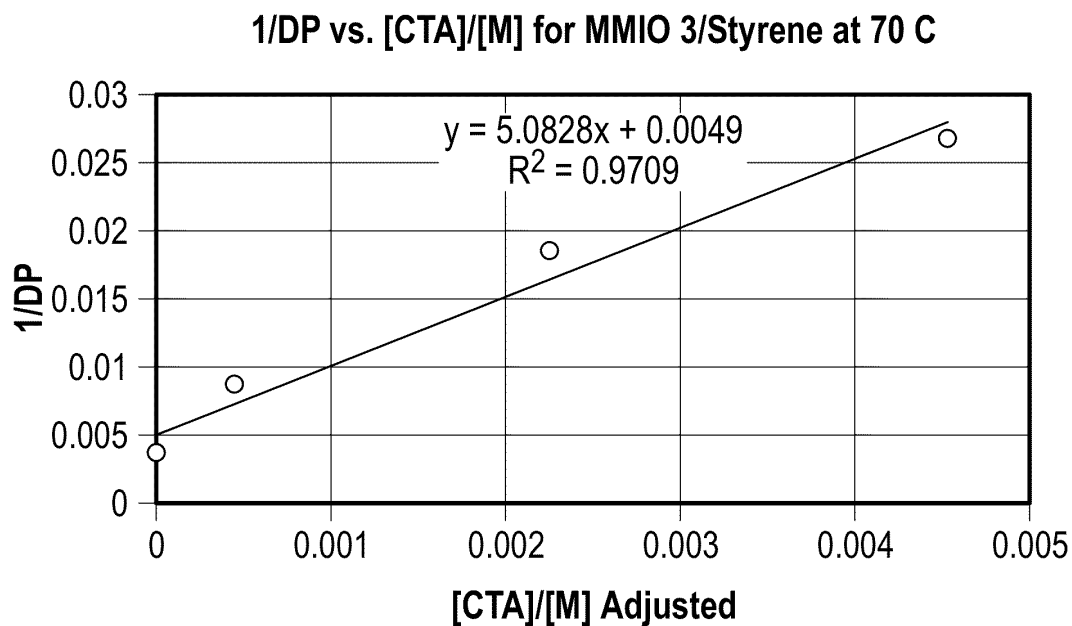


FIG. 14

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1/DP vs. [CTA]/[M] for MMIO 4/Styrene at 70 C

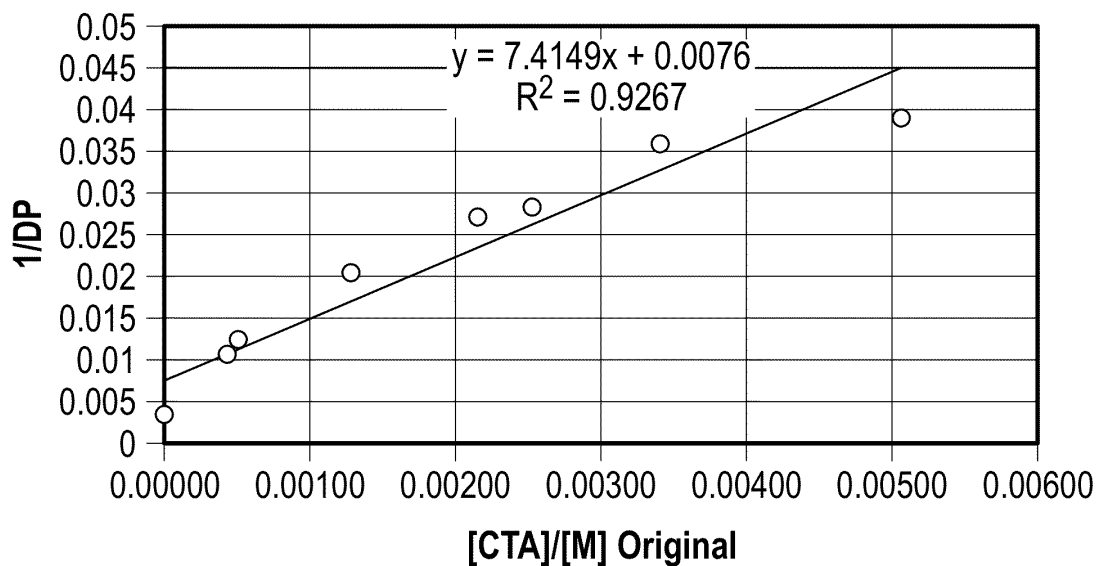


FIG. 15

1/DP vs. [CTA]/[M] for MMIO 4/Styrene at 70 C

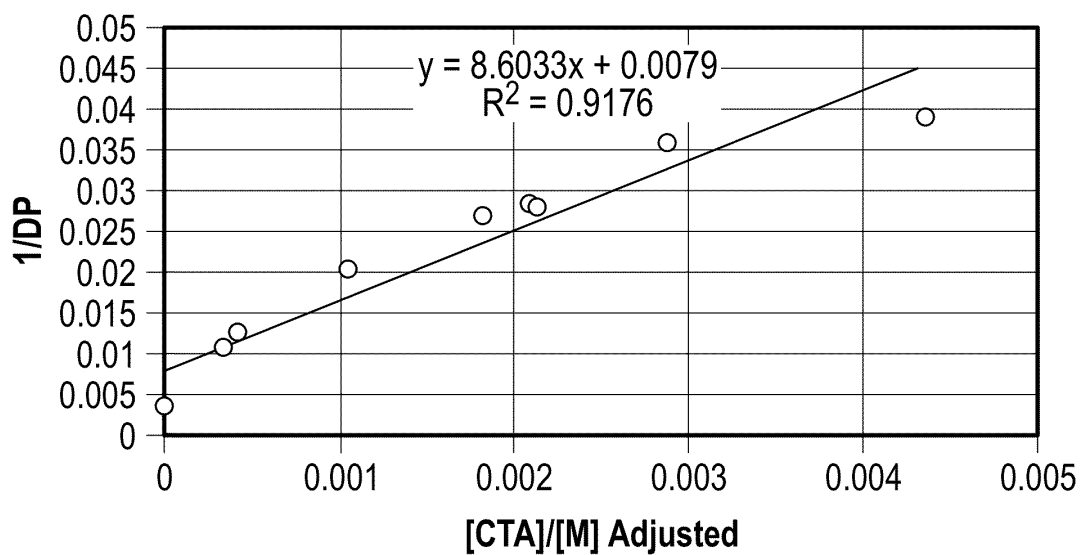


FIG. 16

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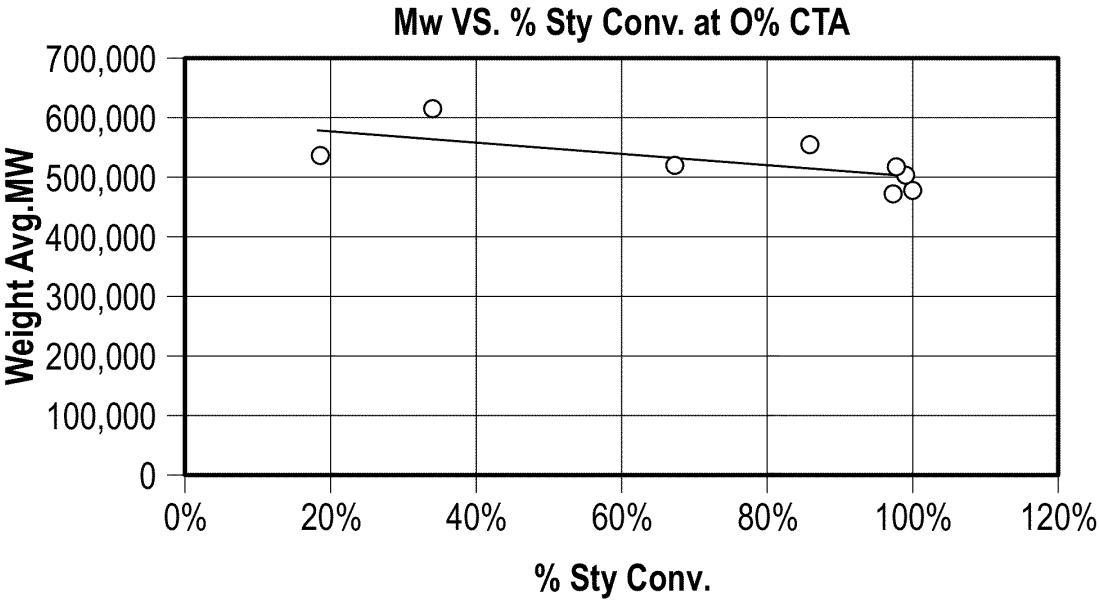


FIG. 17

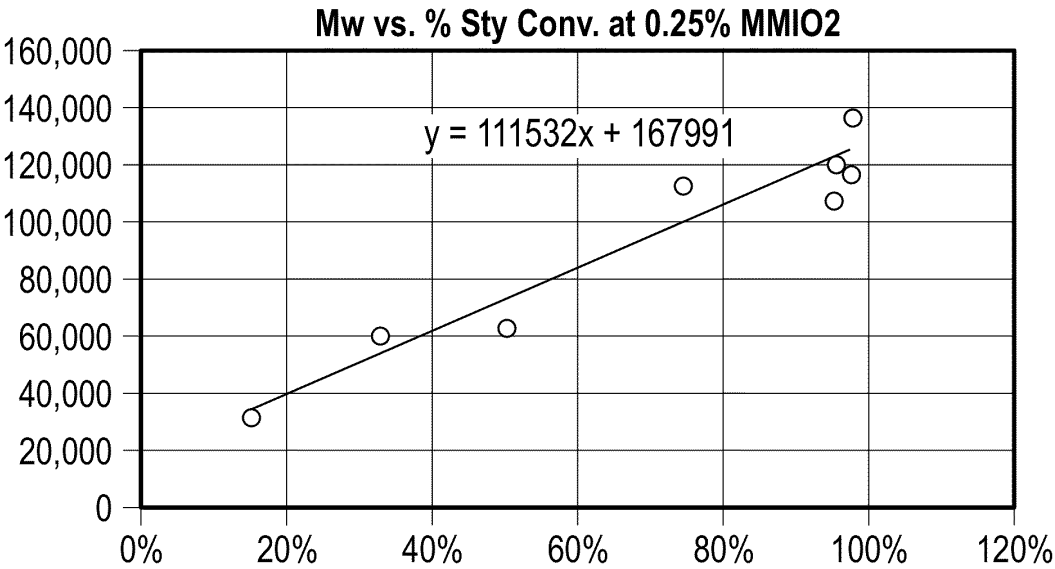


FIG. 18

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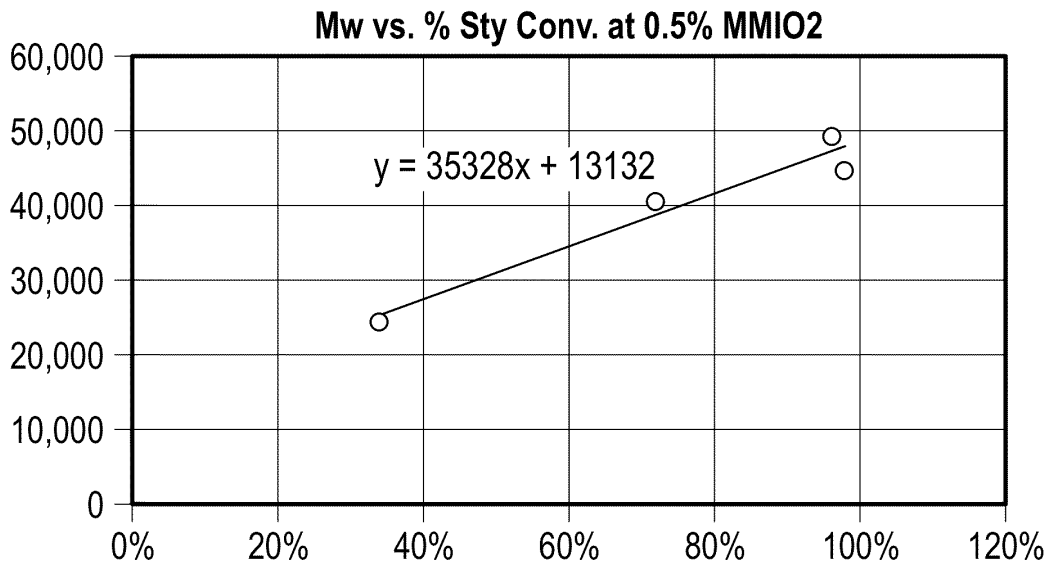


FIG. 19

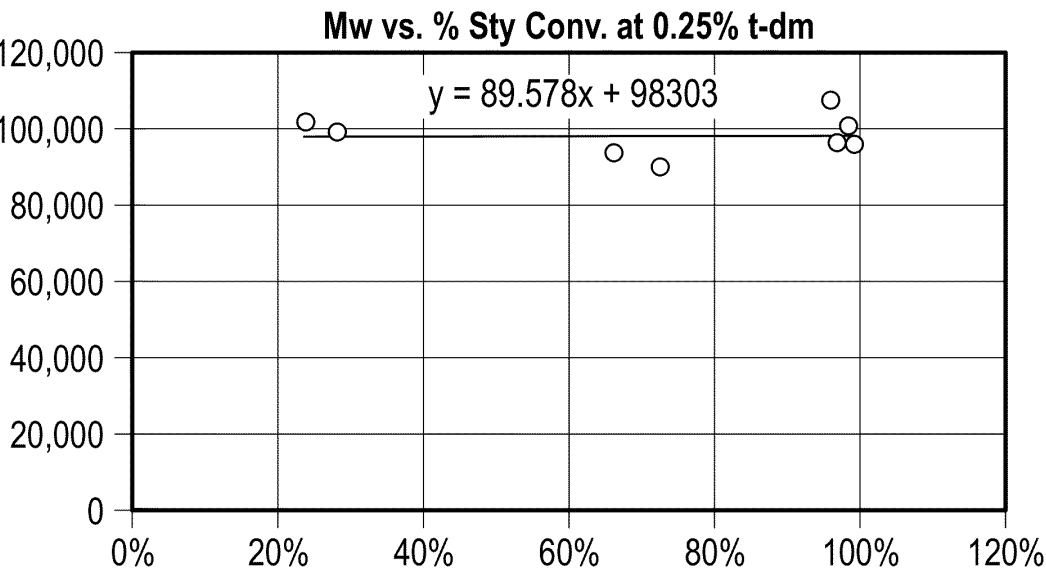


FIG. 20

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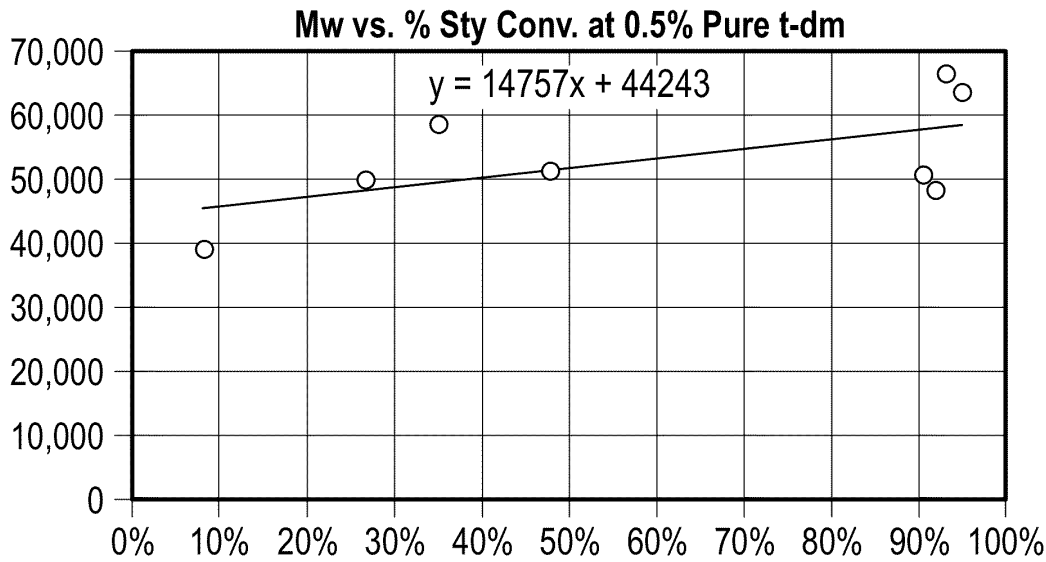


FIG. 21

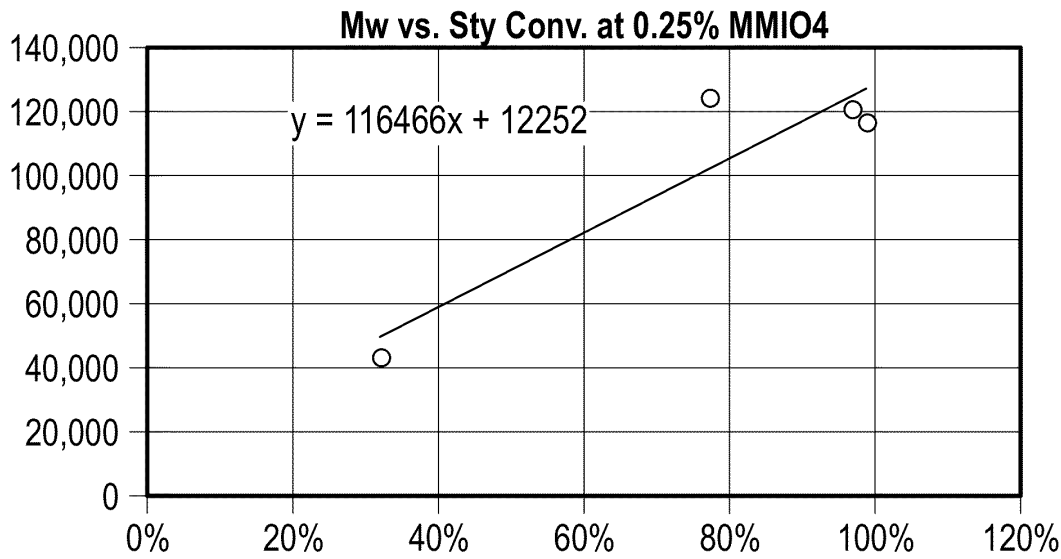


FIG. 22

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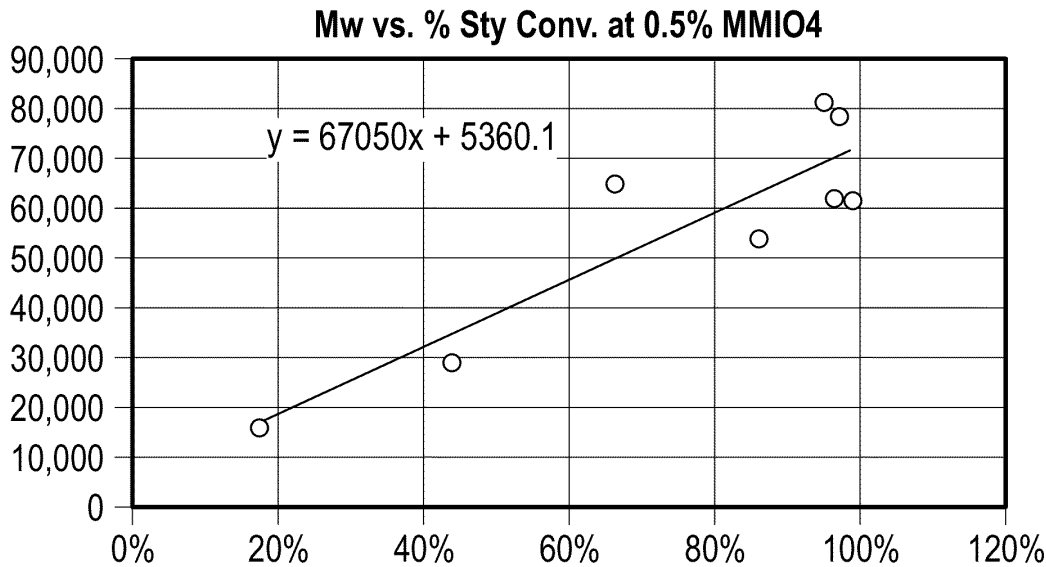


FIG. 23

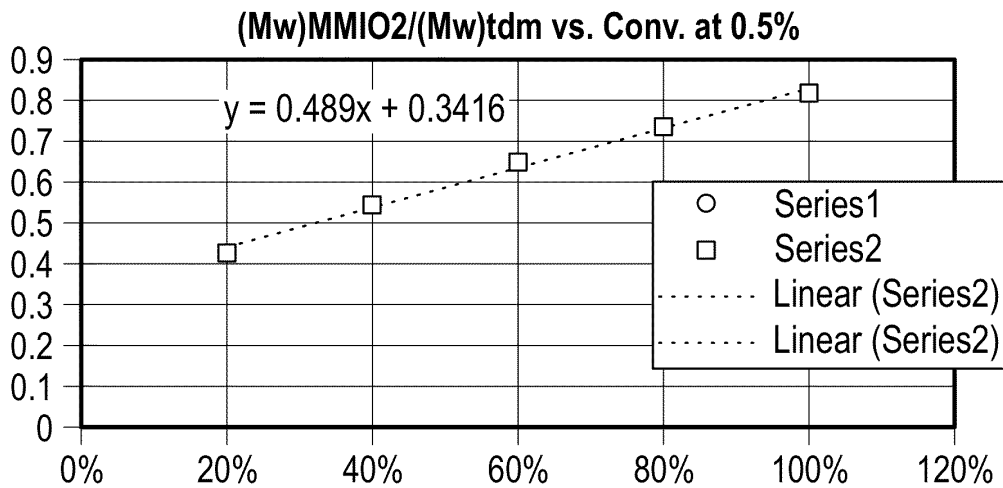


FIG. 24

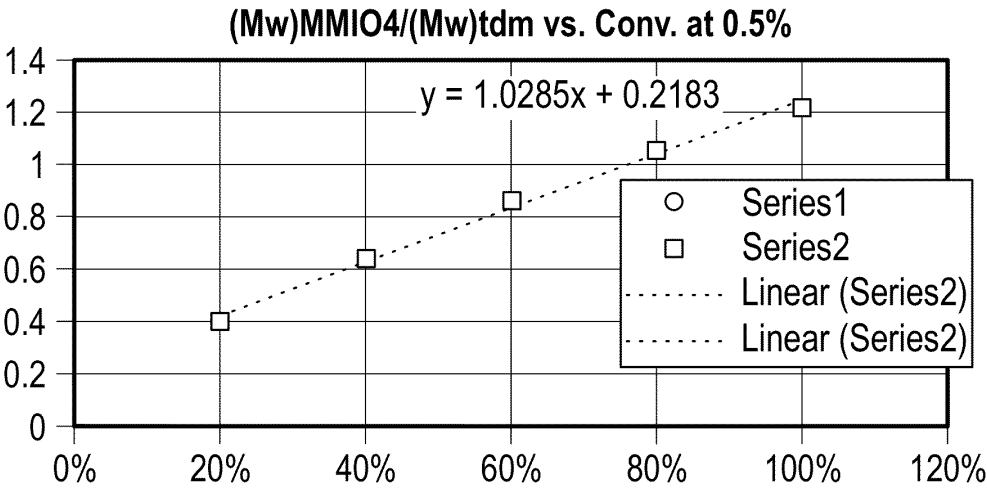


FIG. 25

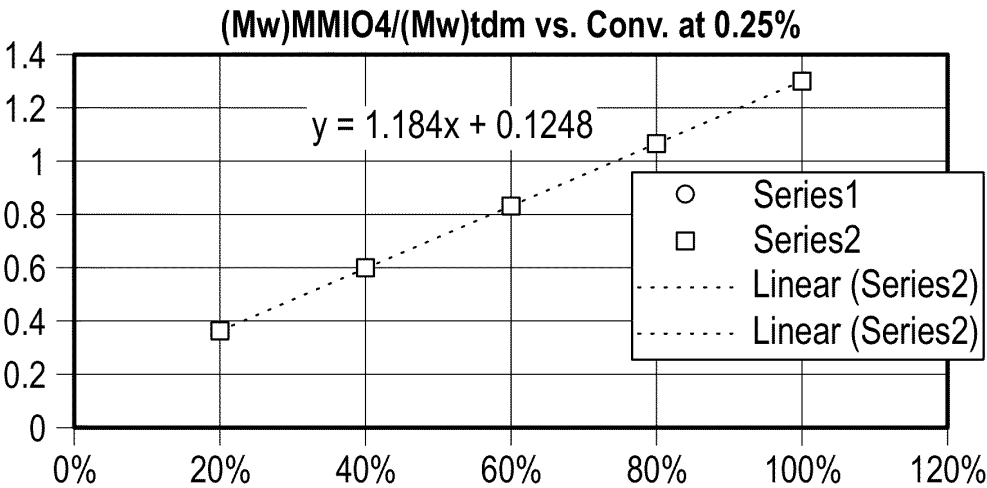


FIG. 26

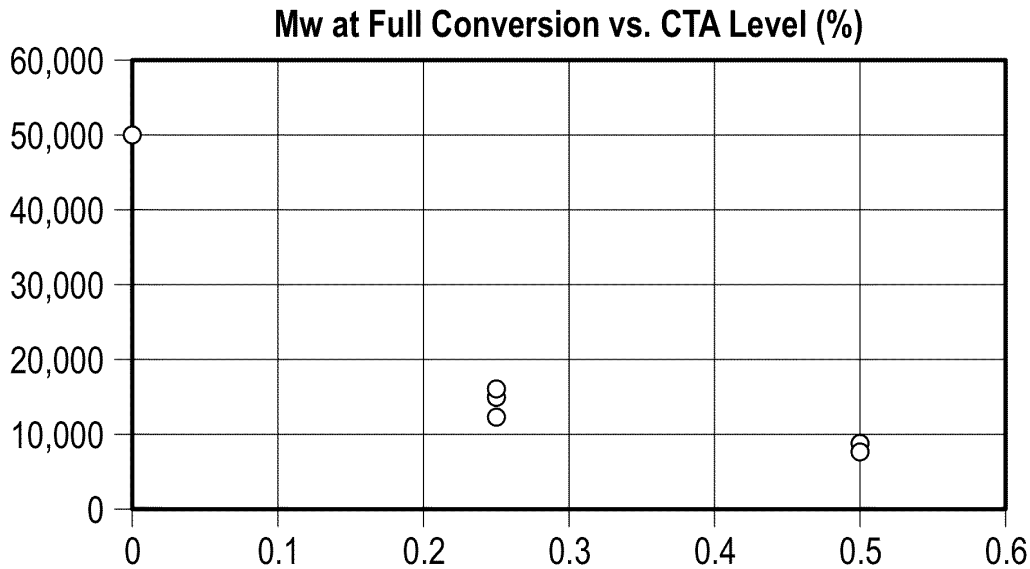


FIG. 27

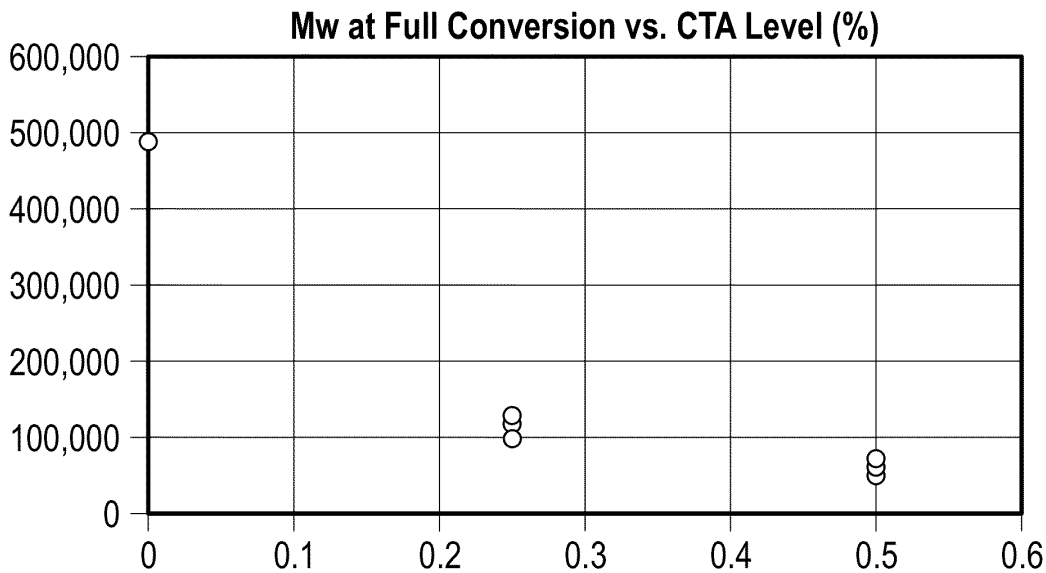


FIG. 28

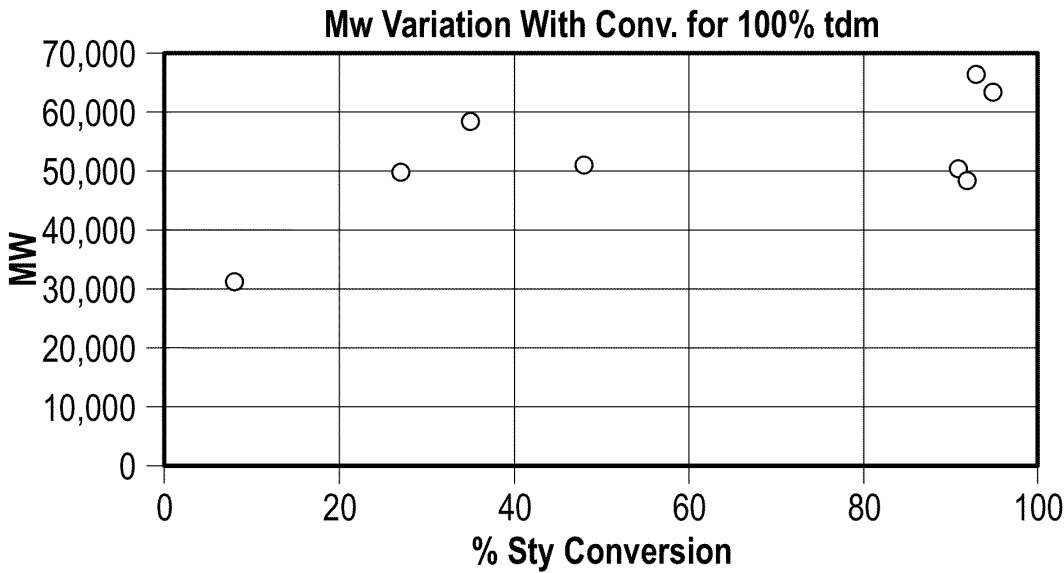


FIG. 29

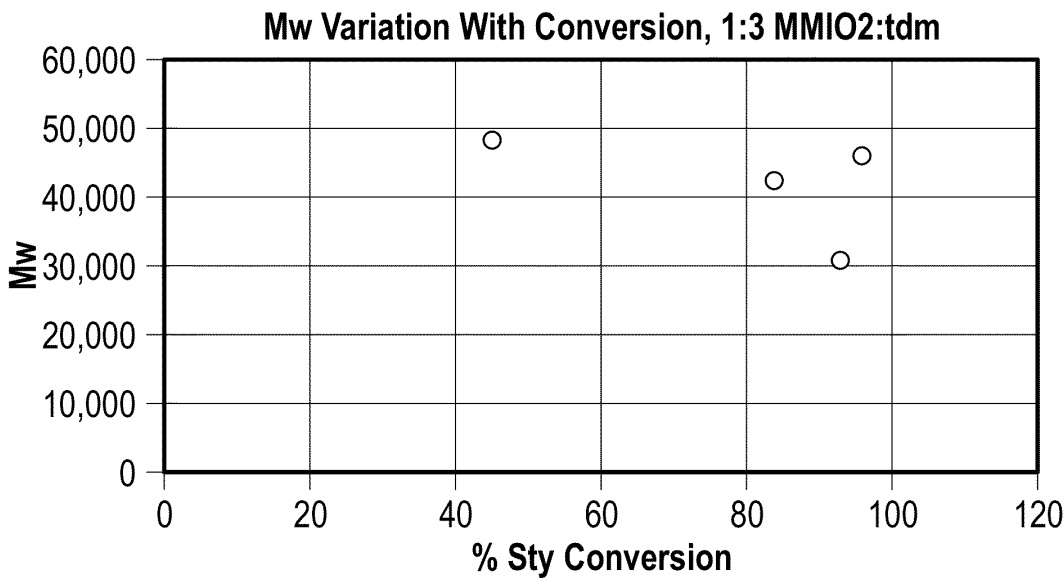


FIG. 30

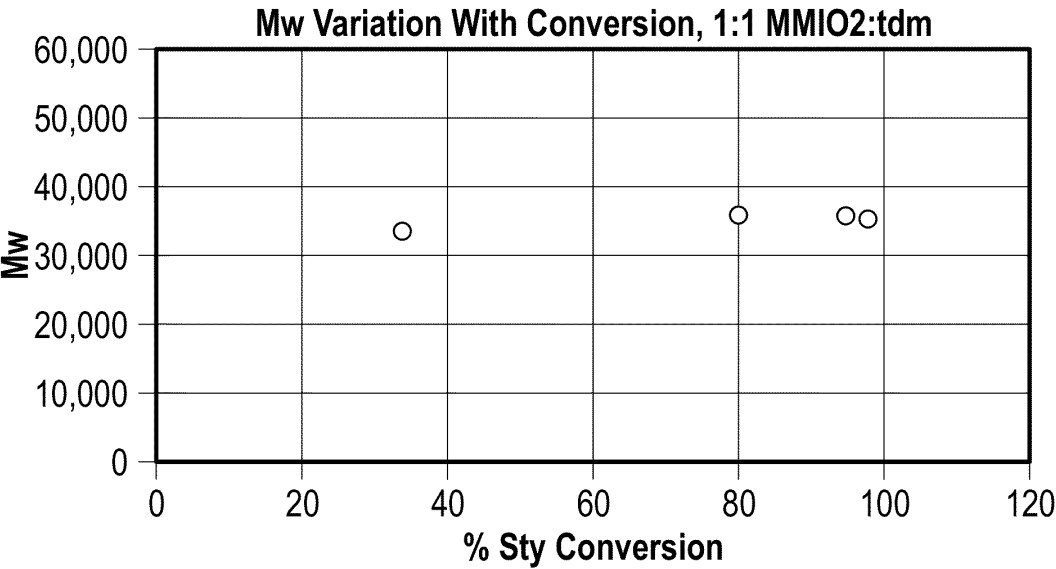


FIG. 31

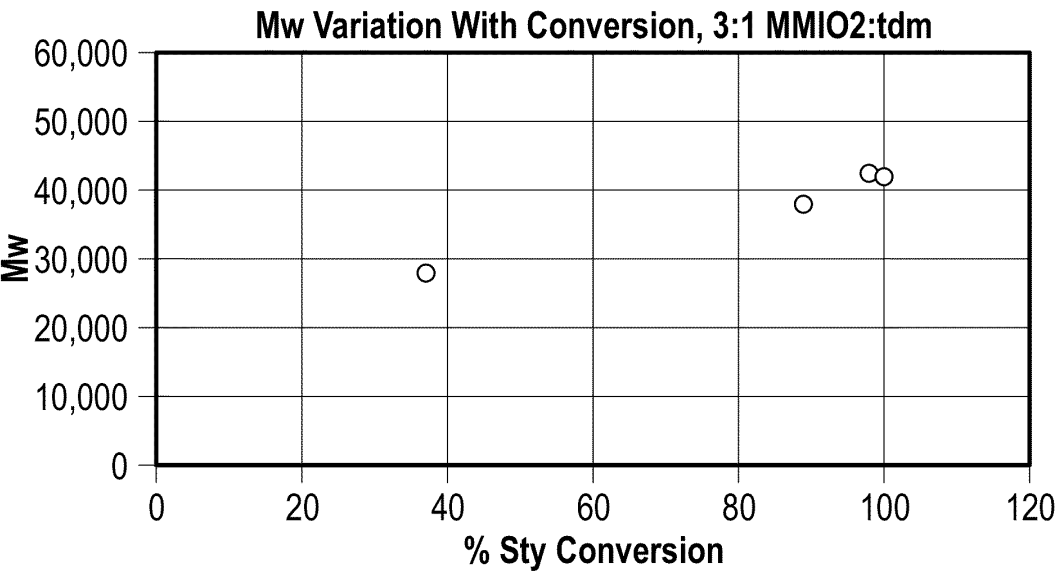


FIG. 32

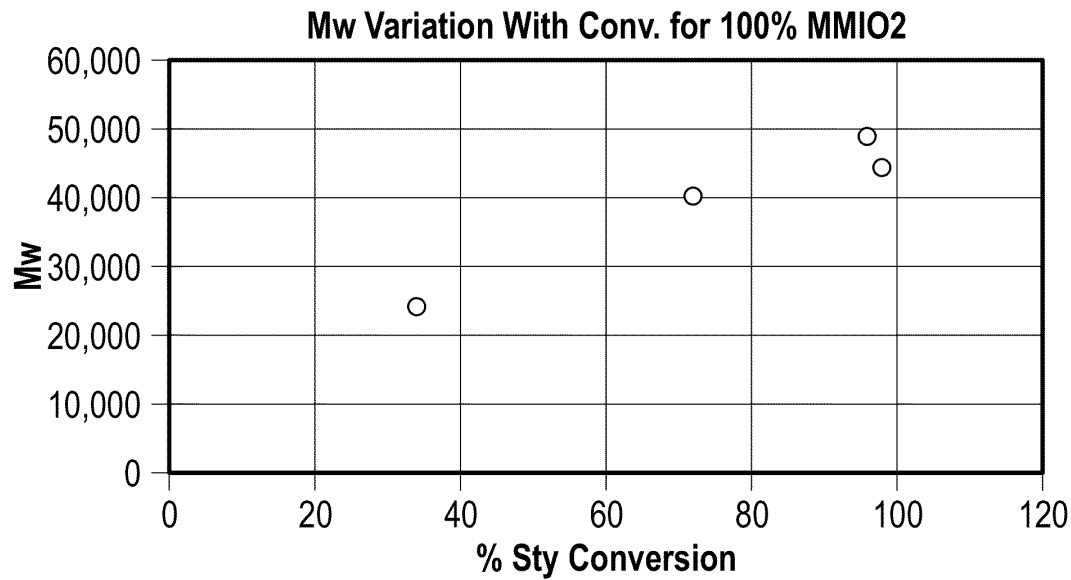


FIG. 33

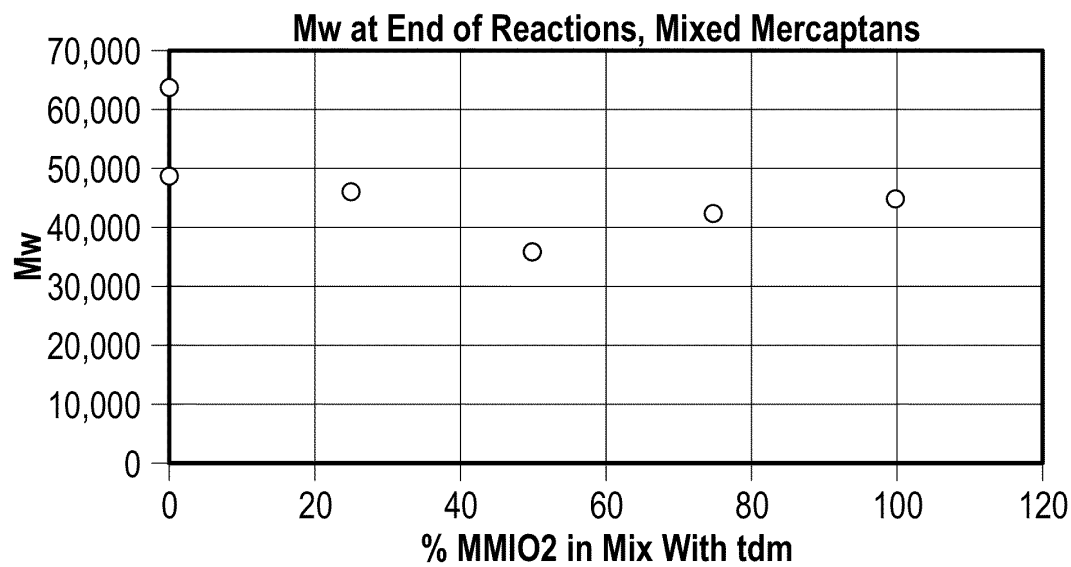


FIG. 34