

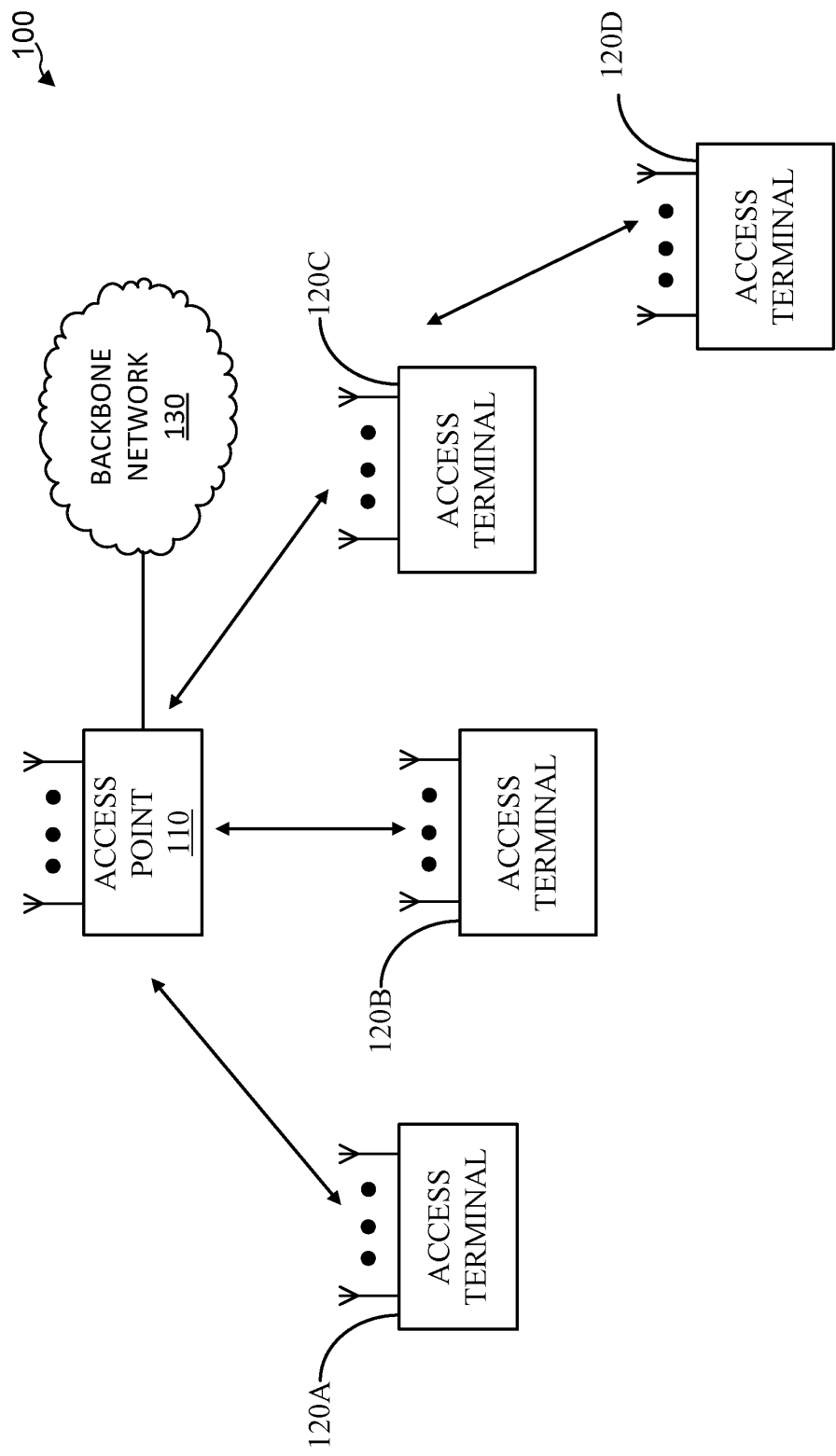


FIG. 1

200

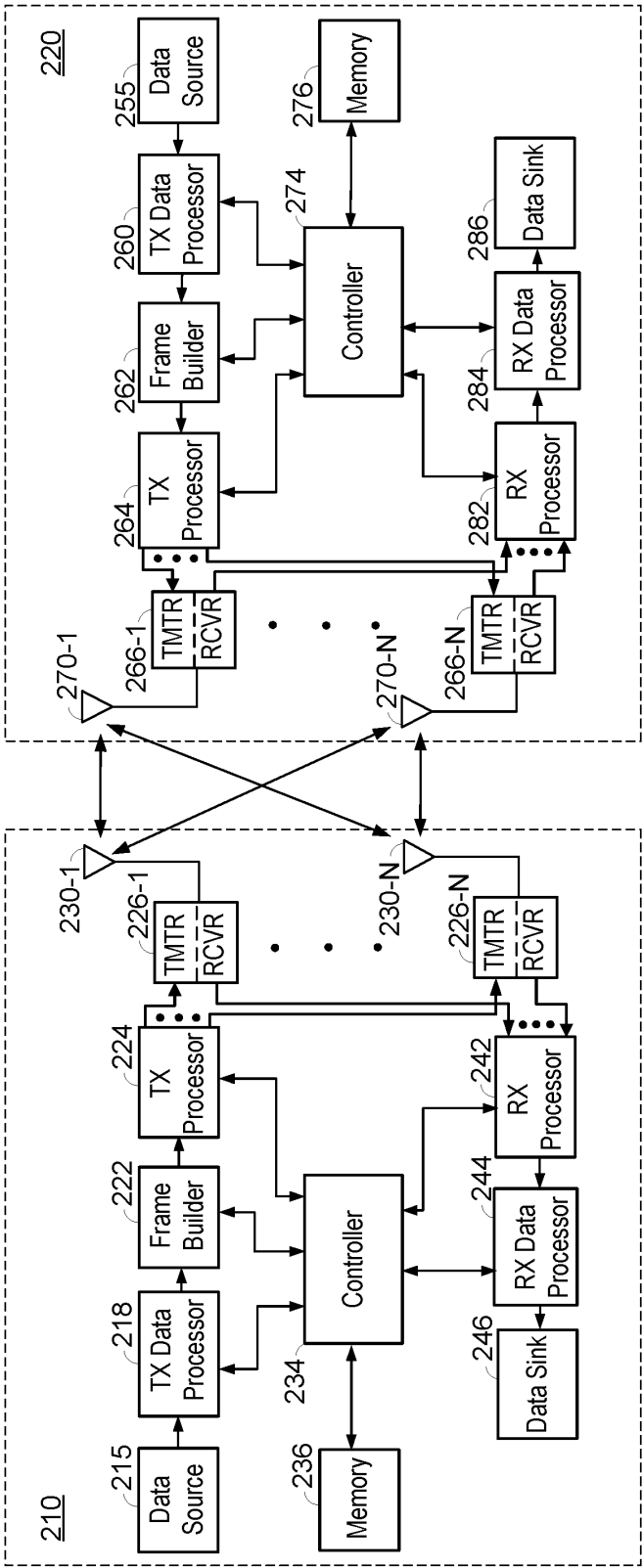


FIG. 2

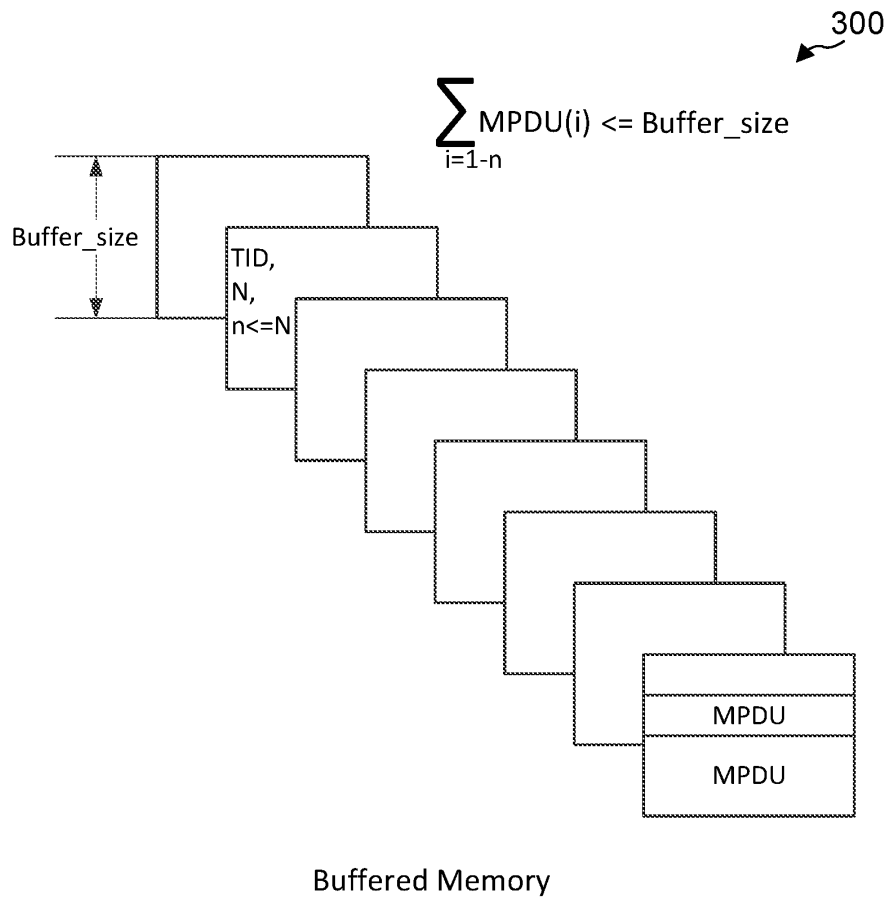


FIG. 3A

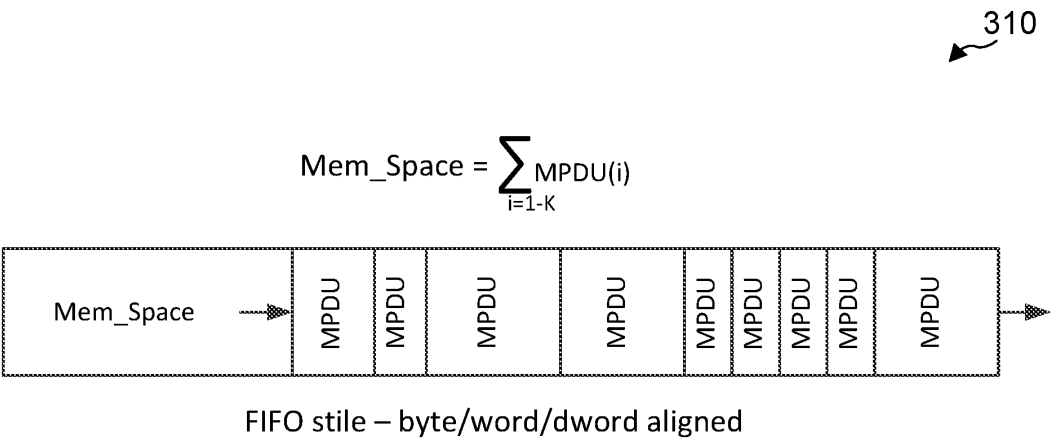


FIG. 3B

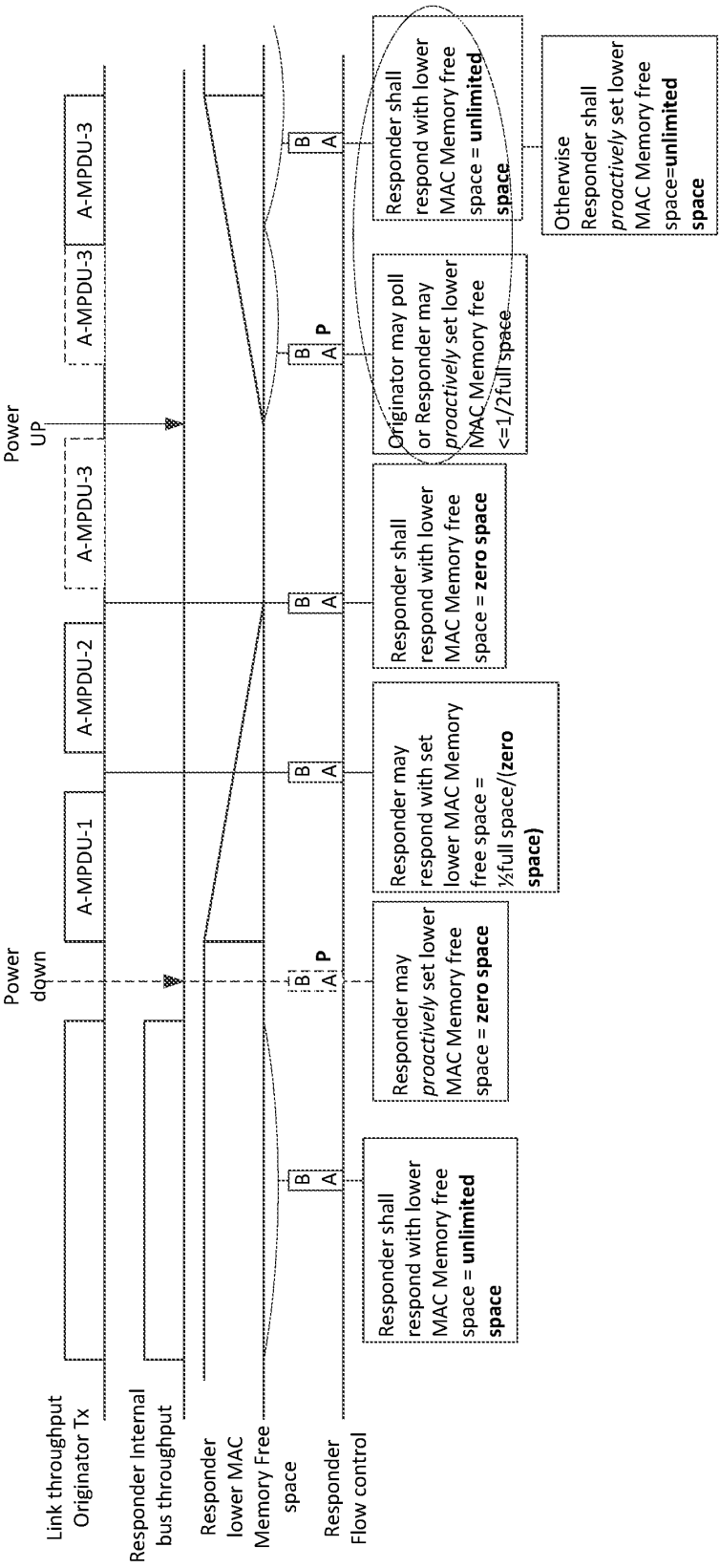


FIG. 6

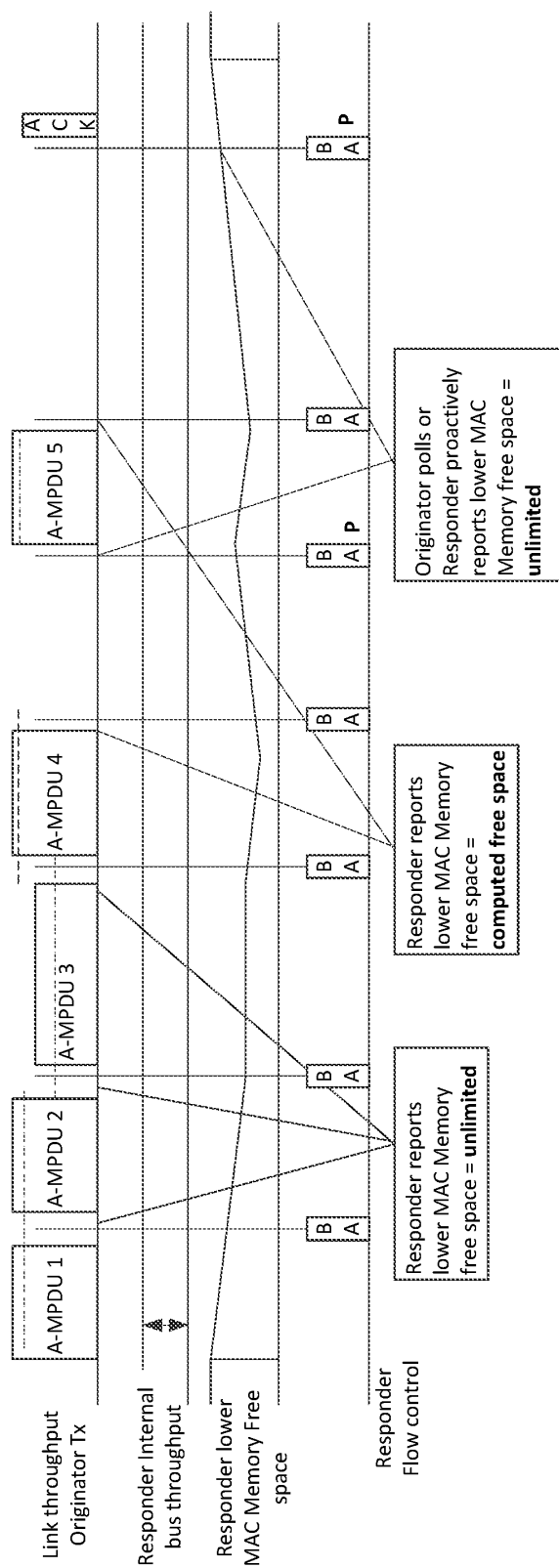


FIG. 7

800


Order	Information
1	Category
2	Block Ack Action
3	Dialog Token
4	Block Ack Parameter Set
5	Block Ack Timeout Value
6	Block Ack Starting Sequence Control
7	GCR Group Address element (optional)
8	Multi-band (optional)
9	TCLAS (optional)
10	ADDBA Extension (optional)
11	EDMG Flow Control Extension Configuration

FIG. 8A

802



Order	Information
1	Category
2	Block Ack Action
3	Dialog Token
4	Status Code
5	Block Ack Parameter Set
6	Block Ack Timeout Value
7	GCR Group Address element (optional)
8	Multi-band (optional)
9	TCLAS (optional)
10	ADDBA Extension (optional)
11	EDMG Flow Control Extension Configuration

FIG. 8B

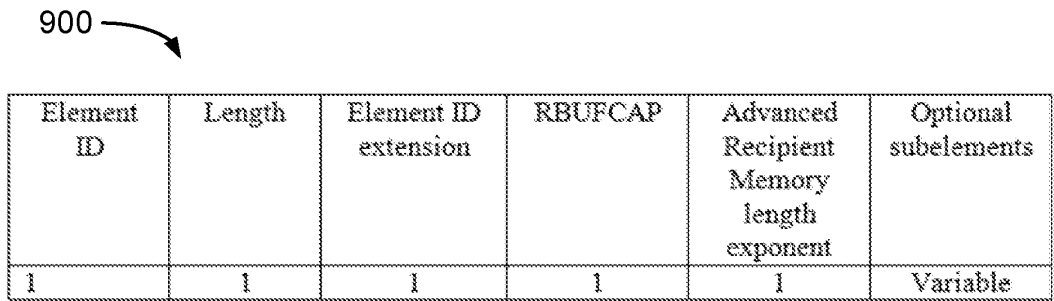


FIG. 9

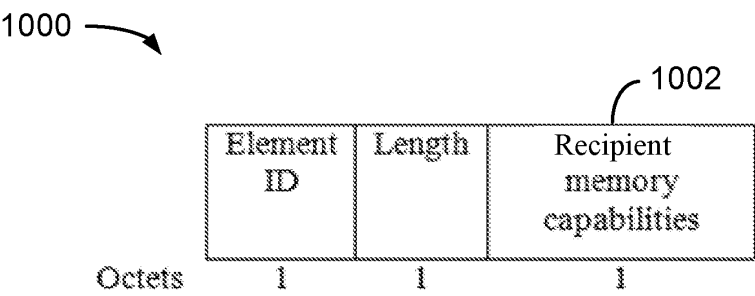


FIG. 10A

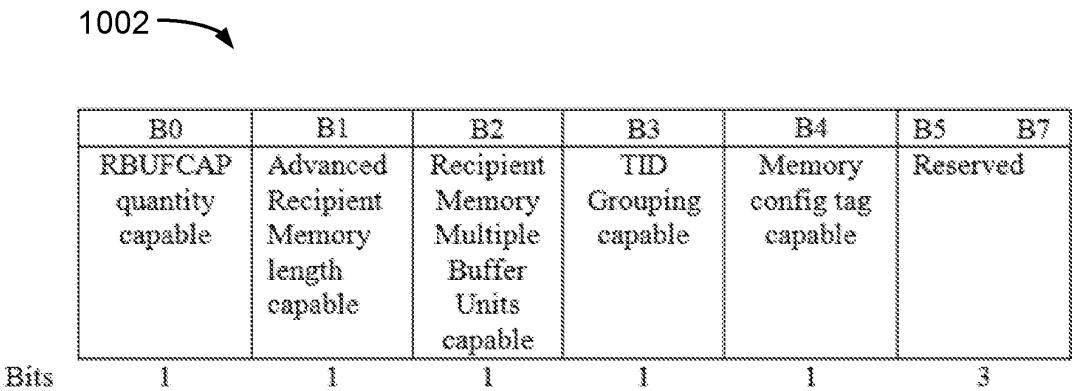
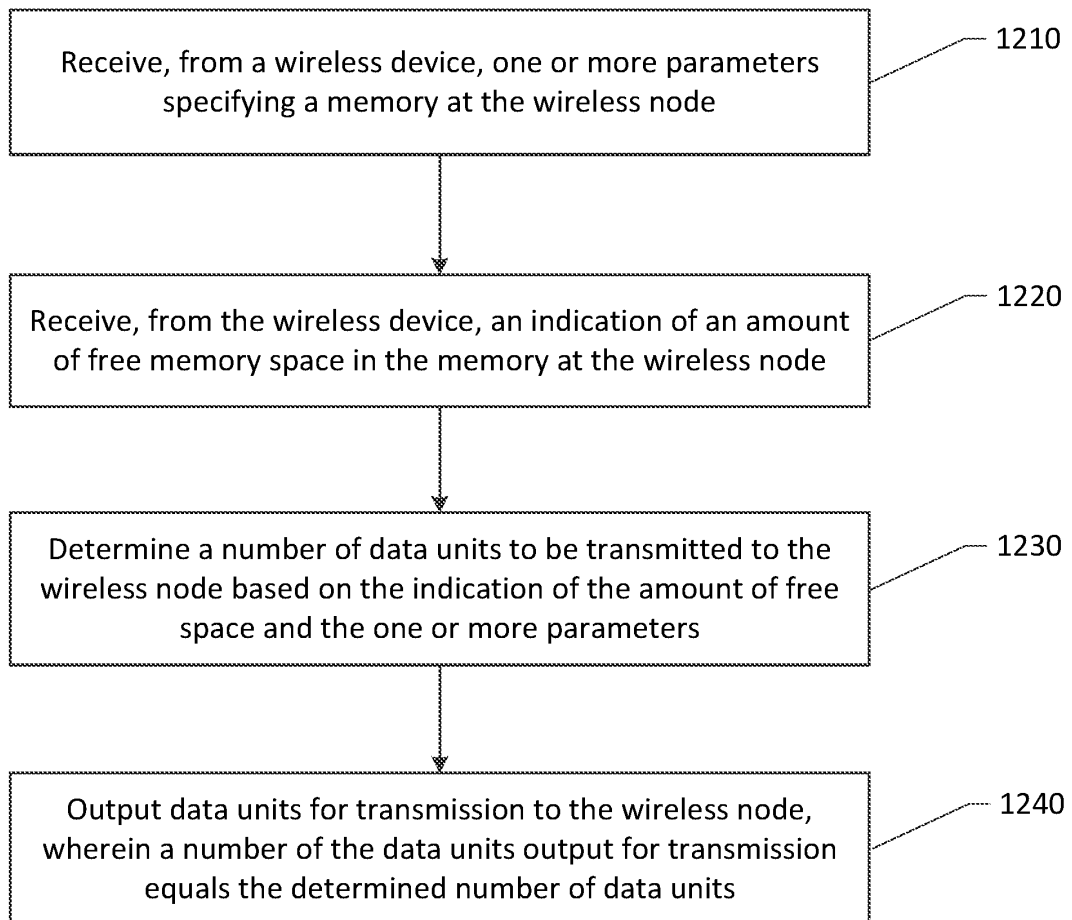


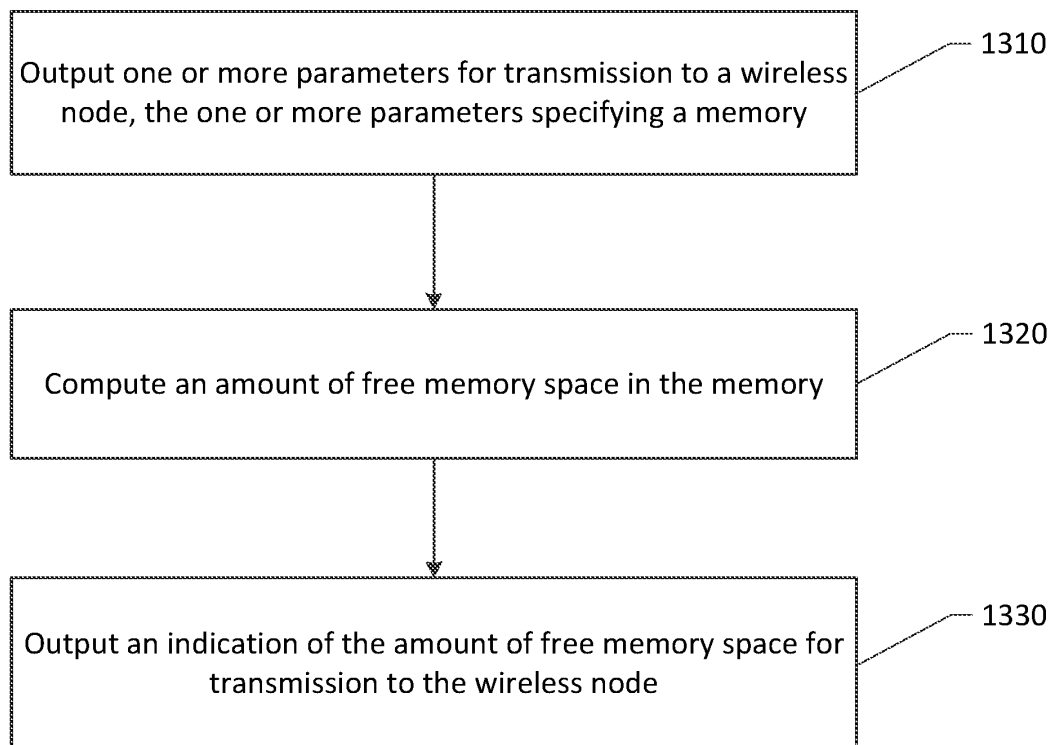
FIG. 10B

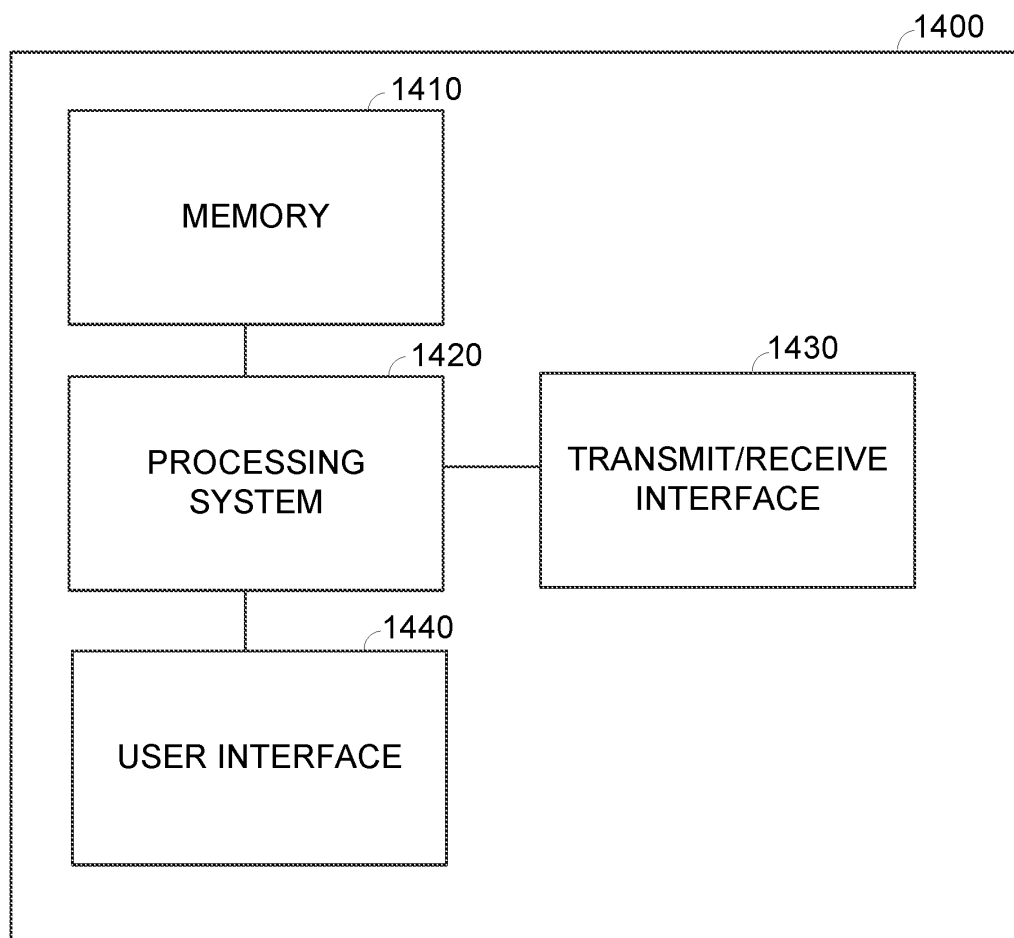
1100 →

Element ID	Length	Memory config tag	RBUF_ Unit_Size	Recipient Memory Multiple Buffer Units		TID grouping															
				Mem_ Unit_Size	Max MPDU_ per_ MemUnit	TID/TSID															
						Mult_ Buff_ MPDU															
				0	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5		
Octets 1				1	1	2	2	1	1	1	1	1	1	1	1	1	1	1	1		

FIG. 11

1200**FIG. 12**

1300**FIG. 13**

**FIG. 14**