

AMPLIFIER HAVING TWO OPERATING MODES

Abstract of the Disclosure

5 An amplifier (50) having an input and output stage for providing drive current to a load (R_L) coupled thereto includes circuitry (42, 44, 46, 52) that senses when an input signal is applied to the amplifier and is responsive thereto for providing an enabling signal at an output thereof and current regulator
10 circuitry (20, 22) that supplies a low drain current to bias the stages when the amplifier is in a quiescent operating mode absent an applied input signal and that is responsive to the enabling signal for increasing the current supplied to the stages to bias the same in a high bias drain current operating mode.

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Fig. 1 accompanies this abstract.