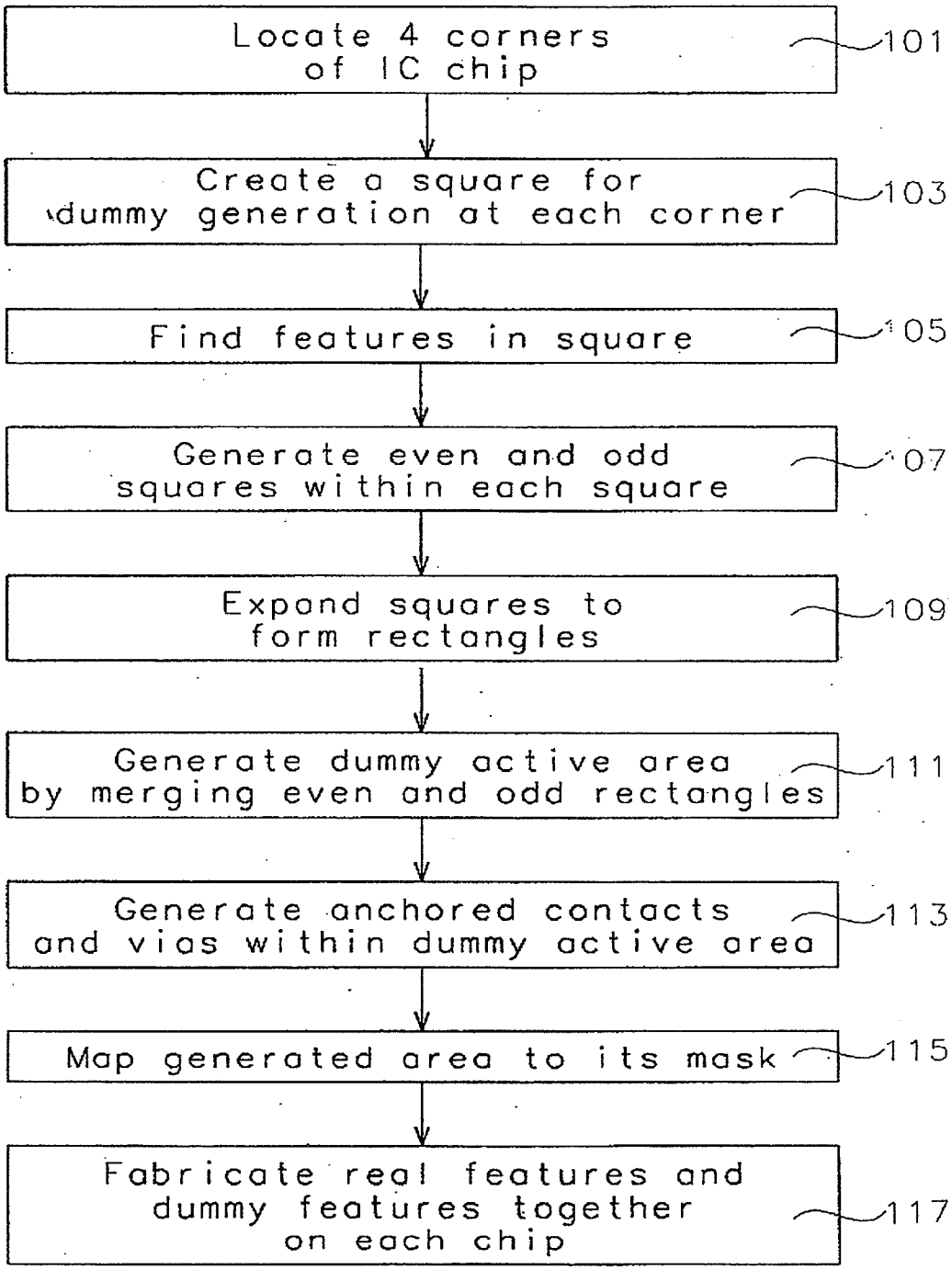

Locate 4 corners of IC chip
101
Create a square for dummy generation at each corner
103
Find features in square
105
Generate even and odd squares within each square
107
Expand squares to form rectangles
109
Generate dummy active area by merging even and odd rectangles
111
Generate anchored contacts and vias within dummy active area
113
Map generated area to its mask
115
Fabricate real features and dummy features together on each chip
117

FIG. 1

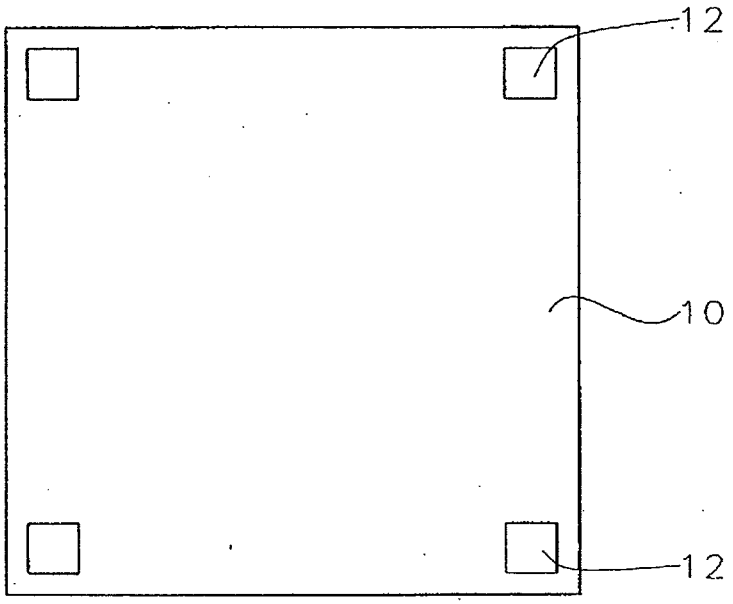


FIG. 2

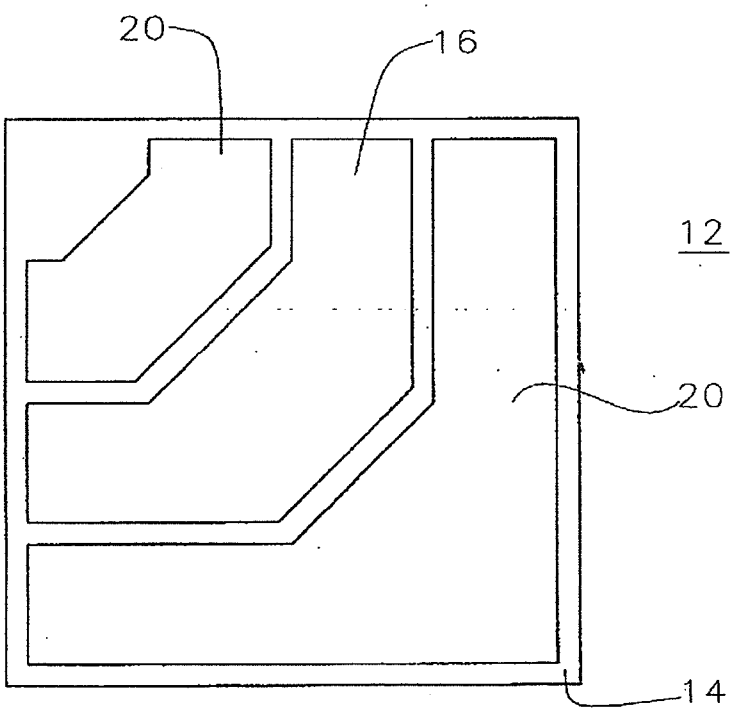


FIG. 3

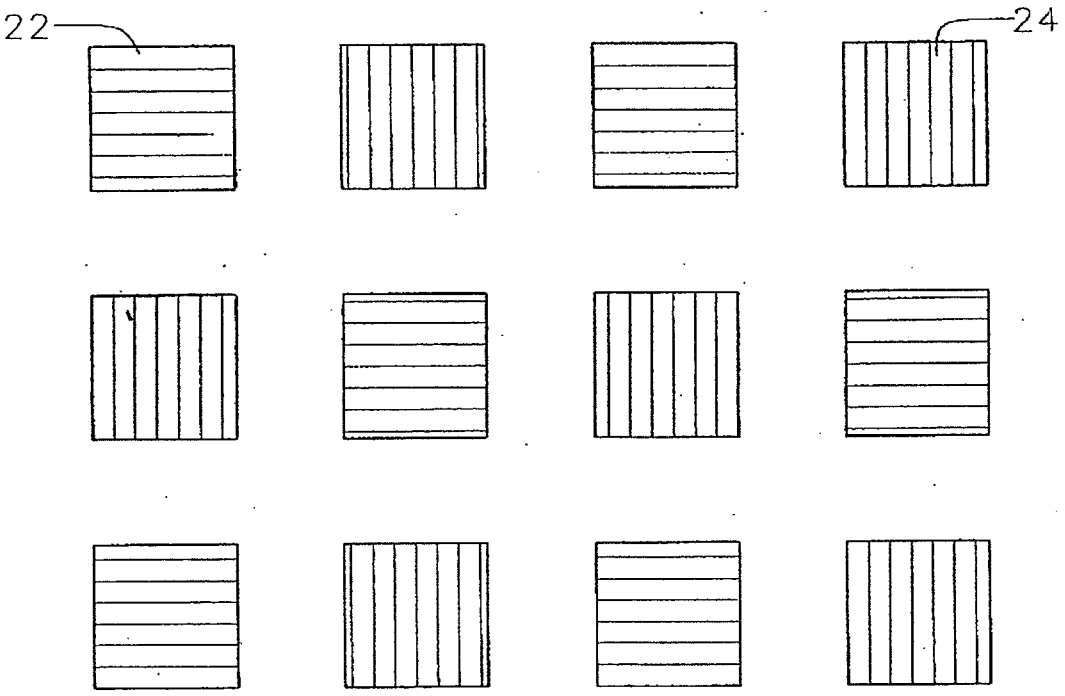


FIG. 4

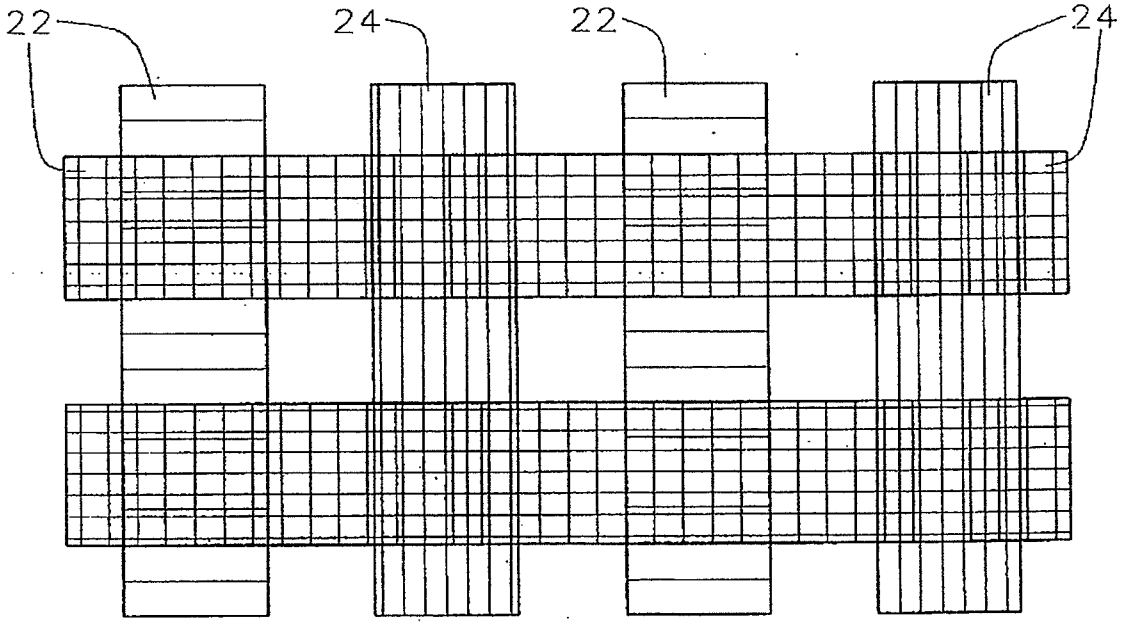


FIG. 5

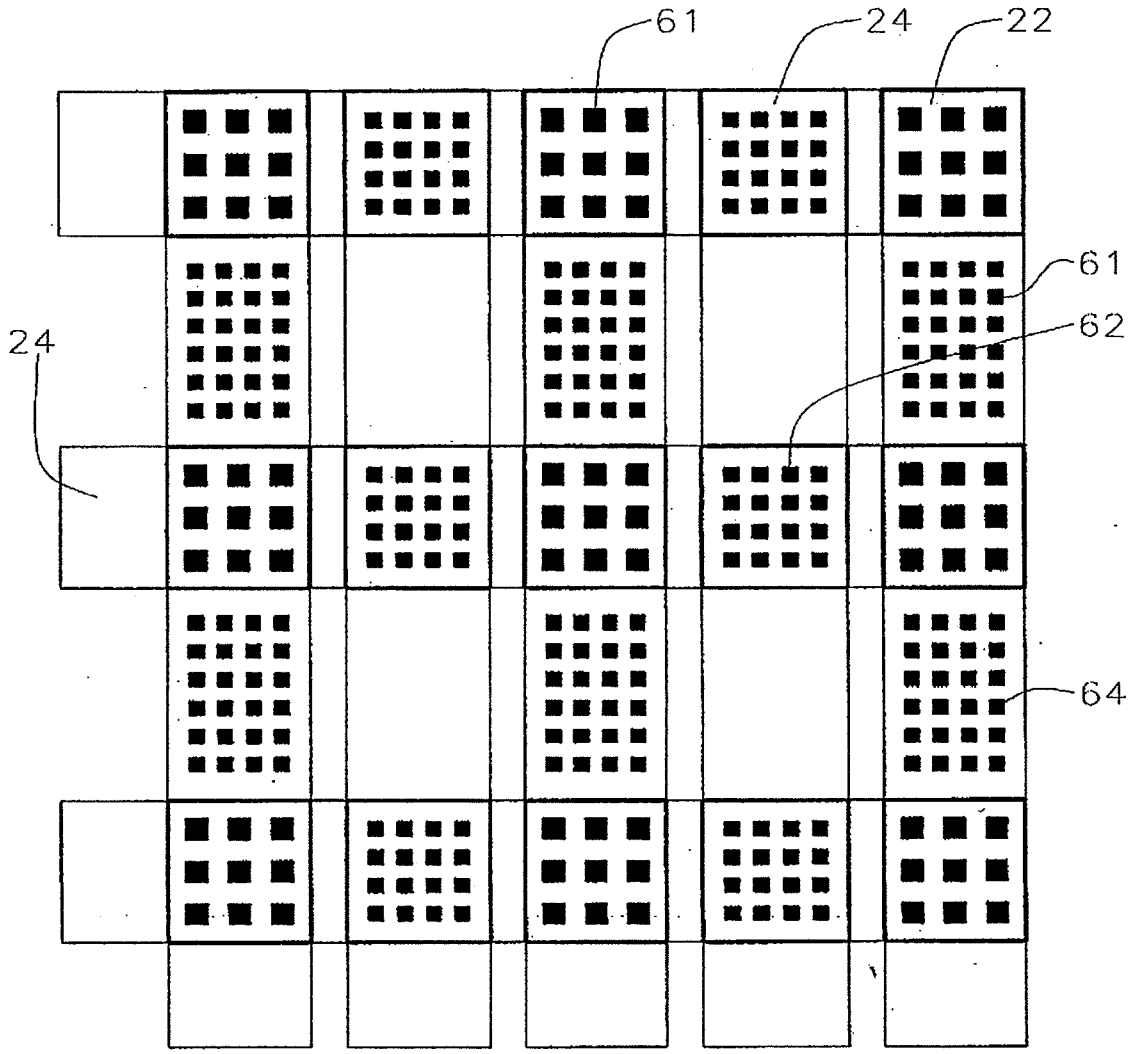


FIG. 6

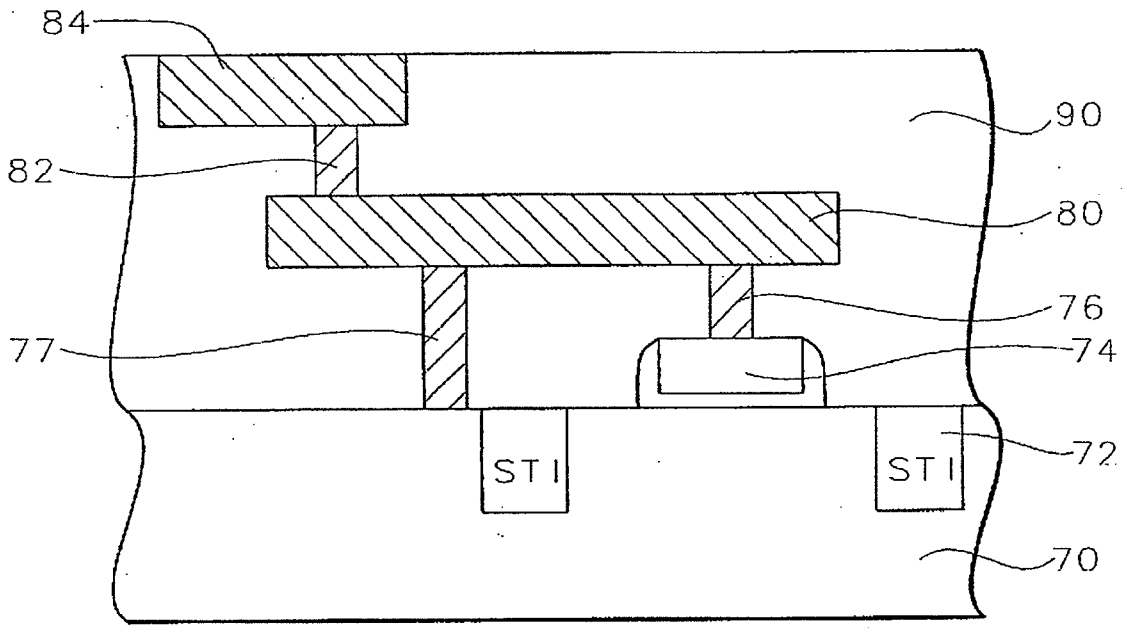


FIG. 7

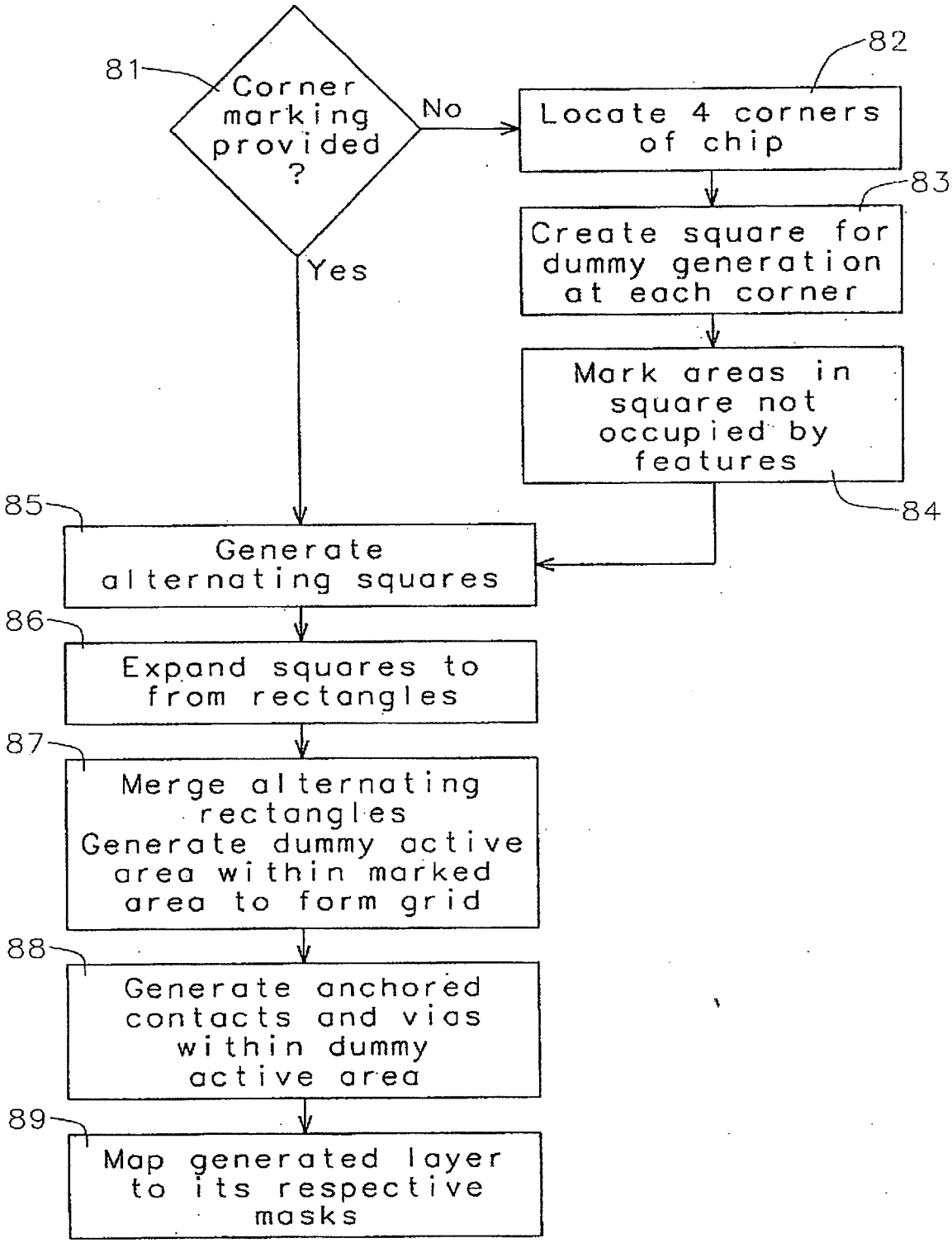

81
Corner marking provided ?
No
Yes
82
Locate 4 corners of chip
83
Create square for dummy generation at each corner
Mark areas in square not occupied by features
84
85
Generate alternating squares
86
Expand squares to from rectangles
87
Merge alternating rectangles Generate dummy active area within marked area to form grid
88
Generate anchored contacts and vias within dummy active area
89
Map generated layer to its respective masks

FIG. 8