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(54) Title: INTERRUPT STEERING FOR A COMPUTER SYSTEM

(57) Abstract

An interrupt request router (107) is described. The interrupt request router (107) stores a configuration value in a register, receives an interrupt request signal of a first plurality of interrupt requests signals (PCI0-PCI15), and generates an interrupt request signal of a second plurality of interrupt requests signals (IRQ1-IRQ15). The interrupt request signal of a second plurality of interrupt request signals (IRQ1-IRQ15) is determined by the configuration value.

