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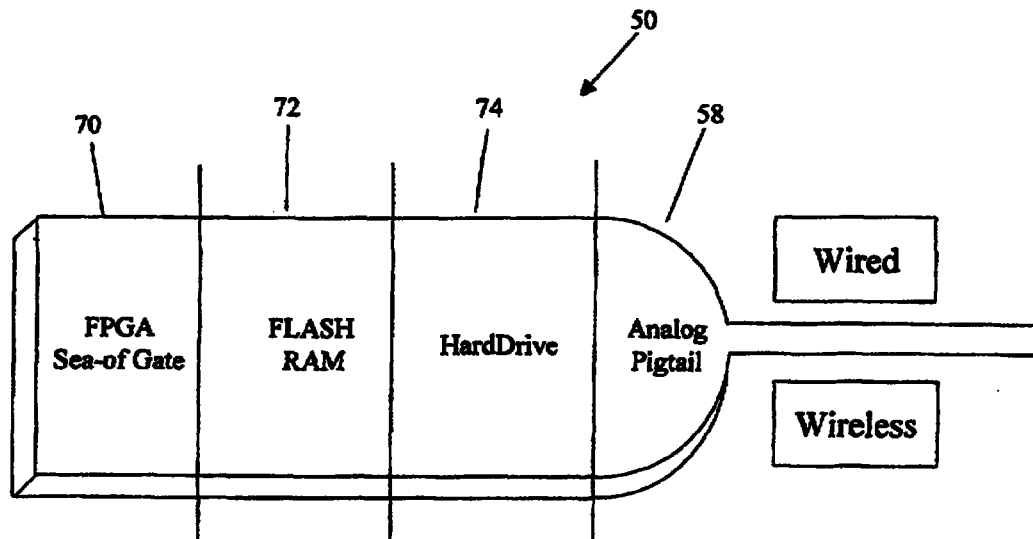
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(54) Title: **METHOD AND APPARATUS FOR COMPILING ONE CIRCUIT IN A SEQUENCE OF CIRCUITS WITHIN A PROGRAMMABLE GATE ARRAY**

NewCo Card Physical



(57) Abstract

A programmable gate array (70) including a plurality of logic gates can be programmed to constitute a series of configurations chosen to consist of a series of circuits. The series of circuits are chosen to accomplish a desired communication capability. Depending upon the circumstances of operation, the series of circuits can initially consist of a grouping of communications initialization circuits, then later a grouping of communications maintenance circuits, and still later a grouping of communications termination circuits.