

ABSTRACT

The purpose of the present invention is to achieve a display device capable of performing an all-selecting drive for gate bus lines without increasing the number of circuit elements more than heretofore and without lowering withstand voltage reliability. In a stage constituent circuit, which constitutes a shift register in a gate driver, an all-selecting signal (ALL-ON) for simultaneously turning all of gate bus lines to a selected state is given, as a low-potential power supply, to a source terminal of a thin film transistor (Tr4) for setting, at a low level, a QB node provided for setting a scanning signal (OUT) at the low level and to a source terminal of a thin film transistor (Tr3) for setting, at the low level, a Q node provided for setting the scanning signal (OUT) at a high level. A wiring (ALL-ON wiring) for supplying a low-potential power supply to the gate driver and a wiring (VSS wiring) for supplying the low-potential power supply to a circuit other than the gate driver are power supply wirings independent of each other.