

Abstract of the Disclosure

A television receiver embodying the invention includes a plurality of luminance signal sources (Y1,Y2), each capacitively coupled (C1,C2) to an input of a luminance signal selection switch (40) and each capacitor being coupled to a respective clamp circuit (42,44) with each clamp circuit having a reference voltage input coupled to a common reference voltage source (50). A sync separator (54) is provided having a first input coupled to receive the output of luminance signal selection switch and a second input coupled to a source of reference potential (51) derived from said reference voltage source. Advantageously, the overall number of clamp circuits is minimized by merging the sync separator circuit with the input signal selection clamping circuit.

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This Sheet Has Been Replaced