

[Name of Document]

ABSTRACT

[Summary]

SAMPLING RATE CONVERTER

[Object]

The invention is intended to provide an accurate sampling rate converter capable of operating stably.

[Configuration]

The converter comprises a sampling rate ratio-generating circuit 17 for creating data d about the ratio of the output sampling rate f_o to the input sampling rate f_{in} , a fine adjustment circuit 18 for finely adjusting the data d , using corrective data p , a forecasting circuit 19 for generating forecasted output timing data g about the timing at which output data D_{ou} is delivered according to output data e from the fine adjustment circuit 18, a comparison-and-correcting circuit 21 for comparing the data g with data m and creating corrective data p from the results of the comparison, and an arithmetic circuit 12 on the output side. The data m indicates the timing at which the output data D_{ou} is actually delivered. The arithmetic circuit 12 creates the output data D_{ou} from both input data D_{in} and data g .

[Selected Figure]

Figure 1

