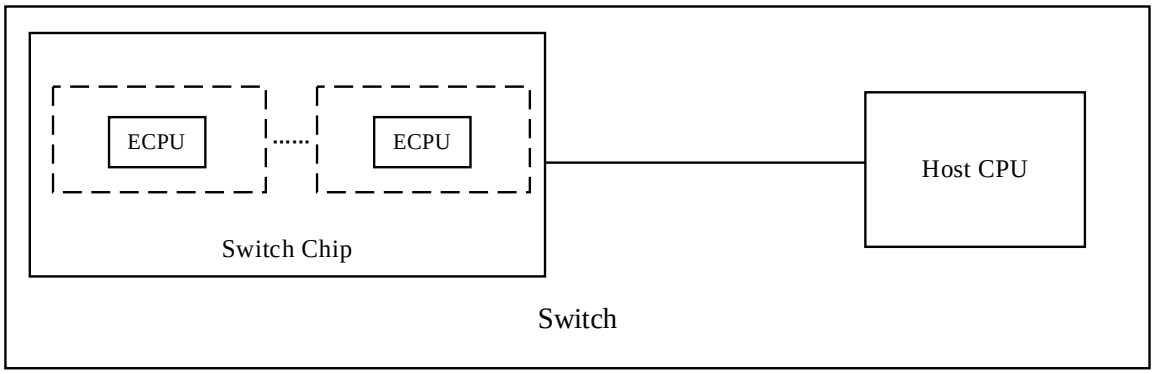

ECPU
ECPU
Switch Chip
Host CPU
Switch

FIG. 5

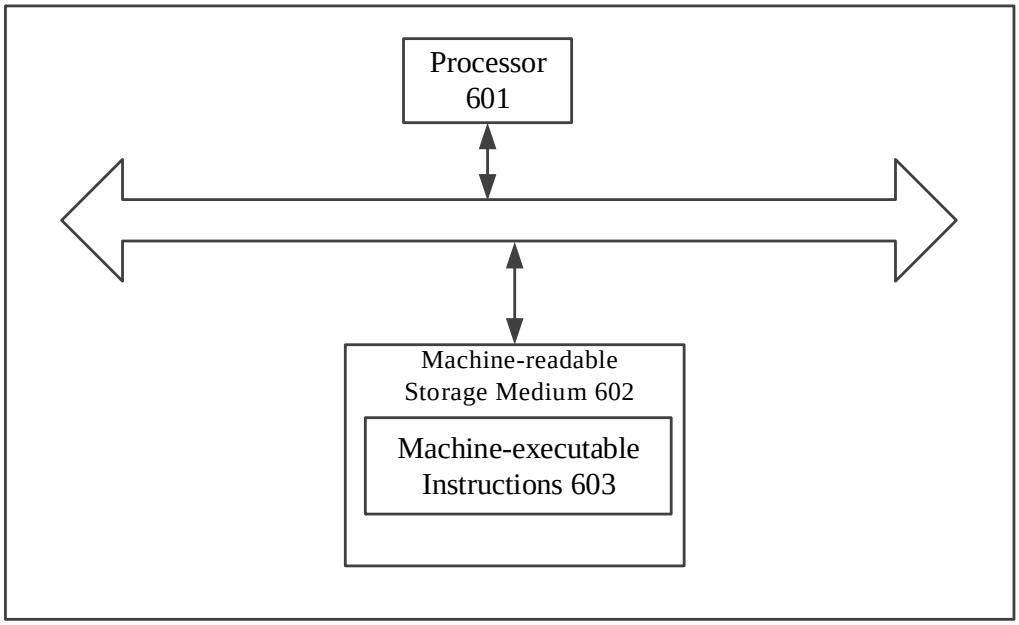

Processor
601
Machine-readable
Storage Medium 602
Machine-executable
Instructions 603

FIG. 6