

Systems and Methods for In-situ Wafer Edge and Backside Plasma Cleaning

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Background

[0001] During semiconductor chip fabrication, a substrate is subjected to a series of material
5 deposition and removal processes to buildup patterns of various conductive and dielectric
materials on the substrate that ultimately form a functional integrated circuit device. During
the various material removal processes, i.e., etching processes, etch byproduct materials can
build up at the edge region of the substrate where plasma density is often lower. The etch
byproduct materials can be of any material type used in the fabrication of the semiconductor
10 chip, and often include polymers comprised of carbon, oxygen, nitrogen, fluorine, among
others. As the etch byproduct material builds up near the peripheral edge of the substrate, the
etch byproduct material can become unstable and flake/detach from the substrate, thereby
becoming a source for potential material contamination of other portions of the substrate
where semiconductor chips are being fabricated. In addition, during the various fabrication
15 processes, byproduct materials can adhere to any exposed portions of the backside surface of
the substrate, thereby becoming another source for potential material contamination of critical
portions of the substrate. Therefore, during the fabrication of semiconductor devices on the
substrate, it is necessary to remove problematic byproduct materials from the peripheral edge
of the substrate and from the backside of the substrate. It is within this context that the
20 present invention arises.

Summary

[0002] In one embodiment, a semiconductor processing system is disclosed. The system includes a lower electrode plate and a radiofrequency power supply connected to supply
5 radiofrequency power to the lower electrode plate. The system also includes a dielectric upper plate positioned parallel to and spaced apart from the lower electrode plate. The system also includes an upper electrode plate positioned next to the dielectric upper plate, such that the dielectric upper plate is located between the lower electrode plate and the upper electrode plate. The upper electrode plate is electrically connected to a reference ground potential. The
10 system also includes a dielectric support defined to support a workpiece in an electrically isolated manner within a region between the lower electrode plate and the dielectric upper plate. The system also includes a purge gas supply channel formed to supply a purge gas to the region between the lower electrode plate and the dielectric upper plate at a central location of the dielectric upper plate. The system also includes a process gas supply channel
15 formed to supply a process gas to the region between the lower electrode plate and the dielectric upper plate at a periphery of the dielectric upper plate. The dielectric support is defined to position the workpiece at a position proximate to and substantially parallel to the dielectric upper plate, such that the purge gas is made to flow from the purge gas supply channel over a top surface of the workpiece between the dielectric upper plate and the top
20 surface of the workpiece, so as to prevent the process gas from flowing over the top surface of the workpiece, and so as to cause the process gas to flow around a peripheral edge of the workpiece and below the workpiece into a region between the lower electrode plate and a bottom surface of the workpiece, when the workpiece is present on the dielectric support.

[0003] In one embodiment, a method is disclosed for plasma cleaning a peripheral region and
25 a bottom surface of a workpiece. The method includes positioning the bottom surface of the workpiece on a dielectric support defined to support the workpiece in an electrically isolated

manner within a region between an upper surface of a lower electrode plate and a lower surface of a dielectric upper plate. An upper electrode plate is positioned next to an upper surface of the dielectric upper plate. The lower electrode plate is connected to receive radiofrequency power. The upper electrode plate is electrically connected to a reference
5 ground potential. The method also includes positioning the dielectric support such that a top surface of the workpiece is separated from the lower surface of the dielectric upper plate by a narrow gap, and such that an open region exists between the bottom surface of the workpiece and the upper surface of the lower electrode plate. The method also includes flowing a purge gas to a central location within the narrow gap between the top surface of the workpiece and
10 the lower surface of the dielectric upper plate, such that the purge gas flows through the narrow gap in a direction away from the central location toward a periphery of the workpiece. The method also includes flowing a process gas to a peripheral region of the workpiece located outside the narrow gap. The process gas flows into the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate. The method also
15 includes supplying radiofrequency power to the lower electrode plate so as to transform the process gas into a plasma around the peripheral region of the workpiece and within the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate.

[0004] In one embodiment, a semiconductor processing system is disclosed. The system
20 includes a lower showerhead electrode plate having an interior region for transforming a process gas into a plasma. The lower showerhead electrode plate has a number of vents extending from an upper surface of the lower showerhead plate to the interior region. The system also includes a process gas supply channel is formed to supply the process gas to the interior region of the lower showerhead electrode plate. The system also includes a
25 radiofrequency power supply connected to supply radiofrequency power to the lower

showerhead electrode plate so as to transform the process gas into the plasma within the interior region of the lower showerhead electrode plate. The system also includes a first upper plate positioned parallel to and spaced apart from the lower showerhead electrode plate. The system also includes a second upper plate positioned next to the first upper plate such that the first upper plate is located between the lower showerhead electrode plate and the second upper plate. The second upper plate is electrically connected to a reference ground potential. The system also includes a dielectric edge ring that has an annular shape with an upper surface defined to contact and support a peripheral region of a bottom surface of a workpiece. The dielectric edge ring is defined to support the workpiece in an electrically isolated manner within a region between the upper surface of the lower showerhead electrode plate and a lower surface of the first upper plate. The system also includes a purge gas supply channel formed to supply a purge gas to the region between the upper surface of the lower showerhead electrode plate and the lower surface of the first upper plate at a central location of the first upper plate. The dielectric edge ring is defined to position the workpiece proximate to and substantially parallel to the first upper plate, such that the purge gas is made to flow from the purge gas supply channel over a top surface of the workpiece, between the lower surface of the first upper plate and the top surface of the workpiece, so as to prevent reactive constituents of the plasma from reaching the top surface of the workpiece, when the workpiece is present on the dielectric edge ring.

[0005] In one embodiment, a method is disclosed for plasma cleaning a bottom surface of a workpiece. The method includes positioning the workpiece on a dielectric edge ring that has an annular shape with an upper surface defined to contact and support a peripheral region of the bottom surface of the workpiece. The dielectric edge ring is defined to support the workpiece in an electrically isolated manner within a region between an upper surface of a lower showerhead electrode plate and a lower surface of a first upper plate. A second upper

plate is positioned next to an upper surface of the first upper plate. The lower showerhead electrode plate is connected to receive radiofrequency power. The second upper plate electrically is connected to a reference ground potential. The method also includes positioning the dielectric edge ring such that a top surface of the workpiece is separated from the lower surface of the first upper plate by a narrow gap, and such that an open region exists between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate. The method also includes flowing a purge gas to a central location within the narrow gap, such that the purge gas flows through the narrow gap in a direction away from the central location toward a periphery of the workpiece. The method also includes flowing a process gas to an interior region of the lower showerhead electrode plate. The method also includes supplying radiofrequency power to the lower showerhead electrode plate so as to transform the process gas into a plasma within the interior region of the lower showerhead electrode plate, whereby reactive constituents of the plasma flow through vents from the interior region of the lower showerhead electrode plate into the open region between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate.

[0006] Other aspects and advantages of the invention will become more apparent from the following detailed description, taken in conjunction with the accompanying drawings, illustrating by way of example the present invention.

Brief Description of the Drawings

[0007] Figure 1A shows a semiconductor processing system, in accordance with one embodiment of the present invention.

[0008] Figure 1B shows a horizontal cross-sectional view A-A as denoted in Figure 1A, in
5 accordance with one embodiment of the present invention.

[0009] Figure 1C shows a variation of the semiconductor processing system in which the process gas supply channel is defined to pass through the dielectric upper plate a various locations about the periphery of the dielectric upper plate, in accordance with one embodiment of the present invention.

10 [0010] Figure 1D shows the horizontal cross-sectional view A-A as denoted in Figure 1C, in accordance with one embodiment of the present invention.

[0011] Figure 1E shows a variation of the semiconductor processing system of Figure 1A defined to use a remote plasma source, in accordance with one embodiment of the present invention.

15 [0012] Figure 1F shows the semiconductor processing system of Figure 1A in a configuration in which the workpiece is lowered to rest on the lower electrode assembly in order to perform plasma processing of the peripheral edge of the workpiece, in accordance with one embodiment of the present invention.

[0013] Figure 2A shows a semiconductor processing system, in accordance with one
20 embodiment of the present invention.

[0014] Figure 2B shows the horizontal cross-sectional view B-B as denoted in Figure 2A, in accordance with one embodiment of the present invention.

[0015] Figure 2C shows an example embodiment in which the dielectric edge ring is defined as a stack of annular shaped rings separated from each other by spaces that form the vents, in
25 accordance with one embodiment of the present invention.

[0016] Figure 2D shows a variation of the semiconductor processing system of Figure 2A defined to use a remote plasma source, in accordance with one embodiment of the present invention.

[0017] Figure 2E shows the semiconductor processing system of Figure 2A in a configuration in which the workpiece is lowered to rest on the lower electrode assembly in order to perform plasma processing of the peripheral edge of the workpiece, in accordance with one embodiment of the present invention.

[0018] Figure 3A shows a semiconductor processing system, in accordance with one embodiment of the present invention.

10 [0019] Figure 3B shows a variation of the semiconductor processing system of Figure 3A defined to use a remote plasma source, in accordance with one embodiment of the present invention.

[0020] Figure 3C shows the semiconductor processing system of Figure 3A in a configuration in which the workpiece is lowered to rest on the lower electrode assembly in order to perform plasma processing of the peripheral edge of the workpiece, in accordance with one embodiment of the present invention.

[0021] Figure 4 shows a semiconductor processing system that is a variation of the system described with regard to Figure 3A, in accordance with one embodiment of the present invention.

20 [0022] Figures 5A and 5B show a semiconductor processing system that is also a variation of the system described with regard to Figure 3A, in accordance with one embodiment of the present invention.

[0023] Figure 5C shows a variation of the semiconductor processing system of Figure 5A defined to use a remote plasma source, in accordance with one embodiment of the present invention.

[0024] Figure 6 shows a flowchart of a method for plasma cleaning a bottom surface of a workpiece, in accordance with one embodiment of the present invention.

[0025] Figure 7 shows a flowchart of a method for plasma cleaning a bottom surface of a workpiece, in accordance with one embodiment of the present invention.

- 5 [0026] Figure 8 shows a flowchart of a method for performing both a bevel edge plasma cleaning process and backside cleaning process on a workpiece within a common plasma processing system, in accordance with one embodiment of the present invention.

Detailed Description

[0027] In the following description, numerous specific details are set forth in order to provide a thorough understanding of the present invention. It will be apparent, however, to one skilled in the art that the present invention may be practiced without some or all of these specific details. In other instances, well known process operations have not been described in detail in order not to unnecessarily obscure the present invention.

[0028] Figure 1A shows a semiconductor processing system 100, in accordance with one embodiment of the present invention. The system includes a chamber 101. Within the chamber 101, a dielectric upper plate 105 is positioned parallel to and spaced apart from a lower electrode plate 103. An upper electrode plate 107 is positioned next to the dielectric upper plate 105, such that the dielectric upper plate 105 is located between the lower electrode plate 103 and the upper electrode plate 107. The upper electrode plate 107 is electrically connected to a reference ground potential 128, as indicated by electrical connection 129. The dielectric upper plate 105 and the upper electrode plate 107 together form an upper electrode assembly 108.

[0029] A radiofrequency (RF) power supply 123 is connected to supply RF power to the lower electrode plate 103, through matching circuitry 125, as indicated by electrical connection 127. It should be understood that the matching circuitry 125 is defined to control an electrical impedance through the electrical connection 127, such that the supplied RF power can be efficiently transmitted through the region 140. The lower electrode plate 103 is disposed within an inner base plate 135, which is held by an outer base plate 136. The outer base plate 136 is electrically connected to a reference ground potential 138, as indicated by electrical connection 137. The inner base plate 135 is formed of a dielectric material, so as to electrically separate the radiofrequency powered lower electrode plate 103 from the grounded outer base plate 136. The lower electrode plate 103, inner base plate 135, and outer base plate

136 together form a lower electrode assembly 104.

[0030] The upper electrode assembly 108 is separated from the lower electrode assembly 104 by a region 140 between an upper surface of the lower electrode plate 103 and a lower surface of the dielectric upper plate 105. A dielectric support is defined to support a workpiece 109 in an electrically isolated manner within the region 140 between the lower electrode plate 103 and the dielectric upper plate 105. In the embodiment of Figure 1A, the dielectric support is defined as a set of dielectric lifting pins 111 that extend through the lower electrode plate 103 to support the workpiece 109 in an electrically isolated manner within the region 140 between the lower electrode plate 103 and the dielectric upper plate 105. In this configuration with the workpiece 109 supported on the set of dielectric lifting pins 111, the workpiece 109 is at a floating electrical potential. In one embodiment, the set of dielectric lifting pins 111 are formed of a ceramic material that is not electrically conductive.

[0031] The set of dielectric lifting pins 111 are defined to extend in a controllable manner into the region 140 between the lower electrode plate 103 and the dielectric upper plate 105 so as to control a distance 112 that forms a gap 113 between the top surface of the workpiece 109 and the dielectric upper plate 105 when the workpiece 109 is present on the set of dielectric lifting pins 111. In one embodiment, the distance 112 measured perpendicularly between the top surface of the workpiece 109 and the dielectric upper plate 105 is about 0.35 mm. However, it should be understood that in other embodiments, the distance 112 between the top surface of the workpiece 109 and the dielectric upper plate 105 can be set as needed. Also, it should be understood that the distance 112 between the top surface of the workpiece 109 and the dielectric upper plate 105 is adjustable during and/or between plasma processing operations.

[0032] In some embodiments, the dielectric upper plate 105 can include heating components to provide for temperature control of the workpiece 109. For example, in some embodiments,

the dielectric upper plate 105 can include radiative heating elements to provide for radiative heating of the workpiece 109 across the gap 113. In other embodiments, the dielectric upper plate 105 can include resistive heaters to provide for heating of the dielectric upper plate 105 and in turn provide for radiative and/or convective heating of the workpiece 109.

5 [0033] A purge gas supply channel 115 is formed to supply a purge gas to the region 140 between the lower electrode plate 103 and the dielectric upper plate 105 at a central location of the dielectric upper plate 105. In one embodiment, such as shown in the example of Figure 1A, the purge gas supply channel 115 is formed through both the upper electrode plate 107 and the dielectric upper plate 105, so as to dispense the purge gas at the central location of the
10 dielectric upper plate 105 and at a substantially central location of the top surface of the workpiece 109 when present on the set of dielectric lifting pins 111. The purge gas supply channel 115 is fluidly connected to a purge gas supply 117 containing the purge gas.

[0034] During plasma processing operations, the purge gas flows radially outward through the gap 113 across the top surface of the workpiece 109 from the central location toward the
15 periphery of the workpiece 109, so as to prevent reactive constituents of a plasma 102 from entering the gap 113 between the top surface of the workpiece 109 and the bottom surface of the dielectric upper plate 105 at the periphery of the top surface of the workpiece 109. Also, during plasma processing operations, the purge gas can provide for cooling of the workpiece 109. In some embodiments that utilize heating components within the dielectric upper plate
20 105, the cooling provided by the purge gas within the gap 113 combines with the heating provided by the heating components to provide an overall control of the workpiece 109 temperature. In various embodiments, the purge gas is defined as an inert gas such as nitrogen or helium, among others. However, it should be understood, that other gases or gas mixtures can be used for the purge gas in other embodiments, so long as the purge gas is
25 chemically compatible with the plasma process and capable of providing both reactive

plasma constituent exclusion from the region over the top surface of the workpiece 109 and required temperature control effects.

[0035] A process gas supply channel 119 is fluidly connected to a process gas supply 121 containing a process gas. The process gas is defined to transform into the plasma 102 when exposed to the RF power. The process gas supply channel 119 is formed to supply the process gas to locations near a periphery of the dielectric upper plate 105. The process gas emanating from the process gas supply channel 119 diffuses into the region 140 between the lower electrode plate 103 and the dielectric upper plate 105. In the example embodiment of Figure 1A, the process gas supply channel 119 is formed through the upper electrode plate 107, and includes an open region 119A formed between the upper electrode plate 107 and dielectric upper plate 105.

[0036] In various embodiments, the process gas is defined as one or more of an oxygen based chemistry, a fluorine based chemistry, a chlorine based chemistry, among others. However, it should be understood, that other gases or gas mixtures can be used for the process gas in other embodiments, so long as the process gas is defined to transform into the plasma 102 having appropriate reactive constituent characteristics when exposed to the RF power supplied through the electrical connection 127. It should also be understood that in various embodiments the process gas can vary in composition depending on the characteristics of the RF power to be used, e.g., frequency, power, and duty cycle, the pressure to be applied inside the chamber 101, the temperature to be applied inside the chamber 101, the flow rate of the process gas through the chamber 101, and the types of reactive constituents needed to effect a particular reaction on the portions of the workpiece 109 in exposure to the plasma 102. In some embodiments, the RF power is supplied at a frequency of 60 megaHertz (MHz) or higher.

[0037] Figure 1B shows a horizontal cross-sectional view A-A as denoted in Figure 1A, in

accordance with one embodiment of the present invention. As shown in Figure 1B, the purge gas supply channel 115 is defined to dispense the purge gas at a substantially central location below the dielectric upper plate 105. Also, the open region between the upper electrode plate 107 and the dielectric upper plate 105 through which the process gas is dispensed is defined in a substantially uniform manner about a periphery of the dielectric upper plate 105, such that the process gas is dispensed in a substantially uniform manner about the periphery of the dielectric upper plate 105.

[0038] Figure 1C shows a variation of the semiconductor processing system 100 in which the process gas supply channel 119 is defined to pass through the dielectric upper plate 105 at various locations about the periphery of the dielectric upper plate 105, as indicated by passages 119B, in accordance with one embodiment of the present invention. Figure 1D shows the horizontal cross-sectional view A-A as denoted in Figure 1C, in accordance with one embodiment of the present invention. As shown in Figure 1D, the passages 119B through which the process gas flows are positioned in a substantially uniform manner about the periphery of the dielectric upper plate 105, such that the process gas is dispensed in a substantially uniform manner about the periphery of the dielectric upper plate 105. Also, it should be noted that Figure 1D shows another embodiment in which the purge gas is supplied through multiple passages 115A to the location underlying the central region of the dielectric upper plate 105.

[0039] With reference back to Figure 1A, during plasma processing operations within the semiconductor processing system 100, the purge gas is flowed through the purge gas supply channel 115 and the process gas is flowed through the process gas supply channel 119. The dielectric support defined as the set of dielectric lifting pins 111 is defined to position the workpiece 109 at a position proximate to and substantially parallel to the dielectric upper plate 105, such that the purge gas is made to flow from the purge gas supply channel 115

over a top surface of the workpiece 109 between the dielectric upper plate 105 and the top surface of the workpiece 109, so as to prevent the process gas from flowing over the top surface of the workpiece 109 and so as to cause the process gas to flow around the peripheral edge of the workpiece 109 and below the workpiece 109 into the region between the lower electrode plate 103 and the bottom surface of the workpiece 109, when the workpiece 109 is present on the set of dielectric lifting pins 111.

[0040] The purge gas outflow at the periphery of the dielectric upper plate 105 prevents the process gas and any reactive constituents of the plasma 102 from entering the region over the top surface of the workpiece 109. The process gas flows around and beneath the workpiece 109 and is transformed into the plasma 102 by the RF power transmitted through the electrical connection 127 to the lower electrode plate 103. The plasma 102 is exposed to the peripheral edge of the workpiece 109 and the bottom surface of the workpiece 109, so as to react with and remove unwanted materials from those regions of the workpiece 109. The process gas, purge gas, and plasma 102 reaction by-product materials are evacuated from the chamber 101 through a port 133 by way of an exhaust 131, as indicated by arrows 139.

[0041] It should be understood that any portion of the various components of the system 100 that are exposed to reactive constituents of the plasma 102 can be protected as necessary through use of plasma erosion resistant materials and/or through use of protective coatings, such as Y₂O₃ or other ceramic coatings. Also, in some embodiments, structures such as the lower electrode assembly 104 may be covered by a thin quartz plate, while ensuring that the RF power transfer from the lower electrode plate 103 to the plasma 102 is not disrupted by the thin quartz plate.

[0042] During plasma processing operations using the system 100, the etch rate of material from the bottom surface of the workpiece 109 is a partial function of the RF power applied to the process gas and the pressure of the process gas within the chamber 101. More

specifically, a higher RF power yields a higher etch rate of material from the bottom surface of the workpiece 109, vice-versa. And, a lower pressure of the process gas within the chamber 101 yields a higher etch rate of material from the bottom surface of the workpiece 109, vice-versa. Additionally, uniformity of the material etch rate across the bottom surface
5 of the workpiece 109 is improved at lower process gas pressure within the chamber 101.

[0043] In various embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 100 Watts (W) to about 10 kiloWatts (kW). In some embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 1 kW to about 3 kW. In some embodiments, the RF power is supplied
10 by the RF power supply 123 within a frequency range extending from about 2 megaHertz (MHz) to about 60 MHz. In some embodiments, direct current (DC) power can also be applied to the lower electrode plate 103. Additionally, in some embodiments, multiple frequencies of RF power can be supplied to the lower electrode plate 103 at either the same time or at different times, such as in a cyclical manner.

15 [0044] In some embodiments, the pressure of the process gas within the chamber is controlled within a range extending from about 50 milliTorr (mT) to about 10 Torr (T). In some embodiments, the pressure of the process gas within the chamber is controlled within a range extending up to about 2 T. In some embodiments, the process gas is supplied to the plasma 102 generation volume at a flow rate within a range extending from about 0.1
20 standard liters per minute (slm) to about 5 slm. In some embodiments, the process gas is supplied to the plasma 102 generation volume at a flow rate within a range extending from about 1 slm to about 5 slm.

[0045] Figure 1E shows a variation of the semiconductor processing system 100 of Figure 1A defined to use a remote plasma source 184, in accordance with one embodiment of the
25 present invention. The remote plasma source 184 is defined to generate reactive constituents

of the plasma 102 external to the chamber 101, and flow the reactive constituents of the plasma 102 through a conduit 180 to the region beneath the workpiece 109, as indicated by arrow 182. Also in this embodiment, the RF power is supplied from the RF power supply 123 to the outer base plate 136, as indicated by electrical connection 127A, so as to generate more reactive constituents of the plasma 102 at the region near the peripheral edge of the workpiece 109. It should be understood that in this embodiment, the RF powered portions of the outer base plate 136 are electrically isolated from the reference ground potential 138.

[0046] In various embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 1 kW to about 10 kW. In some embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 5 kW to about 8 kW. In some embodiments, the RF power is supplied by the RF power supply 123 within a frequency range extending from about 2 MHz to about 60 MHz. In some embodiments, direct current (DC) power can also be applied to the lower electrode plate 104. Additionally, in some embodiments, multiple frequencies of RF power can be supplied to the outer base plate 136 at either the same time or at different times, such as in a cyclical manner.

[0047] Also, in this embodiment, it should be understood that the purge gas is made to flow from the purge gas supply channel 115 over the top surface of the workpiece 109 between the dielectric upper plate 105 and the top surface of the workpiece 109, so as to prevent the reactive constituents of the plasma 102 from flowing over and reacting with the top surface of the workpiece 109. The process gas, purge gas, and plasma 102 reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated by arrows 139. In various embodiments, the remote plasma source 184 is defined to generate reactive constituents of the plasma 102 using RF power, microwave power, or a combination thereof. Also, in various embodiments, the remote plasma source 184 is defined as either a capacitive coupled plasma source or an inductively coupled plasma source.

[0048] In some embodiments, the pressure of a process gas within the remote plasma source 184 is controlled within a range extending from about 0.1 T to about 10 T. In some embodiments, the pressure of the process gas within the remote plasma source 184 is controlled within a range extending from about 1 T to about 10 T. In some embodiments, the process gas is supplied to the remote plasma source 184 at a flow rate within a range extending from about 0.1 slm to about 5 slm. In some embodiments, the process gas is supplied to the remote plasma source 184 at a flow rate within a range extending from about 1 slm to about 5 slm.

[0049] Figure 1F shows the semiconductor processing system 100 in a configuration in which the workpiece 109 is lowered to rest on the lower electrode assembly 104 in order to perform plasma processing of the peripheral edge of the workpiece 109, in accordance with one embodiment of the present invention. In this embodiment, the purge gas is flowed through the purge gas supply channel 115 and the process gas is flowed through the process gas supply channel 119. The set of dielectric lifting pins 111 are fully retracted such that the workpiece 109 rests on the lower electrode assembly 104 at a position proximate to and substantially parallel to the dielectric upper plate 105, such that the purge gas is made to flow from the purge gas supply channel 115 over a top surface of the workpiece 109 between the dielectric upper plate 105 and the top surface of the workpiece 109, so as to prevent the process gas from flowing over the top surface of the workpiece 109 and so as to cause the process gas to flow around the peripheral edge of the workpiece 109.

[0050] The purge gas outflow at the periphery of the dielectric upper plate 105 prevents the process gas and any reactive constituents of the plasma 102A from entering the region over the top surface of the workpiece 109. The process gas flows around the peripheral edge of the workpiece 109 and is transformed into the plasma 102A by the RF power transmitted through the electrical connection 127 to the lower electrode plate 103. The plasma 102A is exposed to

the peripheral edge of the workpiece 109, so as to react with and remove unwanted materials from those regions of the workpiece 109. The process gas, purge gas, and plasma 102A reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated by arrows 139.

5 [0051] Figure 2A shows a semiconductor processing system 200, in accordance with one embodiment of the present invention. As with the system 100 of Figure 1A, the system 200 includes the chamber 101, the upper electrode assembly 108, and the lower electrode assembly 104. The upper electrode assembly 108 includes the dielectric upper plate 105 and the upper electrode plate 107. The upper electrode plate 107 is electrically connected to the
10 reference ground potential 128, as indicated by the electrical connection 129. The purge gas supply channel 115 extends from the purge gas supply 117 through the upper electrode assembly 108 to provide for supply of the purge gas at the central location below the dielectric upper plate 105. The process gas supply channel 119 extends from the process gas supply 121 through the upper electrode assembly 108 to provide for supply of the process gas
15 at the outer peripheral edge of the workpiece 109.

[0052] The lower electrode assembly 104 includes the lower electrode plate 103 supported by the inner base plate 135, which is supported by the outer base plate 136. The lower electrode plate 103 is electrically connected to receive RF power from the RF power supply 123 by way of the matching circuitry 125 and electrical connection 127. The outer base plate 136 is
20 formed of an electrically conductive material and is electrically connected to the reference ground potential 137. The inner base plate 135 is formed of a dielectric material so as to electrically isolate the RF powered lower electrode plate 103 from the grounded outer base plate 136.

[0053] The system 200 can also include a set of lifting pins 111A for handling of the
25 workpiece 109 during placement of the workpiece 109 within the chamber 101 and removal

of the workpiece from the chamber 101. However, unlike the set of dielectric lifting pins 111 in the system 100, the set of lifting pins 111A in the system 200 are not used as the dielectric support to support the workpiece 109 during plasma processing operations within the chamber 101. Instead, the system 200 includes a dielectric edge ring 201 to serve as the
5 dielectric support for the workpiece 109. The dielectric edge ring 201 is formed of a dielectric material and has an annular shape with an upper surface defined to contact and support a peripheral region of the bottom surface of the workpiece 109.

[0054] Figure 2B shows the horizontal cross-sectional view B-B as denoted in Figure 2A, in accordance with one embodiment of the present invention. As shown in Figure 2B, the
10 dielectric edge ring 201 has an annular shape so as to confine a plasma 203 to be generated within the region between the top surface of the lower electrode plate 103 and the bottom surface of the workpiece 109. In this manner, the dielectric edge ring 201 is defined as a plasma exclusion zone (PEZ) ring.

[0055] With reference back to Figure 2A, the dielectric edge ring 201 is defined to extend in
15 a controllable manner into the region 140 between the lower electrode plate 103 and the dielectric upper plate 105 so as to control the distance 112 between the top surface of the workpiece 109 and the dielectric upper plate 105 when the workpiece 109 is present on the dielectric edge ring 201. Extension of the dielectric edge ring 201 into the region 140 between the lower electrode plate 103 and the dielectric upper plate 105 also forms a plasma
20 generation volume beneath the workpiece 109 and above the lower electrode plate 103, such that the bottom surface of the workpiece 109 can be exposed to a plasma 203 generated with the plasma generation volume. Thus, the dielectric edge ring 201 also functions to confine the plasma 203 to the plasma generation volume beneath the workpiece 109. It should be understood that in some embodiments, the position of the dielectric edge ring 201 relative to
25 the lower electrode plate 103 is adjustable, thereby providing for adjustment of the size of the

plasma processing volume between the workpiece 109 and the lower electrode plate 103.

[0056] The dielectric edge ring 201 includes vents 205 defined to allow for flow of the process gas from an output of the process gas supply channel 119 to the region between the lower electrode plate 103 and the bottom surface of the workpiece 109, when the workpiece
5 109 is present on the dielectric edge ring 201. Figure 2C shows an example embodiment in which the dielectric edge ring 201 is defined as a stack of annular shaped rings 201A separated from each other by spaces that form the vents 205. In this embodiment, the annular shaped rings 201A can be held in their spaced apart relationship by structural members 204 that connect to the various annular shaped rings 201A at a number of locations around the
10 circumference of the annular shaped rings 201A. Also, in some embodiments, these structural members 204 can be defined to hold the annular shaped rings 201A in a fixed spatial configuration. And, in some embodiments, these structural members 204 can be defined to provide for controlled variation of the spatial configuration of the annular shaped rings 201A relative to each other, such that the spacing between the various annular shaped rings 201A
15 that form the vents 205 can be adjusted in size.

[0057] It should be understood that the dielectric edge ring 201 embodiment of Figure 2C is one of many possible dielectric edge region 201 embodiments. For example, in other embodiments, the dielectric edge ring 201 may be a single monolithic structure that includes radially oriented passages for venting gases from the plasma processing volume beneath the
20 workpiece 109. Regardless of the particular embodiment, however, it should be understood that the dielectric edge ring 201 is formed of a dielectric material, has a top surface defined to support the workpiece 109 at the radial periphery of the bottom surface of the workpiece 109, and includes through-holes, vents, or other types of passages such that dielectric edge ring 201 serves as baffle for process gases and plasma process by-product materials exiting from
25 the plasma processing volume beneath the workpiece 109.

[0058] During the supply of the process gas through the process gas supply channel 119, the exhaust 131 can be turned off such that the process gas will diffuse through the vents 205 of the dielectric edge ring 201 into the plasma generation volume below the workpiece 109. Then, the purge gas can be supplied through the purge gas supply channel 115 to purge the gap 113 above the workpiece 109 of process gas. RF power can be supplied from the RF power supply 123 to the lower electrode plate 103, by way of the matching circuitry 125 and electrical connection 127, to transform the process gas within the plasma generation volume beneath the workpiece 109 into the plasma 203, whereby reactive constituents of the plasma 203 interact with the bottom surface of the workpiece 109 to remove undesirable materials from the workpiece 109. Then, the exhaust 131 can be turned on to evacuate both purge gases and process gases from within the chamber 101, and to evacuate the process gases and plasma processing by-product materials from the plasma generation volume beneath the workpiece 109, through the vents 205 of the dielectric edge ring 201 to the exhaust port 133, as indicated by arrows 139. Additionally, in some embodiments, the exhaust 131 may be turned on during supply of the RF power to generate the plasma 203, thereby providing for evacuation of process gases, purge gases, and plasma processing by-product materials during the plasma processing operation.

[0059] It should be understood that any portion of the various components of the system 200 that are exposed to reactive constituents of the plasma 203 can be protected as necessary through use of plasma erosion resistant materials and/or through use of protective coatings, such as Y₂O₃ or other ceramic coatings. Also, in some embodiments, structures such as the lower electrode assembly 104 may be covered by a thin quartz plate, while ensuring that the RF power transfer from the lower electrode plate 103 to the plasma 203 is not disrupted by the thin quartz plate.

[0060] During plasma processing operations using the system 200, the etch rate of material

from the bottom surface of the workpiece 109 is a partial function of the RF power applied to the process gas and the pressure of the process gas within the chamber 101. More specifically, a higher RF power yields a higher etch rate of material from the bottom surface of the workpiece 109, vice-versa. And, a lower pressure of the process gas within the chamber 101 yields a higher etch rate of material from the bottom surface of the workpiece 109, vice-versa. Additionally, uniformity of the material etch rate across the bottom surface of the workpiece 109 is improved at lower process gas pressure within the chamber 101.

[0061] In various embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 100 W to about 10 kW. In some embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 1 kW to about 3 kW. In some embodiments, the RF power is supplied by the RF power supply 123 within a frequency range extending from about 2 MHz to about 60 MHz. In some embodiments, direct current (DC) power can also be applied to the lower electrode plate 103. Additionally, in some embodiments, multiple frequencies of RF power can be supplied to the lower electrode plate 103 at either the same time or at different times, such as in a cyclical manner.

[0062] In some embodiments, the pressure of the process gas within the chamber is controlled within a range extending from about 50 mT to about 10 T. In some embodiments, the pressure of the process gas within the chamber is controlled within a range extending up to about 2 T. In some embodiments, the process gas is supplied to the plasma 102 generation volume at a flow rate within a range extending from about 0.1 slm to about 5 slm. In some embodiments, the process gas is supplied to the plasma 102 generation volume at a flow rate within a range extending from about 1 slm to about 5 slm.

[0063] Figure 2D shows a variation of the semiconductor processing system 200 of Figure 2A defined to use a remote plasma source 184, in accordance with one embodiment of the present invention. The remote plasma source 184 is defined to generate reactive constituents

of the plasma 203 external to the chamber 101, and flow the reactive constituents of the plasma 203 through a conduit 180 to the region beneath the workpiece 109, as indicated by arrow 182.

[0064] The process gas, purge gas, and plasma 203 reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated by arrows 139. In various embodiments, the remote plasma source 184 is defined to generate reactive constituents of the plasma 203 using RF power, microwave power, or a combination thereof. Also, in various embodiments, the remote plasma source 184 is defined as either a capacitively coupled plasma source or an inductively coupled plasma source.

10 [0065] In some embodiments, the pressure of a process gas within the remote plasma source 184 is controlled within a range extending from about 0.1 T to about 10 T. In some embodiments, the pressure of the process gas within the remote plasma source 184 is controlled within a range extending from about 1 T to about 10 T. In some embodiments, the process gas is supplied to the remote plasma source 184 at a flow rate within a range
15 extending from about 0.1 slm to about 5 slm. In some embodiments, the process gas is supplied to the remote plasma source 184 at a flow rate within a range extending from about 1 slm to about 5 slm.

[0066] Figure 2E shows the semiconductor processing system 200 in a configuration in which the workpiece 109 is lowered to rest on the lower electrode assembly 104 in order to
20 perform plasma processing of the peripheral edge of the workpiece 109, in accordance with one embodiment of the present invention. In this embodiment, the purge gas is flowed through the purge gas supply channel 115 and the process gas is flowed through the process gas supply channel 119. The dielectric edge ring 201 is fully retracted such that the workpiece 109 rests on the lower electrode assembly 104 at a position proximate to and
25 substantially parallel to the dielectric upper plate 105, such that the purge gas is made to flow

from the purge gas supply channel 115 over a top surface of the workpiece 109 between the dielectric upper plate 105 and the top surface of the workpiece 109, so as to prevent the process gas from flowing over the top surface of the workpiece 109 and so as to cause the process gas to flow around the peripheral edge of the workpiece 109.

5 [0067] The purge gas outflow at the periphery of the dielectric upper plate 105 prevents the process gas and any reactive constituents of the plasma 203A from entering the region over the top surface of the workpiece 109. The process gas flows around the peripheral edge of the workpiece 109 and is transformed into the plasma 203A by the RF power transmitted through the electrical connection 127 to the lower electrode plate 103. The plasma 203A is exposed to
10 the peripheral edge of the workpiece 109, so as to react with and remove unwanted materials from those regions of the workpiece 109. The process gas, purge gas, and plasma 203A reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated by arrows 139.

[0068] Figure 3A shows a semiconductor processing system 300, in accordance with one
15 embodiment of the present invention. The system 300 includes the chamber 101 and an upper electrode assembly 306, which includes a dielectric upper plate 105A and the upper electrode plate 107. The upper electrode plate 107 is electrically connected to the reference ground potential 128, as indicated by the electrical connection 129. The purge gas supply channel 115 extends from the purge gas supply 117 through the upper electrode assembly 306 to
20 provide for supply of the purge gas at the central location below the dielectric upper plate 105A.

[0069] The system 300 also includes a lower electrode assembly 304 that includes a lower showerhead electrode plate 301 having an interior region 303 for transforming a process gas into a plasma 302. The lower showerhead electrode plate 301 includes a number of vents 305
25 extending from an upper surface of the lower showerhead plate 301 to the interior region 303.

The lower showerhead electrode plate 301 is supported by the inner base plate 135, which is supported by the outer base plate 136. The lower showerhead electrode plate 301 is electrically connected to receive RF power from the RF power supply 123 by way of the matching circuitry 125 and electrical connection 127. The outer base plate 136 is formed of
5 an electrically conductive material and is electrically connected to the reference ground potential 137. The inner base plate 135 is formed of a dielectric material so as to electrically isolate the RF powered lower showerhead electrode plate 301 from the grounded outer base plate 136. It should be appreciated that the lower showerhead electrode plate 301 serves as both a process gas distribution plate and an RF transmission electrode.

10 [0070] A process gas supply channel 307 is formed through the lower electrode assembly 304 to supply a process gas from a process gas supply 311 to the interior region 303 of the lower showerhead electrode plate 301, as indicated by arrow 309. The RF power supplied to the lower showerhead electrode plate 301 serves to transform the process gas into the plasma 302 within the interior region 303 of the lower showerhead electrode plate 301.

15 [0071] In view of the foregoing, the dielectric upper plate 105A represents a first upper plate positioned parallel to and spaced apart from the lower showerhead electrode plate 301, where the first upper plate is formed of a dielectric material. And, the upper electrode plate 107 represents a second upper plate positioned next to the first upper plate such that the first upper plate is located between the lower showerhead electrode plate 301 and the second
20 upper plate, where the second upper plate electrically connected to the reference ground potential 128.

[0072] The system 300 can also include a set of lifting pins 111A for handling of the workpiece 109 during placement of the workpiece 109 within the chamber 101 and removal of the workpiece 109 from the chamber 101. However, unlike the set of dielectric lifting pins
25 111 in the system 100, the set of lifting pins 111A in the system 300 are not used as the

dielectric support to support the workpiece 109 during plasma processing operations within the chamber 101. Instead, like the system 200, the system 300 includes the dielectric edge ring 201 to serve as the dielectric support for the workpiece 109.

[0073] As discussed above, the dielectric edge ring 201 is formed of a dielectric material and
5 has an annular shape with an upper surface defined to contact and support a peripheral region of the bottom surface of the workpiece 109, and support the workpiece 109 in an electrically isolated manner within a region 340 between the upper surface of the lower showerhead electrode plate 301 and a lower surface of the dielectric upper plate 105A, i.e., of the first upper plate. Also, as previously discussed, the dielectric edge ring 201 includes vents 205
10 defined to allow for flow of process gases and plasma process by-product materials from the region below the workpiece 109. It should be understood that the dielectric edge ring 201 is formed of a dielectric material, has a top surface defined to support the workpiece 109 at the radial periphery of the bottom surface of the workpiece 109, and includes through-holes, vents, or other types of passages such that dielectric edge ring 201 serves as baffle for process
15 gases and plasma process by-product materials exiting from the region beneath the workpiece 109.

[0074] In the system 300, the dielectric edge ring 201 is defined to extend in a controllable manner into the region 340 between the lower showerhead electrode plate 301 and the dielectric upper plate 105A so as to control the distance 112 between the top surface of the
20 workpiece 109 and the dielectric upper plate 105A when the workpiece 109 is present on the dielectric edge ring 201. The dielectric edge ring 201 is defined to position the workpiece 109 proximate to and substantially parallel to the dielectric upper plate 105A (the first upper plate) such that the purge gas is made to flow from the purge gas supply channel 115 over a top surface of the workpiece 109 through the gap 113 between the lower surface of the
25 dielectric upper plate 105A (first upper plate) and the top surface of the workpiece 109, so as

to prevent reactive constituents of the plasma 302 from reaching the top surface of the workpiece 109, when the workpiece 109 is present on the dielectric edge ring 201.

[0075] Extension of the dielectric edge ring 201 into the region 340 between the lower showerhead electrode plate 301 and the dielectric upper plate 105A also forms a plasma generation volume beneath the workpiece 109 and above the lower showerhead electrode plate 301, such that the bottom surface of the workpiece 109 can be exposed to the plasma 302 generated with the plasma generation volume. Thus, the dielectric edge ring 201 also functions to confine the plasma 302 to the plasma generation volume beneath the workpiece 109. It should be understood that in some embodiments, the position of the dielectric edge ring 201 relative to the lower showerhead electrode plate 301 is adjustable, thereby providing for adjustment of the size of the plasma processing volume between the workpiece 109 and the lower showerhead electrode plate 301.

[0076] During operation of the system 300 to perform plasma processing operations, the purge gas is supplied from the purge gas supply 117 through the purge gas supply channel 115 to flow over the top surface of the workpiece 109 and thereby prevent reactive constituents of the plasma 302 from reaching the top surface of the workpiece 109. Also, the process gas is supplied from the process gas supply 311 through the process gas supply channel 307 to the interior region 303 of the lower showerhead electrode plate 301, while RF power is supplied to the lower showerhead electrode plate 301 from the RF power supply 123 by way of the matching circuitry 125 and electrical connection 127. The RF power transforms the process gas within the interior region 303 of the lower showerhead electrode plate 301 into the plasma 302, whereby reactive constituents of the plasma 302 interact with the bottom surface of the workpiece 109 to remove undesirable materials from the workpiece 109. The exhaust 131 is operated to evacuate both purge gases and process gases from within the chamber 101, and to evacuate the process gases and plasma processing by-product

materials from the plasma generation volume beneath the workpiece 109, through the vents 205 of the dielectric edge ring 201 to the exhaust port 133, as indicated by arrows 139.

[0077] It should be understood that any portion of the various components of the system 300 that are exposed to reactive constituents of the plasma 302 can be protected as necessary through use of plasma erosion resistant materials and/or through use of protective coatings, such as Y₂O₃ or other ceramic coatings. Also, in some embodiments, structures such as the lower showerhead electrode plate 301 may be covered by a thin quartz plate.

[0078] During plasma processing operations using the system 300, the etch rate of material from the bottom surface of the workpiece 109 is a partial function of the RF power applied to the process gas and the pressure of the process gas within the chamber 101. More specifically, a higher RF power yields a higher etch rate of material from the bottom surface of the workpiece 109, vice-versa. And, a lower pressure of the process gas within the chamber 101 yields a higher etch rate of material from the bottom surface of the workpiece 109, vice-versa. Additionally, uniformity of the material etch rate across the bottom surface of the workpiece 109 is improved at lower process gas pressure within the chamber 101.

[0079] In various embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 100 W to about 10 kW. In some embodiments, the RF power is supplied by the RF power supply 123 within a range extending from about 1 kW to about 3 kW. In some embodiments, the RF power is supplied by the RF power supply 123 within a frequency range extending from about 2 MHz to about 60 MHz. In some embodiments, direct current (DC) power can also be applied to the lower electrode plate 103. Additionally, in some embodiments, multiple frequencies of RF power can be supplied to the lower electrode plate 103 at either the same time or at different times, such as in a cyclical manner.

[0080] In some embodiments, the pressure of the process gas within the chamber is controlled within a range extending from about 50 mT to about 10 T. In some embodiments,

the pressure of the process gas within the chamber is controlled within a range extending up to about 2 T. In some embodiments, the process gas is supplied to the plasma 102 generation volume at a flow rate within a range extending from about 0.1 slm to about 5 slm. In some embodiments, the process gas is supplied to the plasma 102 generation volume at a flow rate
5 within a range extending from about 1 slm to about 5 slm.

[0081] Figure 3B shows a variation of the semiconductor processing system 300 of Figure 3A defined to use a remote plasma source 184, in accordance with one embodiment of the present invention. The remote plasma source 184 is defined to generate reactive constituents of the plasma 302 external to the chamber 101, and flow the reactive constituents of the
10 plasma 302 through a conduit 180 to the interior region 303 of the lower showerhead electrode plate 301, as indicated by arrow 182, and ultimately to the region beneath the workpiece 109.

[0082] The process gas, purge gas, and plasma 302 reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated
15 by arrows 139. In various embodiments, the remote plasma source 184 is defined to generate reactive constituents of the plasma 302 using RF power, microwave power, or a combination thereof. Also, in various embodiments, the remote plasma source 184 is defined as either a capacitively coupled plasma source or an inductively coupled plasma source.

[0083] In various embodiments, RF power within a range extending from about 1 kW to
20 about 10 kW is used to generate the plasma 302 in the remote plasma source 184. In some embodiments, RF power within a range extending from about 5 kW to about 8 kW is used to generate the plasma 302 in the remote plasma source 184. In some embodiments, RF power within a frequency range extending from about 2 MHz to about 60 MHz is used to generate the plasma 302 in the remote plasma source 184. In some embodiments, direct current (DC)
25 power can also be applied to the lower showerhead electrode plate 301. Additionally, in some

embodiments, multiple frequencies of RF power can be used to generate the plasma 302 within the remote plasma source 184 at either the same time or at different times, such as in a cyclical manner.

[0084] In some embodiments, the pressure of a process gas within the remote plasma source 184 is controlled within a range extending from about 0.1 T to about 10 T. In some
5 embodiments, the pressure of the process gas within the remote plasma source 184 is controlled within a range extending from about 1 T to about 10 T. In some embodiments, the process gas is supplied to the remote plasma source 184 at a flow rate within a range extending from about 0.1 slm to about 5 slm. In some embodiments, the process gas is
10 supplied to the remote plasma source 184 at a flow rate within a range extending from about 1 slm to about 5 slm.

[0085] Figure 3C shows the semiconductor processing system 300 in a configuration in which the workpiece 109 is lowered to rest on the lower electrode assembly 304 in order to perform plasma processing of the peripheral edge of the workpiece 109, in accordance with
15 one embodiment of the present invention. In this embodiment, the purge gas is flowed through the purge gas supply channel 115 and the process gas is flowed through the process gas supply channel 119. The dielectric edge ring 201 is fully retracted such that the workpiece 109 rests on the lower electrode assembly 304 at a position proximate to and substantially parallel to the dielectric upper plate 105A, such that the purge gas is made to
20 flow from the purge gas supply channel 115 over a top surface of the workpiece 109 between the dielectric upper plate 105 and the top surface of the workpiece 109, so as to prevent the process gas from flowing over the top surface of the workpiece 109 and so as to cause the process gas to flow around the peripheral edge of the workpiece 109.

[0086] The purge gas outflow at the periphery of the dielectric upper plate 105 prevents the
25 process gas and any reactive constituents of the plasma 302A from entering the region over

the top surface of the workpiece 109. The process gas flows around the peripheral edge of the workpiece 109 and is transformed into the plasma 302A by the RF power transmitted through the electrical connection 127 to the lower showerhead electrode plate 301. The plasma 302A is exposed to the peripheral edge of the workpiece 109, so as to react with and remove
5 unwanted materials from those regions of the workpiece 109. The process gas, purge gas, and plasma 302A reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated by arrows 139.

[0087] Figure 4 shows a semiconductor processing system 400 that is a variation of the system 300 described with regard to Figure 3A, in accordance with one embodiment of the
10 present invention. Specifically, the system 400 of Figure 4 is the same as the system 300 of Figure 3A, with the exception that the dielectric upper plate 105A is replaced by a conductive upper plate 105B formed of an electrically conductive material. All other features of the system 400 of Figure 4 are the same as discussed above with regard to the system 300 of Figure 3A. The conductive upper plate 105B is electrically connected to the reference ground
15 potential 128. Therefore, in the system 400, the workpiece 109 is capacitively coupled to the reference ground potential by way of its close proximity to the conductive upper plate 105B.

[0088] Figures 5A and 5B show a semiconductor processing system 500 that is also a variation of the system 300 described with regard to Figure 3A, in accordance with one embodiment of the present invention. Specifically, the system 500 of Figures 5A and 5B is
20 the same as the system 300 of Figure 3A, with the exceptions that the upper electrode assembly 306 is replaced by a configurable upper electrode assembly 510, and that an upper process gas supply 501 is provided. Other features of the system 500 of Figures 5A and 5B are the same as discussed above with regard to the system 300 of Figure 3A.

[0089] In the system 500, the configurable upper electrode assembly 510 includes an
25 electrically conductive interior electrode plate 505, a dielectric member 503, and the upper

electrode plate 107. The dielectric member 503 serves to electrically isolate the electrically conductive interior electrode plate 505 from the upper electrode plate 107. The upper electrode plate 107 is electrically connected to the reference ground potential 128 by way of the electrical connection 129. The electrically conductive interior electrode plate 505 is electrically connected to a switch 509 by way of an electrical connection 507, and the switch 509 is in turn electrically connected to a reference ground potential 512. In this manner, the switch 509 provides for control of electrical connection of the electrically conductive interior electrode plate 505 to the reference ground potential 512.

[0090] Also, the system 500 includes the process gas supply channel 119 formed through the configurable upper electrode assembly 510, similar to the process gas supply channel 119 formed through the upper electrode assembly 108 as discussed with regard to the system 100 of Figure 1A. The process gas supply channel 119 is fluidly connected to an upper process gas supply 501 containing a process gas. The process gas is defined to transform into the plasma 302 when exposed to the RF power. The process gas supply channel 119 is formed to supply the process gas to locations near a periphery of the workpiece 109 when present on the dielectric edge ring 201. A valve 502 is provided to control the flow of process gas through the process gas supply channel 119, such that the flow of process gas from the upper process gas supply 501 can be turned off when performing the backside plasma cleaning of the workpiece 109 and turned on when performing the bevel edge plasma cleaning of the workpiece 109.

[0091] Figure 5A shows the system 500 in a configuration for performing the backside plasma cleaning of the workpiece 109. In this configuration, the dielectric edge ring 201 is raised to create the plasma processing volume beneath the workpiece 109, and the process gas is supplied from the lower process gas supply 311 to the interior region 303 of the lower showerhead electrode plate 301 to generate the plasma 302 beneath the workpiece 109. Also,

in this configuration, the valve 502 is closed so as to turn off the flow of process gas from the upper process gas supply 501. In this configuration, the purge gas is supplied from the purge gas supply 117 to the gap 113 between the configurable upper electrode assembly 510 and the workpiece 109, so as to prevent reactive constituents of the plasma 302 from reaching the top surface of the workpiece 109. Also, in this configuration, the switch 509 is set to electrically connect the electrically conductive interior electrode plate 505 to the reference ground potential 512. In this manner, the workpiece 109 is capacitively coupled to the reference ground potential 512 through the electrically conductive interior electrode plate 505. Otherwise, the backside plasma cleaning of the workpiece 109 using the system 500 is substantially the same as that described with regard to the system 300 of Figure 3A.

[0092] Figure 5B shows the system 500 in a configuration for performing the bevel edge plasma cleaning of the workpiece 109. In this configuration, the dielectric edge ring 201 is fully lowered such that the workpiece rests directly on the lower showerhead electrode plate 301. Also, in this configuration, the lower electrode assembly 304 and the configurable upper electrode assembly 510 are moved toward each other such that the top surface of the workpiece 109 is in close proximity to the configurable upper electrode assembly 510 so as to form the gap 113. In this configuration, the valve 502 is open so as to turn on the flow of process gas from the upper process gas supply 501 to the peripheral region of the workpiece 109. Also, in this configuration, the purge gas is supplied from the purge gas supply 117 to the gap 113 between the configurable upper electrode assembly 510 and the workpiece 109, so as to prevent reactive constituents of a plasma 513 from reaching the top surface of the workpiece 109.

[0093] Also, in the configuration of Figure 5B, RF power is supplied from the RF power supply 123 to the lower showerhead electrode plate 301. The RF power propagates through transmission paths that extend from the lower showerhead electrode plate 301 to both the

grounded outer base plate 137 and grounded upper electrode plate 107, thereby transforming the process gas supplied to the peripheral region of the workpiece 109 into the plasma 513. As this occurs, the purge gas flows radially outward through the gap 113 from the centrally located dispense location of the purge gas supply channel 115 toward the periphery of the workpiece 109, thereby preventing reactive constituents of the plasma 513 from entering the gap 113 and interacting with the top surface of the workpiece 109. Also, it should be understood that in the configuration of Figure 5B, process gas is not supplied from the lower process gas supply 311 to the interior region 303 of the lower showerhead electrode plate 301.

10 [0094] Also, in the configuration of Figure 5B, the switch 509 is set to electrically disconnect the electrically conductive interior electrode plate 505 from the reference ground potential 512, thereby causing the electrically conductive interior electrode plate 505 to have a floating electrical potential. In this manner, the workpiece 109 is not capacitively coupled to the reference ground potential 512, so as to prevent arcing or other undesirable phenomena

15 within the gap 113, due to the closer proximity of the RF powered lower showerhead electrode plate 301 to the configurable upper electrode assembly 510. Also, in the configuration of Figure 5B, the exhaust 131 is operated to draw process gases, purge gases, and plasma processing by-product materials away from the peripheral region of the workpiece 109 where the plasma 513 is generated to the exhaust port 133, as indicated by

20 arrows 139.

[0095] Figure 5C shows a variation of the semiconductor processing system 500 of Figure 5A defined to use a remote plasma source 184, in accordance with one embodiment of the present invention. The remote plasma source 184 is defined to generate reactive constituents of the plasma 302 external to the chamber 101, and flow the reactive constituents of the plasma 302 through a conduit 180 to the interior region 303 of the lower showerhead

25

electrode plate 301, as indicated by arrow 182, and ultimately to the region beneath the workpiece 109.

[0096] The process gas, purge gas, and plasma 302 reaction by-product materials are evacuated from the chamber 101 through the port 133 by way of the exhaust 131, as indicated
5 by arrows 139. In various embodiments, the remote plasma source 184 is defined to generate reactive constituents of the plasma 302 using RF power, microwave power, or a combination thereof. Also, in various embodiments, the remote plasma source 184 is defined as either a capacitively coupled plasma source or an inductively coupled plasma source.

[0097] In various embodiments, RF power within a range extending from about 1 kW to
10 about 10 kW is used to generate the plasma 302 in the remote plasma source 184. In some embodiments, RF power within a range extending from about 5 kW to about 8 kW is used to generate the plasma 302 in the remote plasma source 184. In some embodiments, RF power within a frequency range extending from about 2 MHz to about 60 MHz is used to generate the plasma 302 in the remote plasma source 184. In some embodiments, direct current (DC)
15 power can also be applied to the lower showerhead electrode plate 301. Additionally, in some embodiments, multiple frequencies of RF power can be used to generate the plasma 302 within the remote plasma source 184 at either the same time or at different times, such as in a cyclical manner.

[0098] In some embodiments, the pressure of a process gas within the remote plasma source
20 184 is controlled within a range extending from about 0.1 T to about 10 T. In some embodiments, the pressure of the process gas within the remote plasma source 184 is controlled within a range extending from about 1 T to about 10 T. In some embodiments, the process gas is supplied to the remote plasma source 184 at a flow rate within a range extending from about 0.1 slm to about 5 slm. In some embodiments, the process gas is
25 supplied to the remote plasma source 184 at a flow rate within a range extending from about

1 slm to about 5 slm.

[0099] Figure 6 shows a flowchart of a method for plasma cleaning a bottom surface of a workpiece, in accordance with one embodiment of the present invention. The method includes an operation 601 for positioning the bottom surface of the workpiece on a dielectric support defined to support the workpiece in an electrically isolated manner within a region between an upper surface of a lower electrode plate and a lower surface of a dielectric upper plate, with an upper electrode plate positioned next to an upper surface of the dielectric upper plate. The lower electrode plate is connected to receive radiofrequency power. The upper electrode plate is electrically connected to a reference ground potential. The method also includes an operation 603 for positioning the dielectric support such that a top surface of the workpiece is separated from the lower surface of the dielectric upper plate by a narrow gap, and such that an open region exists between the bottom surface of the workpiece and the upper surface of the lower electrode plate.

[00100] The method also includes an operation 605 for flowing a purge gas to a central location within the narrow gap between the top surface of the workpiece and the lower surface of the dielectric upper plate such that the purge gas flows through the narrow gap in a direction away from the central location toward a periphery of the workpiece. The method also includes an operation 607 for flowing a process gas to a peripheral region of the workpiece located outside the narrow gap, whereby the process gas flows into the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate. It should be understood that flow of the purge gas through the narrow gap in the direction away from the central location toward the periphery of the workpiece prevents the process gas from flowing into the narrow gap and over the top surface of the workpiece.

[00101] The method also includes an operation 609 for supplying radiofrequency power to the lower electrode plate so as to transform the process gas into a plasma around the

peripheral region of the workpiece, and within the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate. The method can also include an operation for exhausting gases from the region above the upper surface of the lower electrode plate, so as to move plasma etching by-product materials away from the workpiece.

5 **[00102]** In one embodiment of the method, the dielectric support is defined as a set of dielectric lifting pins that extend through the lower electrode plate to support the workpiece in an electrically isolated manner within the region between the upper surface of the lower electrode plate and the lower surface of the dielectric upper plate. In this embodiment, positioning the dielectric support such that the top surface of the workpiece is separated from
10 the lower surface of the dielectric upper plate by the narrow gap in operation 603 is performed by moving the set of dielectric lifting pins toward the lower surface of the dielectric upper plate.

[00103] In another embodiment of the method, the dielectric support is defined as a dielectric edge ring having an annular shape with an upper surface defined to contact and
15 support a peripheral region of the bottom surface of the workpiece. The dielectric edge ring includes vents defined to allow for flow of the process gas into the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate and to allow for exhausting gases from the region above the upper surface of the lower electrode plate.

[00104] Figure 7 shows a flowchart of a method for plasma cleaning a bottom surface
20 of a workpiece, in accordance with one embodiment of the present invention. The method includes an operation 701 for positioning the workpiece on a dielectric edge ring having an annular shape with an upper surface defined to contact and support a peripheral region of the bottom surface of the workpiece. The dielectric edge ring is defined to support the workpiece in an electrically isolated manner within a region between an upper surface of a lower
25 showerhead electrode plate and a lower surface of a first upper plate. A second upper plate is

positioned next to an upper surface of the first upper plate. The lower showerhead electrode plate is connected to receive radiofrequency power. The second upper plate electrically connected to a reference ground potential.

[00105] The method also includes an operation 703 for positioning the dielectric edge ring such that a top surface of the workpiece is separated from the lower surface of the first upper plate by a narrow gap, and such that an open region exists between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate. The method also includes an operation 705 for flowing a purge gas to a central location within the narrow gap, such that the purge gas flows through the narrow gap in a direction away from the central location toward a periphery of the workpiece. The method also includes an operation 707 for flowing a process gas to an interior region of the lower showerhead electrode plate.

[00106] The method also includes an operation 709 for supplying radiofrequency power to the lower showerhead electrode plate so as to transform the process gas into a plasma within the interior region of the lower showerhead electrode plate, whereby reactive constituents of the plasma flow through vents from the interior region of the lower showerhead electrode plate into the open region between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate. The method can also include an operation for exhausting gases from the open region between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate through vents defined in the dielectric edge ring.

[00107] Figure 8 shows a flowchart of a method for performing both a bevel edge plasma cleaning process and backside cleaning process on a workpiece within a common, i.e., single, plasma processing system, in accordance with one embodiment of the present

invention. The method includes an operation 801 in which a bevel edge plasma cleaning process is performed on the workpiece with the bottom of the workpiece positioned directly on an RF powered lower electrode and with a narrow gap of purge gas flow provided over a top surface of the workpiece. In operation 801, an upper structural member is provided above
5 the workpiece to form the narrow gap of purge gas flow over the top surface of the workpiece. In one example embodiment, the bevel edge plasma cleaning process of operation 801 is performed using a capacitively coupled plasma generated by RF power at 13.56 MHz. However, it should be understood that in other embodiments, the bevel edge plasma cleaning process can be performed using RF power at other frequencies, powers, and duty cycles, and
10 with any suitable process gas.

[00108] After the bevel edge plasma cleaning process is completed in operation 801, an operation 803 is performed in which the workpiece is raised above the lower electrode to form a plasma processing volume below the bottom surface of the workpiece. Also, in operation 803, the narrow gap for purge gas flow is maintained over top surface of
15 workpiece. In one embodiment, the workpiece is raised above the lower electrode using dielectric lifting pins, such as described with regard to Figure 1A. In another embodiment, the workpiece is raised above the lower electrode using a vented dielectric edge ring, such as described with regard to Figure 2A.

[00109] The method continues with an operation 805 for supplying reactive
20 constituents of a plasma to the plasma processing volume below the bottom surface of workpiece to effect plasma cleaning of bottom surface of workpiece. In one embodiment, operation 805 includes generating reactive constituents of the plasma using a remotely generated plasma, and delivering the reactive constituents of the plasma to the plasma processing volume below the bottom surface of workpiece. In another embodiment, a process
25 gas is flowed to the plasma processing volume below the bottom surface of workpiece, and

RF power is applied to transform the process gas into a plasma within the plasma processing volume below the bottom surface of workpiece. In either embodiment, the reactive constituents of the plasma present within the plasma processing volume below the bottom surface of workpiece are allowed to interact with and remove a target film or material from the bottom surface of the workpiece. Also, during operation 805, a flow of purge gas is maintained over the top surface of the workpiece to prevent reactive constituents of the plasma or any other by-product materials from contacting and interacting with the top surface of the workpiece.

[00110] It should be appreciated that the various semiconductor processing systems disclosed herein provide for performance of both bevel edge plasma cleaning processes and backside plasma cleaning processes in a single tool, i.e., single chamber. Also, it should be appreciated that the backside plasma cleaning processes discussed herein are especially useful in removing carbon, photoresist, and other carbon-related polymers from the bottom surface of the workpiece, as these materials are difficult to remove in alternative wet clean processes. Additionally, it should be appreciated that the backside plasma cleaning processes discussed herein can provide for higher cleaning throughput than the alternative wet clean processes, because of the higher etch rates achievable with the plasma in the backside plasma cleaning processes.

[00111] While this invention has been described in terms of several embodiments, it will be appreciated that those skilled in the art upon reading the preceding specifications and studying the drawings will realize various alterations, additions, permutations and equivalents thereof. Therefore, it is intended that the present invention includes all such alterations, additions, permutations, and equivalents as fall within the true spirit and scope of the invention.

CLAUSES

1. A semiconductor processing system, comprising:
 - a lower electrode plate;
 - a radiofrequency power supply connected to supply radiofrequency power to the
 - 5 lower electrode plate;
 - a dielectric upper plate positioned parallel to and spaced apart from the lower electrode plate;
 - an upper electrode plate positioned next to the dielectric upper plate such that the dielectric upper plate is located between the lower electrode plate and the upper electrode
 - 10 plate, the upper electrode plate electrically connected to a reference ground potential;
 - a dielectric support defined to support a workpiece in an electrically isolated manner within a region between the lower electrode plate and the dielectric upper plate;
 - a purge gas supply channel formed to supply a purge gas to the region between the lower electrode plate and the dielectric upper plate at a central location of the dielectric upper
 - 15 plate; and
 - a process gas supply channel formed to supply a process gas to the region between the lower electrode plate and the dielectric upper plate at a periphery of the dielectric upper plate,
 - wherein the dielectric support is defined to position the workpiece at a position proximate to and substantially parallel to the dielectric upper plate such that the purge gas is
 - 20 made to flow from the purge gas supply channel over a top surface of the workpiece between the dielectric upper plate and the top surface of the workpiece so as to prevent the process gas from flowing over the top surface of the workpiece and so as to cause the process gas to flow around a peripheral edge of the workpiece and below the workpiece into a region between the lower electrode plate and a bottom surface of the workpiece, when the workpiece is present
 - 25 on the dielectric support.

2. A semiconductor processing system as recited in claim 1, wherein the purge gas supply channel is formed through both the upper electrode plate and the dielectric upper plate, and wherein the purge gas supply channel is fluidly connected to a purge gas supply
5 containing an inert gas.

3. A semiconductor processing system as recited in claim 1, wherein the process gas supply channel is formed through the upper electrode plate and includes an open region between the upper electrode plate and dielectric upper plate, and wherein the process gas
10 supply channel is fluidly connected to a process gas supply containing the process gas, the process gas defined to transform into a plasma when exposed to the radiofrequency power.

4. A semiconductor processing system as recited in claim 1, wherein the dielectric support is defined as a set of dielectric lifting pins that extend through the lower
15 electrode plate to support the workpiece in an electrically isolated manner within the region between the lower electrode plate and the dielectric upper plate.

5. A semiconductor processing system as recited in claim 4, wherein the set of dielectric lifting pins are defined to extend in a controllable manner into the region between
20 the lower electrode plate and the dielectric upper plate so as to control a distance between the top surface of the workpiece and the dielectric upper plate when the workpiece is present on the set of dielectric lifting pins.

6. A semiconductor processing system as recited in claim 1, wherein the
25 dielectric support is defined as a dielectric edge ring having an annular shape with an upper

surface defined to contact and support a peripheral region of the bottom surface of the workpiece.

7. A semiconductor processing system as recited in claim 6, wherein the
5 dielectric edge ring includes vents defined to allow for flow of the process gas from an output of the process gas supply channel to the region between the lower electrode plate and the bottom surface of the workpiece, when the workpiece is present on the dielectric edge ring.

8. A semiconductor processing system as recited in claim 7, wherein the
10 dielectric edge ring is defined to extend in a controllable manner into the region between the lower electrode plate and the dielectric upper plate so as to control a distance between the top surface of the workpiece and the dielectric upper plate when the workpiece is present on the dielectric edge ring.

15 9. A method for plasma cleaning a peripheral region and a bottom surface of a workpiece, comprising:

positioning the bottom surface of the workpiece on a dielectric support defined to support the workpiece in an electrically isolated manner within a region between an upper surface of a lower electrode plate and a lower surface of a dielectric upper plate, with an
20 upper electrode plate positioned next to an upper surface of the dielectric upper plate, the lower electrode plate connected to receive radiofrequency power, the upper electrode plate electrically connected to a reference ground potential;

positioning the dielectric support such that a top surface of the workpiece is separated from the lower surface of the dielectric upper plate by a narrow gap, and such that an open

region exists between the bottom surface of the workpiece and the upper surface of the lower electrode plate;

flowing a purge gas to a central location within the narrow gap between the top surface of the workpiece and the lower surface of the dielectric upper plate such that the
5 purge gas flows through the narrow gap in a direction away from the central location toward a periphery of the workpiece;

flowing a process gas to a peripheral region of the workpiece located outside the narrow gap, whereby the process gas flows into the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate; and

10 supplying radiofrequency power to the lower electrode plate so as to transform the process gas into a plasma around the peripheral region of the workpiece and within the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate.

15 10. A method for plasma cleaning a peripheral region and a bottom surface of a workpiece as recited in claim 9, wherein flow of the purge gas through the narrow gap in the direction away from the central location toward the periphery of the workpiece prevents the process gas from flowing into the narrow gap and over the top surface of the workpiece.

20 11. A method for plasma cleaning a peripheral region and a bottom surface of a workpiece as recited in claim 10, further comprising:

exhausting gases from the region above the upper surface of the lower electrode plate so as to move plasma etching by-product materials away from the workpiece.

12. A method for plasma cleaning a peripheral region and a bottom surface of a workpiece as recited in claim 11, wherein the dielectric support is defined as a set of dielectric lifting pins that extend through the lower electrode plate to support the workpiece in an electrically isolated manner within the region between the upper surface of the lower electrode plate and the lower surface of the dielectric upper plate.

13. A method for plasma cleaning a peripheral region and a bottom surface of a workpiece as recited in claim 12, wherein positioning the dielectric support such that the top surface of the workpiece is separated from the lower surface of the dielectric upper plate by the narrow gap is performed by moving the set of dielectric lifting pins toward the lower surface of the dielectric upper plate.

14. A method for plasma cleaning a peripheral region and a bottom surface of a workpiece as recited in claim 11, wherein the dielectric support is defined as a dielectric edge ring having an annular shape with an upper surface defined to contact and support a peripheral region of the bottom surface of the workpiece, wherein the dielectric edge ring includes vents defined to allow for flow of the process gas into the region between the bottom surface of the workpiece and the upper surface of the lower electrode plate and to allow for exhausting gases from the region above the upper surface of the lower electrode plate.

20

15. A semiconductor processing system, comprising:
a lower showerhead electrode plate having an interior region for transforming a process gas into a plasma, the lower showerhead electrode plate having a number of vents extending from an upper surface of the lower showerhead plate to the interior region;

a process gas supply channel formed to supply the process gas to the interior region of the lower showerhead electrode plate;

a radiofrequency power supply connected to supply radiofrequency power to the lower showerhead electrode plate so as to transform the process gas into the plasma within
5 the interior region of the lower showerhead electrode plate;

a first upper plate positioned parallel to and spaced apart from the lower showerhead electrode plate;

a second upper plate positioned next to the first upper plate such that the first upper plate is located between the lower showerhead electrode plate and the second upper plate, the
10 second upper plate electrically connected to a reference ground potential;

a dielectric edge ring having an annular shape with an upper surface defined to contact and support a peripheral region of a bottom surface of a workpiece and support the workpiece in an electrically isolated manner within a region between the upper surface of the lower showerhead electrode plate and a lower surface of the first upper plate; and

15 a purge gas supply channel formed to supply a purge gas to the region between the upper surface of the lower showerhead electrode plate and the lower surface of the first upper plate at a central location of the first upper plate,

wherein the dielectric edge ring is defined to position the workpiece proximate to and substantially parallel to the first upper plate such that the purge gas is made to flow from the
20 purge gas supply channel over a top surface of the workpiece between the lower surface of the first upper plate and the top surface of the workpiece so as to prevent reactive constituents of the plasma from reaching the top surface of the workpiece, when the workpiece is present on the dielectric edge ring.

16. A semiconductor processing system as recited in claim 15, wherein the first upper plate is formed of a dielectric material.

17. A semiconductor processing system as recited in claim 15, wherein the first
5 upper plate is formed of an electrically conductive material.

18. A semiconductor processing system as recited in claim 15, wherein the dielectric edge ring includes vents defined to allow for exhausting gases from a region below a bottom surface of the workpiece and above the upper surface of the lower showerhead
10 plate.

19. A method for plasma cleaning a bottom surface of a workpiece, comprising:
positioning the workpiece on a dielectric edge ring having an annular shape with an upper surface defined to contact and support a peripheral region of the bottom surface of the workpiece, the dielectric edge ring defined to support the workpiece in an electrically isolated
15 manner within a region between an upper surface of a lower showerhead electrode plate and a lower surface of a first upper plate, with a second upper plate positioned next to an upper surface of the first upper plate, the lower showerhead electrode plate connected to receive radiofrequency power, the second upper plate electrically connected to a reference ground
20 potential;

positioning the dielectric edge ring such that a top surface of the workpiece is separated from the lower surface of the first upper plate by a narrow gap, and such that an open region exists between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate;

flowing a purge gas to a central location within the narrow gap such that the purge gas flows through the narrow gap in a direction away from the central location toward a periphery of the workpiece;

flowing a process gas to an interior region of the lower showerhead electrode plate;

5 and

supplying radiofrequency power to the lower showerhead electrode plate so as to transform the process gas into a plasma within the interior region of the lower showerhead electrode plate, whereby reactive constituents of the plasma flow through vents from the interior region of the lower showerhead electrode plate into the open region between the
10 bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate.

20. A method for plasma cleaning a bottom surface of a workpiece as recited in claim 19, further comprising:

15 exhausting gases from the open region between the bottom surface of the workpiece located inside the dielectric edge ring and the upper surface of the lower showerhead electrode plate through vents defined in the dielectric edge ring.

20

CLAIMS

1. A semiconductor processing system, comprising:
a processing chamber including-
a lower electrode plate,
an upper plate disposed above and substantially parallel to the lower electrode plate, the upper plate having a gas supply channel formed to extend through a bottom surface of the upper plate, and
a dielectric edge ring having an upper surface defined to contact and support a peripheral region of a bottom surface of a substrate, the dielectric edge ring formed to circumscribe the lower electrode plate and extend in a controllable manner above the lower electrode plate into a region between the lower electrode plate and the upper plate, such that a lower processing region is formed inside the dielectric edge ring between a top surface of the lower electrode plate and a plane corresponding to the upper surface of the dielectric edge ring;
a conduit configured to extend into the chamber to the lower processing region; and
a remote plasma source configured generate reactive constituents of a plasma external to the chamber and flow the reactive constituents of the plasma through the conduit to the lower processing region.
2. The semiconductor processing system as recited in claim 1, wherein the remote plasma source is configured to generate reactive constituents of the plasma using radiofrequency power.
3. The semiconductor processing system as recited in claim 2, wherein the radiofrequency power is within a range extending from about 1 kiloWatt to about 10 kiloWatts.

4. The semiconductor processing system as recited in claim 2, wherein the radiofrequency power is within a range extending from about 5 kiloWatts to about 8 kiloWatts.

5. The semiconductor processing system as recited in claim 2, wherein the radiofrequency power is generated using one or more radiofrequency signals within a range extending from about 2 megaHertz to about 60 megaHertz.

6. The semiconductor processing system as recited in claim 1, wherein the remote plasma source is configured to generate reactive constituents of the plasma using microwave power.

7. The semiconductor processing system as recited in claim 1, wherein the remote plasma source is configured to generate reactive constituents of the plasma using a combination of radiofrequency power and microwave power.

8. The semiconductor processing system as recited in claim 1, wherein the remote plasma source is configured as a capacitively coupled plasma source.

9. The semiconductor processing system as recited in claim 1, wherein the remote plasma source is configured as an inductively coupled plasma source.

10. The semiconductor processing system as recited in claim 1, wherein the remote plasma source is configured to generate reactive constituents of the plasma using a

process gas supplied at a flow rate within a range extending from about 0.1 standard liters per minute to about 5 standard liters per minute, and at a pressure within a range extending from about 0.1 Torr to about 10 Torr.

11. The semiconductor processing system as recited in claim 1, wherein the dielectric edge ring is configured as a stack of annular shaped ring structures separated from each other by spaces that form vents for fluid communication from the lower processing region to an exhaust region.

12. The semiconductor processing system as recited in claim 11, wherein the dielectric edge ring includes a plurality of structural members connected to the stack of annular shaped ring structures, the plurality of structural members located at spaced apart locations about a circumference of the dielectric edge ring.

13. The semiconductor processing system as recited in claim 12, wherein the plurality of structural members are defined to hold the stack of annular shaped ring structures in a fixed spatial configuration.

14. The semiconductor processing system as recited in claim 12, wherein the plurality of structural members are defined to provide for controlled variation of a spatial configuration of the stack of annular shaped ring structures, such that spaces between the annular shaped rings that form the vents are adjustable in size by adjustment of the plurality of structural members.

15. The semiconductor processing system as recited in claim 11, wherein each annular shaped ring structure has a substantially same size and shape.

16. The semiconductor processing system as recited in claim 1, further comprising:

a radiofrequency power supply connected to supply radiofrequency signals to the lower electrode plate.

17. The semiconductor processing system as recited in claim 1, wherein the upper plate includes a dielectric upper plate positioned in exposure to the lower electrode plate.

18. The semiconductor processing system as recited in claim 17, wherein the upper plate includes an upper electrode plate, wherein the dielectric upper plate is positioned between the upper electrode plate and the lower electrode plate.

19. A method for plasma cleaning of a substrate, comprising:

positioning a substrate on a dielectric edge ring within a processing chamber, the dielectric edge ring having an upper surface defined to contact and support a peripheral region of a bottom surface of the substrate, the dielectric edge ring formed to circumscribe a lower electrode plate and extend in a controllable manner above the lower electrode plate into a region between the lower electrode plate and an upper plate, such that a lower processing region is formed inside the dielectric edge ring between a top surface of the lower electrode plate and the bottom surface of the substrate;

generating reactive constituents of a plasma within a remote plasma source external to the chamber; and

flowing the reactive constituents of the plasma through a conduit to the lower processing region.

20. The method for plasma cleaning of the substrate as recited in claim 19, further comprising:

flowing a process gas to a peripheral region of the substrate;

flowing a purge gas through a central location of the upper plate to central location of a top surface of the substrate, the purge gas preventing flow of the process gas toward the central location of the top surface of the substrate; and

supplying radiofrequency power to the lower electrode plate, the radiofrequency power transforming the process gas into a second plasma in exposure to the peripheral region of the substrate.