

ABSTRACTSEMICONDUCTOR MEMORY DEVICE AND METHOD FOR
MANUFACTURING SAME

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A semiconductor memory device includes a first semiconductor layer; a stacked body including a plurality of electrode layers stacked in a first direction; a metal layer provided in the first direction between the first semiconductor layer and the stacked body; a second semiconductor
10 layer extending in the first direction through the stacked body and the metal layer, and being electrically connected to the first semiconductor layer.

(FIG. 2A)